

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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# 2 Line USB 1.1 Upstream EMI Filter

This device is a 2 line EMI filter array for USB port protection in wireless applications. Greater than -20 dB attenuation is obtained at frequencies from 900 MHz to 3.0 GHz. It also has 2 lines for dedicated ESD protection. ESD protection is provided across all capacitors.

### **Features**

- EMI Filtering and ESD Protection
- Integration of 10 Discretes
- Provides Protection for IEC61000-4-2 (Level 4)
  - ◆ 15 kV (Contact)
- Flip-Chip Package
- Moisture Sensitivity Level 1
- ESD Rating: Machine Model = C; Human Body Model = 3B

#### **Benefits**

- Reduces EMI/RFI Emissions on a Data Line
- Integrated Solution Offers Cost and Space Savings
- Reduces Parasitic Inductances Which Offer a More "Ideal" Low Pass Filter Response
- Integrated Solution Improves System Reliability
- This is a Pb-Free Device

### **Applications**

- EMI Filtering and ESD Protection for Data Lines
- Cell Phones
- Handheld Products
- Notebook Computers
- MP3 Players

### **MAXIMUM RATINGS** (T<sub>A</sub> = 25°C unless otherwise noted)

Rating		Symbol	Value	Unit
ESD Discharge IEC61000-4-2	Contact Discharge	V <sub>PP</sub>	15	kV
Operating Temperature Range		T <sub>OP</sub>	-40 to +85	°C
Storage Temperature Range		T <sub>STG</sub>	-55 to +150	°C
Junction Temperature		$T_J$	+125	°C

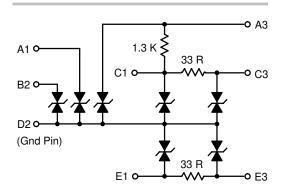
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



### ON Semiconductor®

### http://onsemi.com





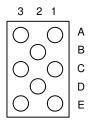
### Flip-Chip FC SUFFIX CASE 499AM



2222 = Specific Device Code A = Assembly Location

Y = Year WW = Work Week

### **PIN CONFIGURATION**



### **ORDERING INFORMATION**

Device	Package	Shipping†
NUF2222FCT1G	Flip-Chip	3000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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Characteristic	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Reverse Working Voltage	$V_{RWM}$	-	-	-	5.6	٧
Breakdown Voltage	$V_{BR}$	I <sub>R</sub> = 1.0 mA	6.0	-	8.8	V
Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 3.3 V	-	1.0	100	nA
Pull Up Resistance	R <sub>pu</sub>	-	1.1	1.3	1.5	kΩ
Series Resistance	R <sub>S</sub>	-	28	33	38	Ω
Capacitance	C <sub>LINE</sub>	f = 1.0 MHz, 0 Vdc	-	36	40	pF
Cut-Off Frequency	f <sub>3dB</sub>	50 $\Omega$ Source and 50 $\Omega$ Load Termination	_	190	-	MHz

### **TYPICAL PERFORMANCE CURVES**

(T<sub>A</sub> = 25°C unless otherwise specified)

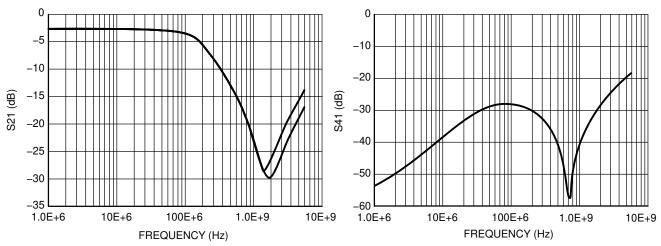


Figure 1. Typical Insertion Loss Characteristics

Figure 2. Typical Crosstalk Characteristics

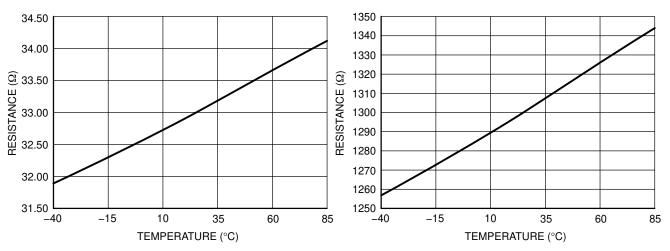


Figure 3. Typical Resistance (R33) vs. Temperature

Figure 4. Typical Resistance (R1300) vs. Temperature

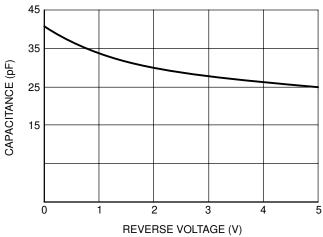


Figure 5. Typical Line Capacitance vs. Reverse Bias Voltage

### PRINTED CIRCUIT BOARD RECOMMENDATIONS

Parameter	500 μm Pitch 300 or 350 μm Solder Ball	
PCB Pad Size	250 μm +25 -0	
Pad Shape	Round	
Pad Type	NSMD	
Solder Mask Opening	350 μm ±25	
Solder Stencil Thickness	125 µm	
Stencil Aperture	250 x 250 μm sq.	
Solder Flux Ratio	50/50	
Solder Paste Type	No Clean Type 3 or Finer	
Trace Finish	OSP Cu	
Trace Width	150 μm Max	

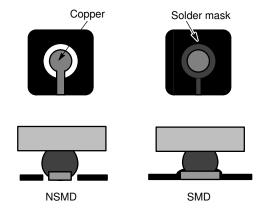


Figure 6. NSMD vs. SMD

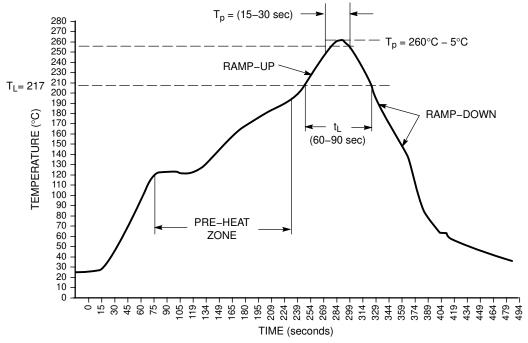
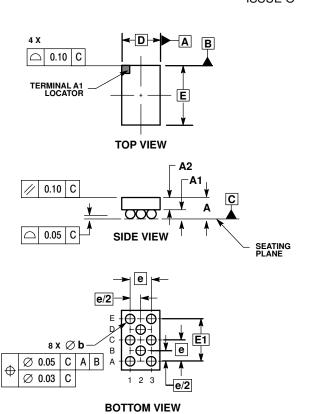


Figure 7. Typical Pb-Free Solder Heating Profile

### **PACKAGE DIMENSIONS**

### **8 PIN FLIP-CHIP** CASE 499AM-01 ISSUE O



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETER.
  3. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

	MILLIMETERS		
DIM	MIN	MAX	
Α		0.700	
A1	0.210	0.270	
A2	0.380	0.430	
D	1.270 BSC		
E	1.970 BSC		
b	0.290	0.340	
е	0.700 BSC		
E1	1.400 BSC		

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