# mail

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# STRATUM 3E High Stability Oven Stabilized Oscillator OH300 Series



2111 Comprehensive Drive Aurora, Illinois 60505 Phone: 630-851-4722 Fax: 630-851-5040 www.conwin.com

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### Description

Connor-Winfield's high stability OH300 OCXO / OCVXCO series are exceptionally precise frequency standard, excellent for use in cellular base stations, test equipment, Synchronous Ethernet, VSAT and Stratum 3E applications

These unique OCXOs and OCVCXOs provide frequency stabilities in the range of  $\pm 5$  ppb to  $\pm 50$  ppb, over the commercial, extended commercial or the industrial temperature range. Power requirements are 1.1W over the commercial temperature range and 1.5W over the industrial temperature range after warm-up. Additionally, excellent aging is achieved through the use of overtone SC cut crystals.

The OH300 series is available with CMOS logic or Sinewave output along with Electronic Frequency Tuning. These oscillators provide outstanding phase noise that varies depending on frequency requirements. Allan Variance specifications are rated for primary reference standards. Warm up times are on the order of 5 minutes to 0.10 ppm of final frequency.



#### **Features**

Frequency Range: 5 to 40 MHz OCXO - Fixed Frequency OCVCXO - Voltage Controlled Frequency 3.3 Vdc or 5.0 Vdc Operation Available Frequency Stabilities: ±5.0 ppb, ±10.0 ppb, ±20 ppb or ±50 ppb Available Temperature Ranges: 0 to 70°C, -20 to 70°C, -20 to 75°C, -40 to 70°C or -40 to 85°C Low Phase Noise / Phase Jitter Output: CMOS Logic or Sinewave SMT Package: 22 mm x 25 mm x 12.7 mm Tape and Reel Packaging RoHS Compliant / Lead Free

## **Ordering Information**



\* ±5.0 ppb stability models are

#### only available from 0 to 70°C.

#### Example Part Number:

OH300-61003CF-038.88M = 22x25x12.7mm SMT package, -40 to 85°C temperature range, ±10.0 ppb frequency stability, 3.3 Vdc supply voltage, CMOS square wave output, fixed output frequency OCXO, 38.88 MHz output frequency.

*OH300-52005CV-010.0M = 22x25x12.7mm SMT package, 0 to 70°C temperature range, ±20.0 ppb frequency stability, 5.0 Vdc supply voltage, CMOS square wave output, voltage controlled output frequency, OCVCX0, 10.0 MHz output frequency.* 

0H300-75005SV-020.0M = 22x25x12.7mm SMT package, -20 to 70°C temperature range, ±50.0 ppb frequency stability, 5.0 Vdc supply voltage, Sinewave output, voltage controlled output frequency, OCVCXO, 20.0 MHz output frequency.





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Absolute Maximum Ratings					
Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	125	°C	
Supply Voltage - 3.3 Vdc (Vcc)	-0.5	-	4.5	Vdc	
Supply Voltage - 5.0 Vdc (Vcc)	-0.5	-	7.0	Vdc	
Control Voltage (Vc)	-0.5	-	7.0	Vdc	
	Operating Sp	opifications			
Paramatar	Minimum	Nominal	Movimum	Linita	Notoo
Conter Frequency (Fo)		Nominal	10 IVIAXIMUM		notes
Operating Temperature Pange:	5	-	40	IVIITZ	
Operating Temperature Range:	0		70	$^{\circ}$ C	
Temperature Code 5	40	-	70	°C	
Temperature Code 6	-40	-	00 70	°C	
Temperature Code 7	-20	-	70	°C	
Temperature Code 0	-40	-	70	°C	
Eroquonov Calibration:	-20	-	0.1		@ 25°C
Frequency Stability vs. Change in Tomporature:	-0.1	-	0.1	ррп	@ 25 C
Stability Code 05	5.0		5.0	nnh	1 2
Stability Code 10	-0.0	-	10.0	ppp	1, Z
Stability Code 10 Stability Code 20	-10.0	-	20.0	ppb	2
Stability Code 20 Stability Code 50	-20.0	-	20.0	ppb	2
Frequency Stability vs Load	-30.0	-	2.0	ppb	<u> </u>
Frequency Stability vs Voltage	-2.0		2.0	ppb	±5%
Aging: Daily:	-2.0		2.0	μρυ	1070
5 MHz to 20 MHz	-10	_	10	nnh/day	З
>20 MHz to 40 MHz	-2.0	-	2.0	ppb/day	3
Aging: First Year:	2.0		2.0	ppb/day	0
5 MHz to 20 MHz	-50	-	50	nnh	
>20 MHz to 40 MHz	-100	-	100	ppb	
Lifetime Tolerance: (20 Years)					
5 MHz to 20 MHz	-300	-	300	daa	4
>20 MHz to 40 MHz	-500	-	500	dqq	4
Supply Voltage: (Vcc) (Note: Power supply turn-on ramp time must be $> 5$ ms to 90% of Vcc.)					
Voltage Code 03	3.13	3.30	3.47	Vdc	5
Voltage Code 05	4.75	5.00	5.25	Vd	5
Power Consumption: Turn-On					
0 to 70°C Models	-	-	3.00	W	6
-20 to 70°C Models	-	-	3.20	W	6
-20 to 75°C Models	-	-	3.30	W	6
-40 to 85°C Models	-	-	3.80	W	6
Power Consumption: Steady State @ 25°C					
0 to 70°C Models	-	-	1.10	W	6
-20 to 70°C Models	-	-	1.15	W	6
-20 to 75°C Models	-	-	1.20	W	6
-40 to 85°C Models	-	-	1.50	W	6
Phase Jitter: (BW: 10 Hz to Fo/2)	-	-	1.0	ps rms	
Short Term Allan Deviation (1s)	-	1.0E-11	-	rms	
Start-Up Time:	-	-	500	ms	
Warm Up Time @ 25°C:	-	-	5	minutes	7

# Phase Noise Characteristics

Typical Phase Noise for 10 MHz OXCO's with CMOS Output					
Parameter	Minimum	Nominal	Maximum	Units	Notes
@1Hz offset	-	-	-85	dBc/Hz	
@ 10Hz offset	-	-	-115	dBc/Hz	
@ 100Hz offset	-	-	-140	dBc/Hz	
@ 1KHz offset	-	-	-145	dBc/Hz	
@ 10KHz offset	-	-	-150	dBc/Hz	
@ 100KHz offset		-	-150	dBc/Hz	

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	OCVCXO Inpu	t Characteris	tics (OCXO T	ype Code V)		
Parameter		Minimum	Nominal	Maximum	Units	Notes
Control Voltage Range: (Vc)						
3.3 Vdc Models		0.30	1.65	3.00	V	8
5.0 Vdc Models		0.50	2.50	4.50	V	8
Frequency Pullability:		±0.4	-	-	ppm	
Input Impedance		100K	-	-	Ohms	
	CMOS Outp	out Character	ristics (Output	t Code C)		
Parameter		Minimum	Nominal	Maximum	Units	Notes
Load		-	15	-	pF	
Output Voltage:					· · · · · · · · · · · · · · · · · · ·	
3.3 Vdc Models High (Voh)		3.0	-	-	V	
Low (Vol)		-	-	0.4		
5.0 Vdc Models High (Voh)		4.7	-	-	V	
Low (Vol)		-	-	0.4		
Duty Cycle at 50% of Vcc		45	50	55	%	
Rise / Fall Time: 10% to 90%		-	-	6	ns	
Spurious Output		-	-	-80	dBc	
	Sinewave Ou	tput Charact	eristics (Outp	ut Code S)		
Parameter		Minimum	Nominal	Maximum	Units	Notes
Load		-	50	-	Ohms	
Output Power		5.0	-	-	dBm:	
Harmonics		-	-	-30	dBc	
Spurious		-	-	-80	dBc	
		Package Cha	racteristics			
OH300 Package	Package consis	ting of a FR4 sul	bstrate and Rytor	n-R4 cover. Water Re	esistant packaç	je,
	Recor	nmended C	leaning Proc	2200		
Wash only in a in-line high pressure wash station that has an air knife and drying capabilities.						
(Drying temperature range from 85° to 100°C)						
	En	/ironmental C	naracteristic	S		
Shock	500 Gis 1ms, Ha	Itsine, 3 shocks	per direction, per	r MIL-STD 202G, ME	thod 213B les	t Condition D.
Sinusoidal Vibration	0.06 D.A. or 100	a S Peak, TU to 5	DU HZ, per MIL-S	TD-202G, Method 2	04D, lest Cond	dition A.
Random Vibration	5.35 G S rms. 20 t	0 2000 Hz per M	IIL-STD-202G, Me	thod 214, lest Condit	ion TA, 15 minu	ites each axis.
Moisture	IU CYCIES, 95% H	(H, Per MIL-STD	-202G, Method T	12.		
Marking Permanency	Per MIL-STD-202	G, Method 215J	J. (1)			
Solder Process	Rons compliant,	lead free. See s	solder profile.			
Re-Stabilization Time						
Off Time	Re stabilization Ti	ime				
<1 Hour	<2 Hours *					
<6 Hour	<12 Hours *					
<24 Hour	<48 Hours *					
1 to 16 Days	48 Hours + 1/4 C	off Time *				
>16 Days	<6 Days *					
* For a given off time, the time required	to meet daily aging, shor	t term stability require	ements.			

#### Notes:

1. ±5.0 ppb stability models are only available from 0 to 70°C.

2. Referenced to the frequency measured @ 25°C. OCVCXO control voltage (Vc) = nominal voltage.

3. At time of shipment after 48 hours of operation.

4. Inclusive of calibration, operating temperature, supply voltage change, load change and 20 years aging.

5. Minimum "Power On Time" after rail rises from 0 to within +/-5% of Vcc = 1 second. Vcc ramp rate must be <0.3 volts per millisecond.

6. Measured with Vcc = Nominal, in calm air.

7. Measured @ 25°C, within ± 100 ppb, referenced one hour after turn-on.

8. To ensure proper operation of OCVCXO's the control voltage input must be to bias the Vc input will cause an unstable output condition.

Dullatin

Shock	500 G's 1ms, Halfsine, 3 shocks per direction, per MIL-STD 202G, Method 213B Test Condition D.	
Sinusoidal Vibration	0.06" D.A. or 10G's Peak, 10 to 500 Hz, per MIL-STD-202G, Method 204D, Test Condition A.	
Random Vibration	5.35 G's rms. 20 to 2000 Hz per MIL-STD-202G, Method 214, Test Condition 1A, 15 minutes each axis.	
Moisture	10 cycles, 95% RH, Per MIL-STD-202G, Method 112.	
Marking Permanency	Per MIL-STD-202G, Method 215J.	
Solder Process	RoHS compliant, lead free. See solder profile.	
Re-Stabilization Time		

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biased the nominal control voltage. Failure	Revision	11
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Attention: System Designers please review Application Note AN2093: System Design Information and Printed Circuit Board Layout Guidelines for OCXO Oscillators. @ www.conwin.com/technologies.html



**Package Outline** 

Pad Termination Finish: Gold Flash <10 micro inches

# Marking Information



# Suggested Pad Layout



• \* Do not route any traces in the keep out area. It is recommended the next layer under the keep out area is to be ground plane.

### **Pad Connections**

- 1: Control Voltage or N/C
- 2: N/C
- 3: Supply Voltage (Vcc)
- 4: RF Output
- 5: Do Not Connect
- 6: Do Not Connect
- 7: Ground (Case)

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# Test Circuit CMOS Output



# **Test Circuit Sinewave Output**





# **CMOS** Output Waveform







Meets IPC/JEDEC J-STD-020C

# **Tape and Reel Information**

