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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



Photologic® Optical Flag Switch

OPB685-3, OPB686, OPB687, OPB695, OPB696, OPB697, OPB698 Series

Features:

- Photologic[®] output
- Four output options
- Mechanical switch replacement
- Printed circuit board mounting (OPB685-3)
- 2.5mm, 3-pin connector mates with Molex connector 5051 series housing and 4809 series terminal for OPB695 Series

Description:

Each **OPB685-3** and **OPB695** series flag switch consists of an infrared emitting diode and a monolithic integrated circuit that incorporates a photodiode, a linear amplifier and a Schmitt trigger. A lever arm actuated flag interrupts the light beam, which switches the output between states that can readily drive logic gates.

The **OPB695** series is designed to easily snap mount into a $0.037'' \pm 0.001''$ (0.940 mm ± 0.025 mm) thick material with a rectangular opening of $0.320'' \pm 0.003'' \times 0.472''$ (8.13 mm x 11.99 mm) minimum. Insertion into the punched side of metal is recommended.

Devices in these series feature TTL/LSTTL compatible logic level output that can drive up to 10 TTL loads over a voltage range from 4.5 V to 16 V.

Customized lever arms and spring torques can be designed for specific applications for each of the devices.

Custom electrical, wire, cabling and connectors are available. Contact your local representative or OPTEK for more information.

Applications:

- Mechanical switch replacement
- Speed indication (tachometer)
- Mechanical limit indication
- Edge sensing

Ordering Information						
Part Number	LED Peak Wavelength	Sensor Photologic®	Flag Travel Degrees Max	Lead Length / Spacing or Connector		
OPB685-3		10K Pull-Up				
OPB686		Open Collector	59°	0.100" / 0.275"		
OPB687		Inv. 10K Pull-Up				
OPB695AZ	890 nm	10K Pull-Up		Mates with		
OPB696AZ		Open Collector		3 Pin—Molex 5051 (22-01-1032) Housing & 4809 (08-70-0069) Terminals		
OPB697AZ		Inv. 10K Pull-Up	70°			
OPB698CZ		Inv. Open Collector				



General Note

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OPB685-3, OPB686, OPB687, OPB695, OPB696, OPB697, OPB698 Series





OPB685-3, OPB686, OPB687,

Pin #	Description			
1	Anode			
5	Cathode			
2	Ground			
3	Output			
4	V _{CC}			

OP695, OPB696, OPB697, OPB698

Part Number	Max. Torque (Grams)		
OPB685-3	3.0		
OPB686	1.5		
OPB687	1.5		
OPB695	1.5		
OPB696	1.5		
OPB697	1.5		
OPB698	1.5		

Torque is measured at the end of the arm from the resting position to the switching point of the flag



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Absolute Maximum Ratings (T _A = 25° C unless otherwise noted)					
Storage & Operating Temperature Range	-40°C to +100°C				
Lead Soldering Temperature [1/16 inch (1.6mm) from the case for 5 sec. with soldering iron] ⁽¹⁾	260°C				
Input Diode					
Reverse Voltage	2.0 V				
Continuous Forward Current	50 mA				
Peak Forward Current	3.0 A				
Total Device Power Dissipation ⁽²⁾	100 mW				
Output Photologic [®]					
Supply Voltage, V _{CC}	18 V				
Duration of Output Short to V _{CC}	1 second				
Voltage at Output	30 V				
Low Level Output Current (sinking)	16 mA				
Power Dissipation ^{(3) (4)}	240 mW				

Notes:

(1) RMA flux is recommended. Duration can be extended to 10 seconds maximum when flow soldering.

(2) Derate linearly 1.33 mW/°C above 25° C.

(3) Derate linearly 2.00 mW/°C above 25° C (OPB680, OPB680-20, OPB690Z).

(4) Derate linearly 2.50 mW/°C above 25° C (OPB685-3, OPB686, OPB687, OPB695, OPB696, OPB697, OPB698).

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OPB685-3, OPB686, OPB687, OPB695, OPB696, OPB697, OPB698 Series



OPB685-3 and OPB695 Series



OPB687, OPB697 Inverted 10K Pull-Up



OPB686, OPB696 Buffered Open-Collector

OPB698 Inverted Open-Collector



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OPB685-3, OPB686, OPB687, OPB695, OPB696, OPB697, OPB698 Series



Electrical Characteristics (T _A = 25° C unless otherwise noted)							
SYMBOL	PARAMET	ER	MIN	ТҮР	МАХ	UNITS	TEST CONDITIONS
Input Diode	1						
V _F	Forward Voltage		-	-	1.6	V	I _F = 10 mA
I _R	Reverse Current		-	-	100	μA	V _R = 3 V
Output Pho	tologic [®] Sensor						
V _{cc}	Operating DC Supply Volta	ge OPB685-3 Series OPB695-698A OPB695-698B OPB695-698C	4.5 4.5 8.0 13.5	- 5.0 12.0 15.0	16.0 8.0 13.5 16.0	V	
I _{cc}	Operating DC Supply Volta OPB695-698A/B/C	ge	-	20	30	mA	
I _{CCL}	Low Level Supply Current: Buffered 10k Pull-Up Buffered Open-Collector	OPB685-3 OPB686		5.5 4.0	12 12	mA	V_{CC} = 16 V, I _F = 0 mA (no load on output)
	Inverted 10k Pull-Up	OPB687	-	6.5	12	mA	V_{CC} = 16 V, I _F = 10 mA (no load on output)
logu	High Level Supply Current: Buffered 10k Pull-Up Buffered Open-Collector	OPB685-3 OPB686	-	5.0	12	mA	V_{cc} = 16 V, I _F = 10 mA (no load on output)
	Inverted 10k Pull-Up	OPB687	-	4.0	12	mA	$V_{CC} = 16 \text{ V}, I_F = 0 \text{ mA} \text{ (no load on output)}$
V _{OL}	Low Level Output Voltage ⁽³ Buffered 10k Pull-Up Buffered Open-Collector	.). OPB685-3 OPB686 OPB695 OPB696A/B/C		- - -	0.4 0.4 0.4 0.4	V	$V_{CC} = 4.5 \text{ V}, I_{OL} = 16 \text{ mA}, I_F = 0$ $V_{CC} = 4.5 \text{ V} \text{ to } 8 \text{ V}, I_{OL} = 16 \text{ mA}$ $V_{CC} = 8.5 \text{ V} \text{ to } 13 \text{ V}, I_{OL} = 16 \text{ mA}$ $V_{CC} = 13.5 \text{ V} \text{ to } 16 \text{ V}, I_{OL} = 16 \text{ mA}$
	Inverted 10k Pull-Up ⁽²⁾ Inverted Open-Collector	OPB685-3 OPB686 OPB695 OPB696A/B/C		- - - -	0.4 0.4 0.4 0.4	V	$V_{CC} = 4.5 V, I_{OL} = 16 mA, I_F = 0$ $V_{CC} = 4.5 V to 8 V, I_{OL} = 16 mA$ $V_{CC} = 8.5 V to 13 V, I_{OL} = 16 mA$ $V_{CC} = 13.5 V to 16 V, I_{OL} = 16 mA$
V	High Level Output Voltage ⁽ Buffered 10k Pull-Up	2):	V _{cc} -1.5	-	-	v	I _{OH} = 100 μA, I _F = 10 mA
V _{OH}	Inverted 10k Pull-Up ⁽¹⁾ Inverted Open-Collector		V _{cc} -1.5	-	-	v	I _{OH} = 100 μA, I _F = 0 mA
I _{он}	High Level Output Voltage ⁽ Buffered Open-Collector	²⁾ : OPB686 OPB696A OPB696B OPB696C	- - -	- - - -	100 100 100 100	μΑ	$V_{CC} = 16 \text{ V}, \text{ I}_{F} = 10 \text{ mA}, \text{ V}_{CH} = 30 \text{ V}$ $V_{CC} = 4.5 \text{ V} \text{ to } 8 \text{ V}, \text{ V}_{OH} = 30 \text{ V}$ $V_{CC} = 8.5 \text{ V} \text{ to } 13 \text{ V}, \text{ V}_{OH} = 30 \text{ V}$ $V_{CC} = 13.5 \text{ V} \text{ to } 16 \text{ V}, \text{ V}_{OH} = 30 \text{ V}$
	Inverted 10k Pull-Up ⁽¹⁾	OPB698A OPB698B OPB698C			100 100 100	μΑ	$V_{CC} = 4.5 V \text{ to } 8 V, V_{OH} = 30 V^{(1)}$ $V_{CC} = 8.5 V \text{ to } 13 V, V_{OH} = 30 V^{(1)}$ $V_{CC} = 13.5 V \text{ to } 16 V, V_{OH} = 30 V^{(1)}$

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Photologic® Optical Flag Switch

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Electrical Characteristics (T _A = 25° C unless otherwise noted)								
SYMBOL	PARAMETER		MIN	ТҮР	МАХ	UNITS	TEST CONDITIONS	
Output Photologic [®] Sensor (continued)								
I _{F(+)}	LED Positive-Going Threshol	d Current OPB685-3—687	0.1	1.8	10	mA	V _{cc} = 5 V	
I _{F(+)} /I _{F(-)}	Hysteresis	OPB685-3	1.0	1.2	1.6	mA	V _{cc} = 5 V	
t _r t _f	Rise Time, Fall Time		-	30	-	ns		
t _{plh} t _{phl}	Propagation Delay Low-High Buffer, 10k Pull-Up Buffer, Open-Collector Inverter, 10k Pull-Up	a & High-Low: OPB685-3 OPB686 OPB687	- - -	1 (LH) 2 (HL) 2 (LH)	- -	μs μs μs	V _{cc} = 5 V, I _F = 0 or 10 mA R _L = 300 Ω, DC = 50% f = 10 kHz	

Notes:

(1) Test requires lever arm in "blocked" position.

(2) Test requires lever arm in "unblocked" position

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Issue	Change Description	Approval	Date
А	Initial Release—New Layout		03/08/06
A.1	Changed connector information	Mark Miller	1/22/07
A.2	Fixed Absolute Maximum ratings for the LED on page 3	Mark Miller	04/03/2009
A.3	Added Notes #2—"Test requires lever arm in "unblocked" position	Tom Osborne	03/05/2015
В	Transferred to the new TT Electronics template	L. Timpa	10/5/2016
С	Eliminated obsolete product #s	Julia Knight	9/20/17

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