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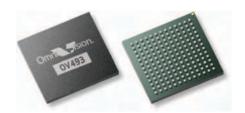
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OV493 companion ISP product brief



Cost-Effective Surround-View Image Processing Solution for Automotive Applications

available in a lead-free package

OmniVision's OV493 is a compact image signal processor (ISP) companion chip that brings best-inclass image quality to 360-degree surround-view and video mirror imaging applications. By processing two video streams simultaneously, the OV493 makes it possible for a system with only two ISP companion chips to process four camera inputs, enabling flexible electronic control unit (ECU) designs for surround-view applications and dramatically reducing overall system cost.

While most ISP companion chips are located on the camera module, the OV493 can be placed on the ECU module for unprecedented flexibility and streamlined

system architectures. The OV493 is designed to work seamlessly with OmniVision's high-performance OV9716 and OV10640 automotive image sensors, supporting dual sensor input of up to 1.4 megapixels at 30 frames per second (fps) with high dynamic range (HDR) each.

The 0V493 comes in an AEC-Q100 Grade 2-qualified 7×7 mm ball grid array (BGA) package.

Find out more at www.ovt.com.





Applications

- Surround view system
- Camera Monitoring System (CMS)

■ e-Mirror

Product Features

- advanced 100Mp/s throughput ISP for high quality image capturing and video streaming
- local and global tone mapping support
- up to 3-capture HDR combination
- simultaneous output of fully processed YUV for display-based applications and RAW for machine vision applications
- embedded information including frame counter, temperature, and register data for each image to enable critical automotive safety applications
- auto white balance (AWB)
- automatic exposure control (AEC) / automatic gain control (AGC)
- supports statistics data of up to four user programmable ROIs
- SCCB master/slave interface for sensor and ASIC configuration

- supports four-wire or two-wire serial interface to retrieve stored firmware from external memory devices, such as EEPROM
- on-chip PLL to generate internal clocks
- on-chip voltage regulator 1.8V to 1.1V
- brown-out detection circuit and output flag
- supports 2x2 lane MIPI RX/TX and 1x4 lane MIPI RX/TX
- supports 12-bit DVP input, shared with MIPI RX
- supports 12-bit DVP output, shared with MIPI TX
- supports 24-bit DVP output, shared with BT1120 output
- embedded 32-bit RISC processor for high performance and flexibility
- supports 1K bits of one-time programmable memory (OTP)
- JTAG boundary scan



- 0V00493-B69G-1A (lead-free, 169-pin BGA, packed in tray)
- 0V00493-B69G-TA (lead-free, 169-pin BGA, packed in tape and reel)

Product Specifications

- power supply: core: 1.1V ±5% I/O: 1.8V ±5% or 3.3V ±5%
- power requirements: 256 mW, measured at 1392x976 @ 30 fps, 12-bit plus 12-bit input from 0V9716 via MIPI interface and 1392x976 YUV422 output via MIPI interface
- temperature range:
 operating: -40°C to +105°C ambient temperature and -40°C to +125°C junction temperature
- package dimensions: 7 mm x 7 mm

Advanced Architecture Example

ECU module camera modules 30 fps RAW 2x [RAW @ 30 fps] output LVDS MIPI CSI-2 / DVP 2x 2 lane MIPI CSI-2 1x 4 lane MIPI CSI-2 OV9716 **0V493 SER** I2C I2C I2C I2C ¥ 30 fps RAW MIPI CSI-2 / DVP LVDS OV9716 SER I2C I2C GPU / video SPI DSP / auad DES flash MCU / 30 fps RAW FPGA MIPI CSI-2 / DVP **LVDS** OV9716 SER I2C I2C 30 fps RAW 2x [RAW @ 30 fps] output LVDS MIPI CSI-2 / DVP 2x 2 lane MIPI CSI-2 1x 4 lane MIPI CSI-2 OV9716 **0V493** SER [4] [2] 120 I2C 120 120

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