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## OCXO **Specification** OX914xS3 Series



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#### **Description:**

Connor-Winfield model series OX914xS3 is a 3.3 Vdc. Oven Compensated Crystal Oscillator (OCXO) in a 9x14 mm SMT package. The OX914xS3 series is a low cost, high performance OCXO that meets STRATUM 3 requirements.



Features: **OCXO** 3.3 Vdc Operation SMT Package Frequency Stability: +/-140 ppb Temperature Ranges Available: 0 to 70°C, -20 to 70°C or -40 to 85°C LVCMOS Output Logic Tape and Reel Packaging RoHS Compliant / Lead Free

**Absolute Maximum Ratings** 

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	85	°C	
Supply Voltage (Vcc)	-0.5	_	5.5	Vdc	
Supply vertage (199)		g Specification			
Parameter	Minimum	Nominal	Maximum	Units	Notes
Frequencies Available: (Fo)		.8,13, 19.2, 19.44		MHz	110100
Freq. Calibration @ 25°C	-1.0	-	1.0	ppm	1
Freq. Stability vs. Temperature	-140	_	140	dqq	2
Daily Aging	-40		40	pbb	3
Holdover Stability:	-320	_	320	ppb	4
MTIE	-	_	1E-6	ppo	5
Aging per Year	-300		300	ppb	3
Freq. Stability vs. Supply Voltage	-20	_	20	ppb	(+/-5%)
Freq. Stability vs. Load Change	-10	-	10	daa	(+/-10%)
Short Term Stability	-		1.0E-9/s	PPS	(17 1070)
Total Frequency Tolerance (20 Years)	) -4.60	_	4.6	ppm	6
Operating Temperature Range: (See		rmation on page			
Models OX9140S3	0	- -	70	°C	
Models OX9142S3	-20	_	70	°C	
Models OX9143S3	-40	-	85	°C	
Supply Voltage: (+/-5%) (Vcc)	3.135	3.30	3.465	Vdc	
Power Consumption: Turn On	-	_	3.00	W	
Power Consumption: Steady State	-	-	1.30	W	
Warm Up Time (Within Specification	@ 25°C)	-	60	S	
Warm Up Time (Within Specification		-	90	S	
		utput Charact	eristics	-	
Parameter	Minimum	Nominal	Maximum	Units	Notes
Load -	-	15	-	pF	7
Voltage: High (Voh)	2.7	-		Vdc	•
Low (Vol)	-	_	0.3	VGO	
Duty Cycle at 50% of Vcc	45	50	55	%	
Rise / Fall Time: 10% to 90%	-	-	6.5	ns	
Phase Jitter (BW=12KHz to fo/2)	_	0.5	1	ps rms	
Typical Phase Noise for OX9140S3-0	)10.0M	0.0		ренне	
SSB Phase Noise at 1Hz offset	-	-65	_	dBc/Hz	
		-105	_	dBc/Hz	
SSB Phase Noise at Turiz offset	-				
SSB Phase Noise at 10Hz offset SSB Phase Noise at 100Hz offset	-		_	dBc/Hz	
SSB Phase Noise at 100Hz offset	-	-130	-	dBc/Hz dBc/Hz	
	-		- - -	dBc/Hz dBc/Hz dBc/Hz	



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#### **Package Characteristics** Package:

Package consisting of a FR4 substrate and a Ryton-R4 cover.

#### Notes:

- 1. Initial calibration @ 25°C.
- 2. Frequency stability vs. change in temperature. [±(Fmax Fmin)/2.Fo].
- 3. After 30 days of operation.
- 4. Peak to peak frequency stability vs. change in temperature, frequency stability vs. change in voltage, frequency stability vs. change in load and aging over a 24 hour period.
- 5. 0.16 seconds < Observed time < 64 seconds at a constant temperature with 1 hour warm-up.
- 6. Inclusive of calibration @ 25°C, frequency vs. change in temperature, change in supply voltage (±5%), load change (±10%), shock and vibration and 20 years aging
- 7. Attention: To achieve optimal frequency stability, and in some cases to meet the specification stated on this data sheet, it is required that the circuit connected to this OCXO output must have the equivalent input capacitance that is specified by the nominal load capacitance. Deviations from the nominal load capacitance will have a graduated effect on the stability of approximately 20 ppb per pF load difference.

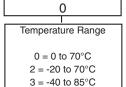


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## **Ordering Information**





S3
STRATUM 3
S3

-019.44M

Output Frequency
Frequency Format
-xxx.xM Min.\*
-xxx.xxxxxM Max\*

Example Part Number:

OX9140S3-019.44M = OCXO, 9x14mm package, 0 to 70°C, STRATUM 3, 19.44 MHz

\*Amount of numbers after the decimal point. M = MHz

#### **Environmental Characteristics**

Vibration:	Vibration per Mil Std 883E Method 2007.3 Test Condition A
Shock:	Mechanical Shock per Mil Std 883E Method 2002.4 Test Condition B.
Soldering Proce	ss: RoHS compliant lead free. See soldering profile on page 2.

## **Recommended Cleaning Process**

Wash only in a in-line high pressure wash station that has an air knife and drying capabilities. (Drying temperature range from 85° to 100°C

## Package Outline

Side

0926 140S3 . MHZ

SS

0X91<sup>2</sup>

## Suggested Pad Layout

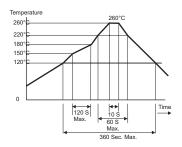
# 0.085 (2.16mm) (9.52mm) (2.16mm) (0.085 (2.16m

Dimensional Tolerance: +/-0.005 (0.127mm)

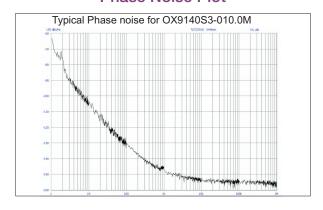
## **Pad Connections**

1:	N/C
2:	N/C
3	Ground:
4:	Output
5:	N/C
6:	Supply Voltage (Vcc)

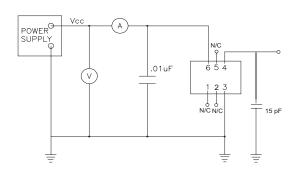
#### Solder Profile



## **Phase Noise Plot**

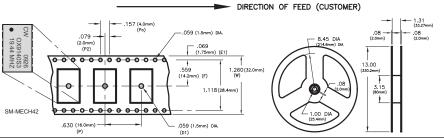


## **Test Circuit**



## **Tape and Reel Information**

MEETS EIA-481A & EIAJ-1009B 500 PCS/REEL MAXIMUM



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