

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China











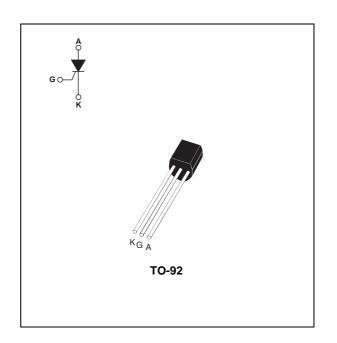
SENSITIVE 0.8A SCRs

MAIN FEATURES:

Symbol	Value	Unit
I _{T(RMS)}	0.8	Α
V _{DRM} /V _{RRM}	100	V
I _{GT}	1	μA

DESCRIPTION

The P0130AA is a gate sensitive SCR, packaged in TO-92, used in conjunction of a TN22 A.S.D $^{\text{TM}}$ and of a resistor in electronic starter for fluorescent tubelamps.



ABSOLUTE RATINGS (limiting values)

Symbol	Parameter			Value	Unit
I _{T(RMS)}	RMS on-state current (180° conduction angle)		TI = 55°C	0.8	А
IT _(AV)	Average on-state current (180° conduction angle)		TI = 55°C	0.5	А
I _{TSM}	Non repetitive surge peak on-state current	tp = 8.3 ms	Tj = 25°C	8	Α
		tp = 10 ms	1) = 23 0	7	^
l ² t	I ² t Value for fusing	tp = 10ms	Tj = 25°C	0.24	A ² S
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, tr $\leq 100 \text{ ns}$	F = 60 Hz	Tj = 125°C	50	A/µs
I _{GM}	Peak gate current	tp = 20 μs	Tj = 125°C	1	Α
P _{G(AV)}	Average gate power dissipation Tj = 125°0		Tj = 125°C	0.1	W
T _{stg} Tj	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C

May 2002 - Ed: 2

P0130AA

ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

Symbol	Test Conditions			P0130AA	Unit
I _{GT}	MIN.		0.1		
	$V_D = 12 V$ $R_L = 140 \Omega$		MAX.	1	μA
V _{GT}	MAX.		0.8	V	
V _{GD}	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $R_{GK} = 1 \text{ k}\Omega$	Tj = 125°C	MIN.	0.1	V
V _{RG}	I _{RG} = 10 μA MI		MIN.	8	V
lн	$I_T = 50 \text{ mA}$ $R_{GK} = 1 \text{ k}\Omega$		MAX.	5	mA
ΙL	$I_G = 1 \text{ mA}$ $R_{GK} = 1 \text{ k}\Omega$		MAX.	6	mA
dV/dt	$V_{D} = 67 \% V_{DRM} R_{GK} = 1 k\Omega$ $Tj = 125 °C$		MIN.	25	V/µs
V _{TM}	I _{TM} = 1.6 A tp = 380 μs	Tj = 25°C	MAX.	1.95	V
V _{t0}	Threshold voltage	Tj = 125°C	MAX.	0.95	V
R _d	Dynamic resistance	Tj = 125°C	MAX.	600	mΩ
I _{DRM}	$V_{DRM} = V_{RRM}$ $R_{GK} = 1 \text{ k}\Omega$	Tj = 25°C	MAX.	1	μA
I _{RRM}		Tj = 125°C	MAX.	100	

THERMAL RESISTANCES

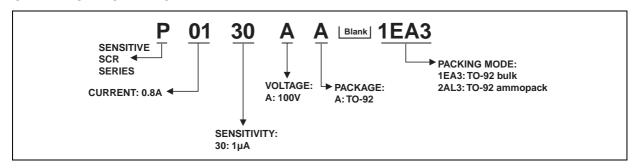
Symbol	Parameter	Value	Unit
R _{th(j-i)}	Junction to case (DC)	80	°C/W
R _{th(j-a)}	Junction to ambient (DC)	150	°C/W

PRODUCT SELECTOR

Part Number	Voltage	Sensitivity	Package
P0130AA	100V	1 μΑ	TO-92

2/5

ORDERING INFORMATION



OTHER INFORMATION

Part Number	Marking	Weight	Base Quantity	Packing mode
P0130AA 1EA3	P0130AA	0.2 g	2500	Bulk
P0130AA 2AL3	P0130AA	0.2 g	2000	Ammopack

Note: xx = sensitivity, y = voltage

Fig. 1: Maximum average power dissipation versus average on-state current.

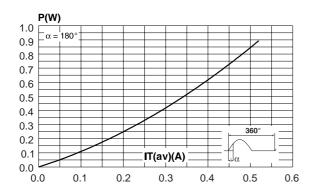


Fig. 2-2: Average and D.C. on-state current versus ambient temperature.

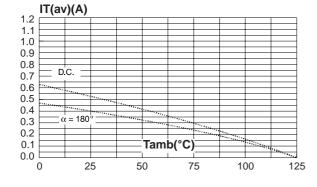


Fig. 2-1: Average and D.C. on-state current versus lead temperature.

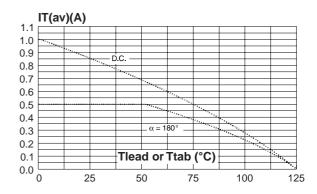
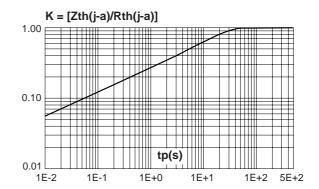


Fig. 3: Relative variation of thermal impedance junction to ambient versus pulse duration.



577

Fig. 4: Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values).

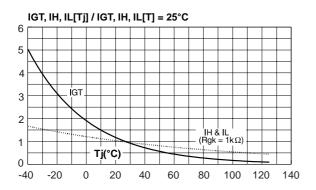


Fig. 6: Relative variation of dV/dt immunity versus gate-cathode resistance (typical values).

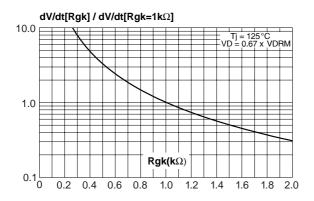


Fig. 8: Surge peak on-state current versus number of cycles.

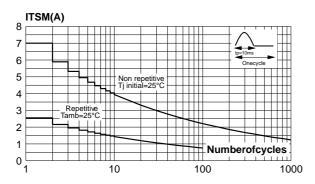


Fig. 5:Relative variation of holding current versus gate-cathode resistance (typical values).

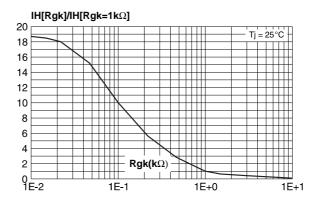


Fig. 7: Relative variation of dV/dt immunity versus gate-cathode capacitance (typical values).

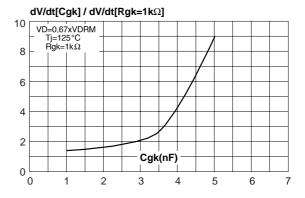
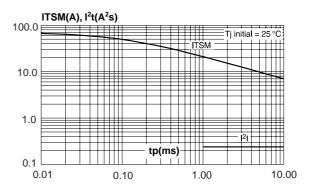
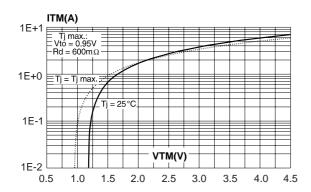


Fig. 9: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10 ms, and corresponding value of I^2t .



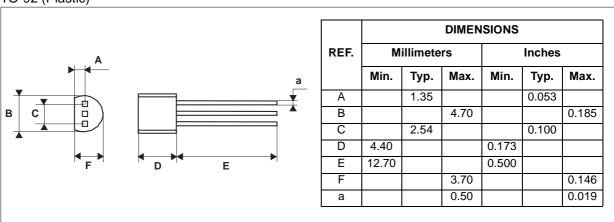
4/5

Fig. 10: On-state characteristics (maximum values).



PACKAGE MECHANICAL DATA

TO-92 (Plastic)



Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

 $\ensuremath{\text{@}}$ The ST logo is a registered trademark of STMicroelectronics

 $\hbox{@\,}2002$ STMicroelectronics - Printed in Italy - All Rights Reserved

STMicroelectronics GROUP OF COMPANIES
Australia - Brazil - Canada - China - Finland - France - Germany
Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore
Spain - Sweden - Switzerland - United Kingdom - United States

http://www.st.com

