

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Twin SLIC Protector



Subscriber Line Interface Circuits (SLIC) are highly susceptible to transient voltages, such as lightning and power cross conditions. To minimize this threat, Littelfuse provides this dual-chip, fixed-voltage SLIC protector device.

For specific design criteria, see details in Figure 3.29.

Electrical Parameters

Part	V _{DRM} Volts	V _S Volts	Vτ	VF	I _{DRM}	ls	lτ	lн	co
Number *	Pins 1-2, 3-2		Volts	Volts	μAmps	mAmps	Amps	mAmps	pF
P0641CA2	58	77	4	5	5	800	1	120	60
P0721CA2	65	88	4	5	5	800	1	120	60
P0901CA2	75	98	4	5	5	800	1	120	60
P1101CA2	95	130	4	5	5	800	1	120	60
P1701CA2	160	200	4	5	5	800	1	120	70

^{*} For surge ratings, see table below.

General Notes:

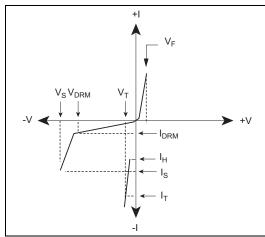
- All measurements are made at an ambient temperature of 25 °C. IPP applies to -40 °C through +85 °C temperature range.
- $\ensuremath{\mathsf{I}_{PP}}$ is a repetitive surge rating and is guaranteed for the life of the product.
- V_{DRM} is measured at I_{DRM}.
- V_S and V_F are measured at 100 V/ μ s.
- Special voltage (V_S and V_{DRM}) and holding current (I_H) requirements are available upon request.
- Off-state capacitance (C_O) is measured across pins 1-2 or 3-2 at 1 MHz with a 2 V bias. Capacitance across pins 1-3 is approximately half.
- Parallel capacitive loads may affect electrical parameters.
- Compliance with GR 1089 or UL 60950 power cross tests may require special design considerations. Contact the factory for further information

Surge Ratings (Preliminary Data)

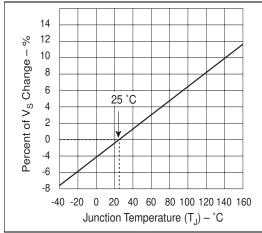
	Series	l _{PP} 2x10 μs Amps	I _{PP} 8x20 μs Amps	I _{PP} 10x160 μs Amps	I _{PP} 10x560 μs Amps	I _{PP} 10x1000 μs Amps	I _{TSM} 60 Hz Amps	di/dt Amps/µs
l	Α	150	150	90	50	45	20	500

Thermal Considerations

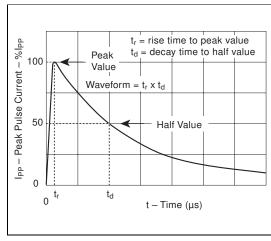
Package	Symbol	Parameter	Value	Unit
Modified DO-214AA	TJ	Operating Junction Temperature Range	-40 to +150	°C
Pin 3	Ts	Storage Temperature Range	-65 to +150	°C
Pin 1	$R_{ hetaJA}$	Thermal Resistance: Junction to Ambient	85	°C/W



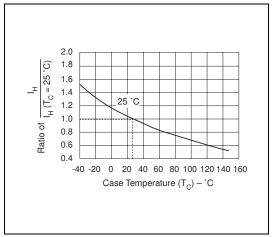
V-I Characteristics



Normalized V_S Change versus Junction Temperature



 $t_{\rm r} \ x \ t_{\rm d}$ Pulse Wave-form



Normalized DC Holding Current versus Case Temperature