Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

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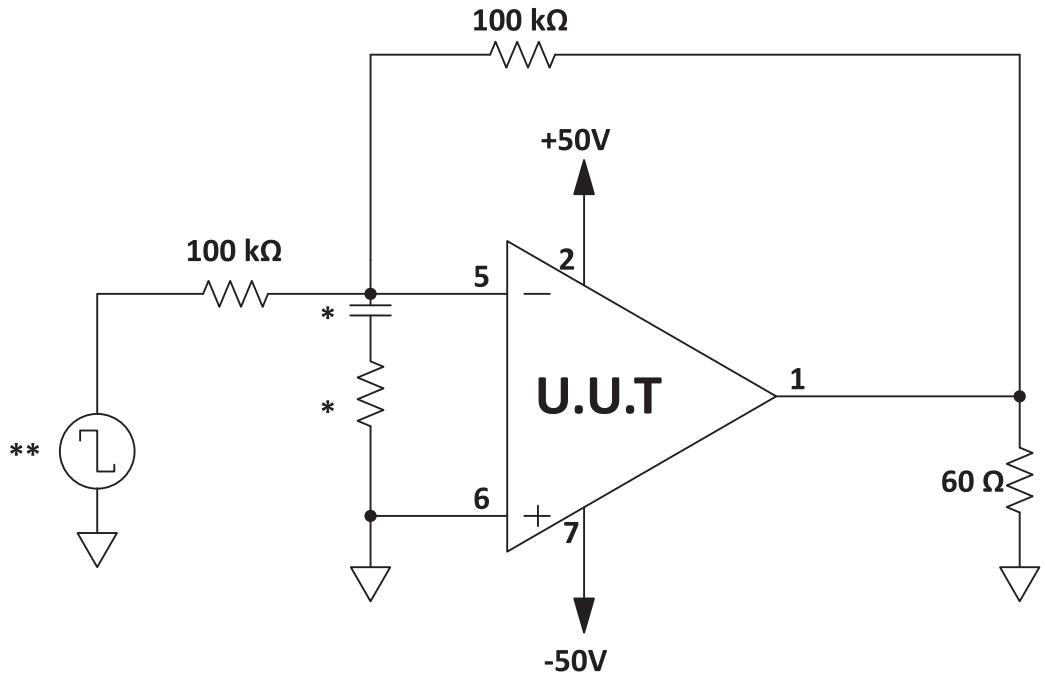
Table 4 Group A Inspection

SG	Parameter	Symbol	Temp.	Power	Test Conditions	Min	Max	Units
1	Quiescent Current	I_Q	25°C	±150V	V_{IN} = 0, A_V = 100		7.5	mA
1	Input Offset Voltage	V_{OS}	25°C	±150V	V_{IN} = 0, A_V = 100		3	mV
1	Input Offset Voltage	V_{OS}	25°C	±15V	V_{IN} = 0, A_V = 100		5.7	mV
1	Input Bias Current, +IN	$+I_B$	25°C	±150V	V_{IN} = 0		50	pA
1	Input Bias Current, –IN	$-I_B$	25°C	±150V	V_{IN} = 0		50	pA
1	Input Offset Current	I_{OS}	25°C	±150V	V_{IN} = 0		50	pA
3	Quiescent Current	I_Q	–55°C	±150V	V_{IN} = 0, A_V = 100		9.5	mA
3	Input Offset Voltage	V_{OS}	–55°C	±150V	V_{IN} = 0, A_V = 100		5	mV
3	Input Offset Voltage	V_{OS}	–55°C	±15V	V_{IN} = 0, A_V = 100		7.7	mV
3	Input Bias Current, +IN	$+I_B$	–55°C	±150V	V_{IN} = 0		50	pA
3	Input Bias Current, –IN	$-I_B$	–55°C	±150V	V_{IN} = 0		50	pA
3	Input Offset Current	I_{OS}	–55°C	±150V	V_{IN} = 0		50	pA
2	Quiescent Current	I_Q	125°C	±150V	V_{IN} = 0, A_V = 100		9.5	mA
2	Input Offset Voltage	V_{OS}	125°C	±150V	V_{IN} = 0, A_V = 100		5.5	mV
2	Input Offset Voltage	V_{OS}	125°C	±15V	V_{IN} = 0, A_V = 100		8.2	mV
2	Input Bias Current, +IN	$+I_B$	125°C	±150V	V_{IN} = 0		10	nA
2	Input Bias Current, –IN	$-I_B$	125°C	±150V	V_{IN} = 0		10	nA
2	Input Offset Current	I_{OS}	125°C	±150V	V_{IN} = 0		10	nA
4	Output Voltage, I_O = 40mA	V_O	25°C	±47V	R_L = 1 k	40		V
4	Output Voltage, I_O = 28.6mA	V_O	25°C	±150V	R_L = 5 k	143		V
4	Output Voltage, I_O = 15mA	V_O	25°C	±80V	R_L = 5 k	75		V
4	Current Limits	I_{CL}	25°C	±20V	R_L = 100 Ω	36	70	A
4	Stability/Noise	E_N	25°C	±150V	R_L = 5 k, A_V = 1, C_L = 10nF		1	mV
4	Slew Rate	SR	25°C	±150V	R_L = 5 k, C_C = 50pF	100	600	V/µs
4	Open Loop Gain	A_{OL}	25°C	±150V	R_L = 5 k, F = 10 Hz	100		dB
4	Common Mode Rejection	CMR	25°C	±32.5V	R_L = 5 k, F = DC, V_{CM} = ±22.5V	90		dB

SG	Parameter	Symbol	Temp.	Power	Test Conditions	Min	Max	Units
6	Output Voltage, I_O = 40mA	V_O	–55°C	±47V	R_L = 1 k	40		V
6	Output Voltage, I_O = 28.6mA	V_O	–55°C	±150V	R_L = 5 k	143		V
6	Output Voltage, I_O = 15mA	V_O	–55°C	±80V	R_L = 5 k	75		V
6	Stability/Noise	E_N	–55°C	±150V	R_L = 5 k, A_V = 1, C_L = 10nF		1	mV
6	Slew Rate	SR	–55°C	±150V	R_L = 5 k, C_C = 50pF	100	600	V/µs
6	Open Loop Gain	A_{OL}	–55°C	±150V	R_L = 5 k, F = 10 Hz	100		dB
6	Common Mode Rejection	CMR	–55°C	±32.5V	R_L = 5 k, F = DC, V_{CM} = ±22.5V	90		dB
5	Output Voltage, I_O = 30mA	V_O	125°C	±37V	R_L = 1 k	30		V
5	Output Voltage, I_O = 28.6mA	V_O	125°C	±150V	R_L = 5 k	143		V
5	Output Voltage, I_O = 15mA	V_O	125°C	±80V	R_L = 5 k	75		V
5	Stability/Noise	E_N	125°C	±150V	R_L = 5 k, A_V = 1, C_L = 10nF		1	mV
5	Slew Rate	SR	125°C	±150V	R_L = 5 k, C_C = 50pF	100	600	V/µs
5	Open Loop Gain	A_{OL}	125°C	±150V	R_L = 5 k, F = 10 Hz	100		dB
5	Common Mode Rejection	CMR	125°C	±32.5V	R_L = 5 k, F = DC, V_{CM} = ±22.5V	90		dB

BURN IN CIRCUIT

Figure 1: Burn In Circuit



* These components are used to stabilize device due to poor high frequency characteristics of burn in board.
** Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.