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## Description

The PAM242x devices are high-performance, fixed frequency, current-mode PWM step-up DC/DC converters that incorporate internal power MOSFETs. The PAM242x includes an integrated power MOSFET that supports peak currents of up to 3A/4.5A/5.5A.

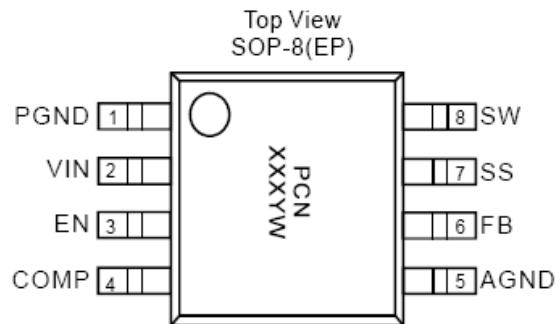
The PAM242x utilizes simple external loop compensation allowing optimization between component size, cost and AC performance across a wide range of applications. Additional functions include an externally programmable soft-start function for easy inrush current control, internal over-voltage protection (OVP), cycle-by-cycle current limit protection, under voltage lock-out and thermal shutdown.

The PAM242x is available in the small SOP-8(EP) package.

## Features

- Greater than 90% Efficiency
- Adjustable Output Voltage Up to 24V
- Internal 24V Power MOSFET
- Peak Current 3A,4.5A,5.5A
- 520kHz Frequency
- Built-in Over-Voltage Protection (OVP)
- Open Protection
- Programmable Soft-Start Function
- Thermal Shutdowns
- Under-Voltage-Lockout
- Over-Current Protection
- SOP-8(EP) Package

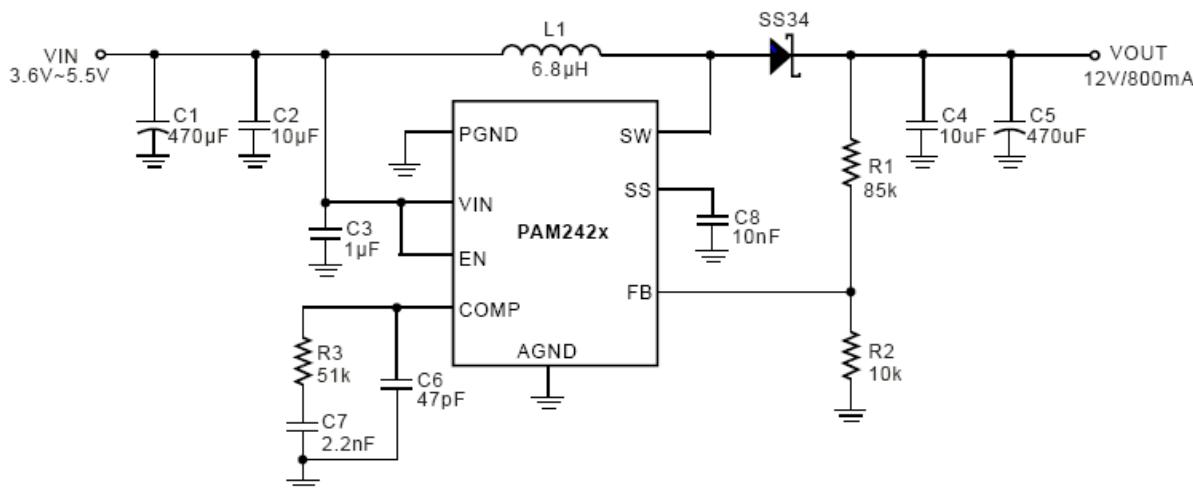
## Pin Assignments



## Applications

- TFT LCD Monitors
- Battery-Powered Equipment
- Set-Top Boxes
- DSL and Cable Modems and Routers

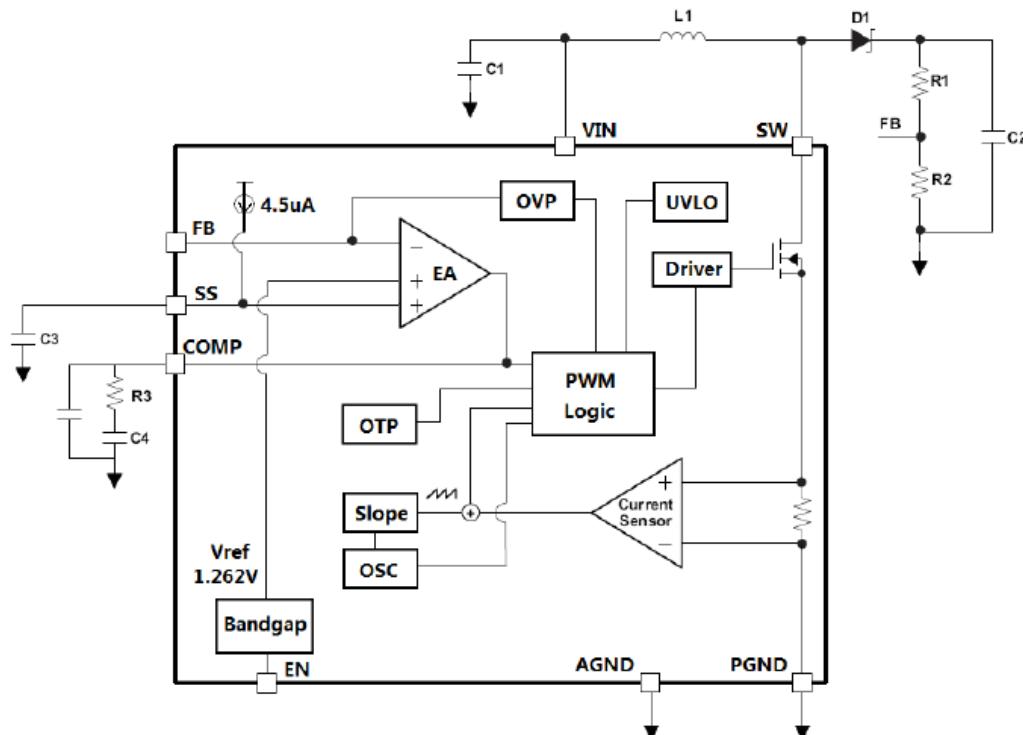
## Typical Applications Circuit



## Pin Descriptions

Pin Number	SOP-8(EP) Pin Name	Function
1	PGND	Power Ground
2	IN	Supply Input
3	EN	Enable Control. Connect to a logic high level to enable the IC. Connect to a logic low level to disable the IC. When unused, connect EN pin to IN (do not leave pin floating).
4	COMP	Error Amplifier Compensation Node
5	AGND	Analog Ground
6	FB	Feedback Input. Connect this pin a resistor divider from the output to set the output voltage. FB is regulated to 1.262V.
7	SS	Soft Start Control Input. Connect a capacitor from this pin to Ground to set soft-start timing duration. SS is discharged to ground in shutdown. SS may be left unconnected if soft start is not desired.
8	SW	Switch Output. Connect this pin to the inductor and the schottky diode. To minimize EMI, minimize the PCB trace path between this pin and the input bypass capacitor.
—	Exposed PAD	Connected to Ground

## Functional Block Diagram



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**Absolute Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

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These are stress ratings only and functional operation is not implied. Exposure to absolute maximum ratings for prolonged time periods may affect device reliability. All voltages are with respect to ground.

Parameter	Rating	Unit
SW to Ground	-0.3 to +28	V
VIN, EN, FB, SS, COMP to Ground	-0.3 to +6.5	V
Continuous SW Current	Internally Limited	V
Operating Temperature Range	-40 to +150	°C
Storage Temperature Range	-55 to +150	°C
Lead Temperature (Soldering, 10 sec)	300	°C

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**Recommended Operating Conditions** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

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Parameter	Rating	Unit
Junction Temperature Range	-40 to +125	°C
Ambient Temperature Range	-40 to +85	

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**Thermal Information**

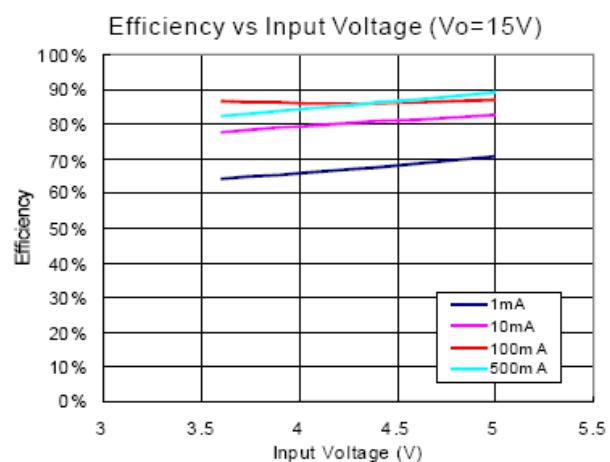
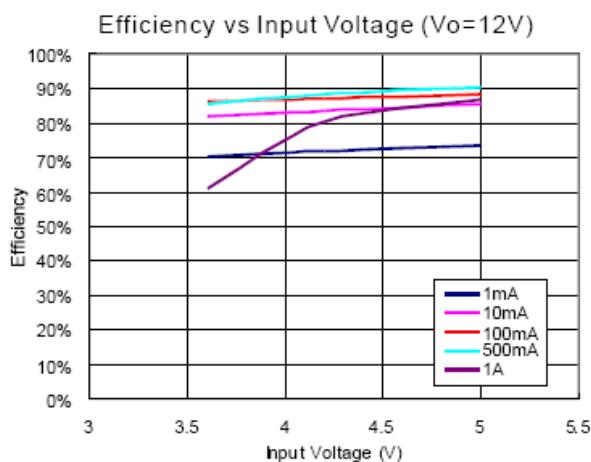
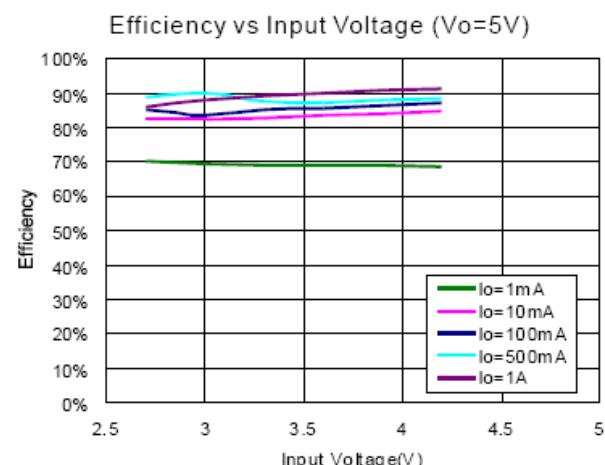
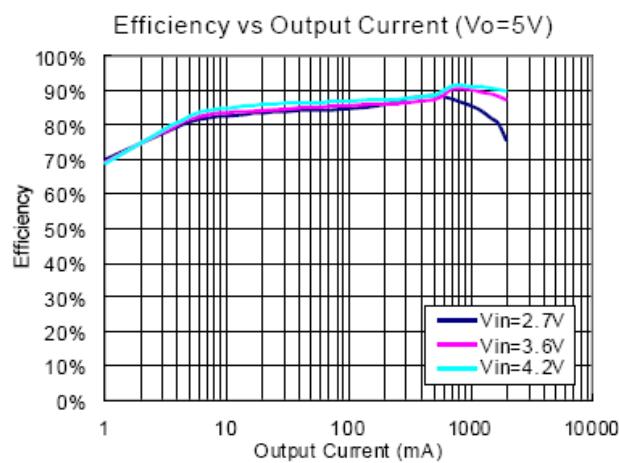
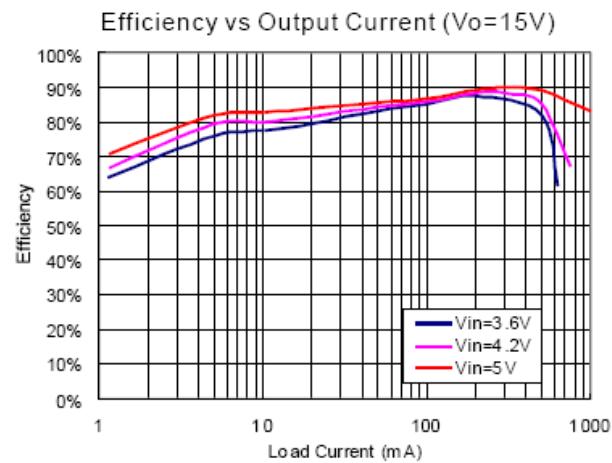
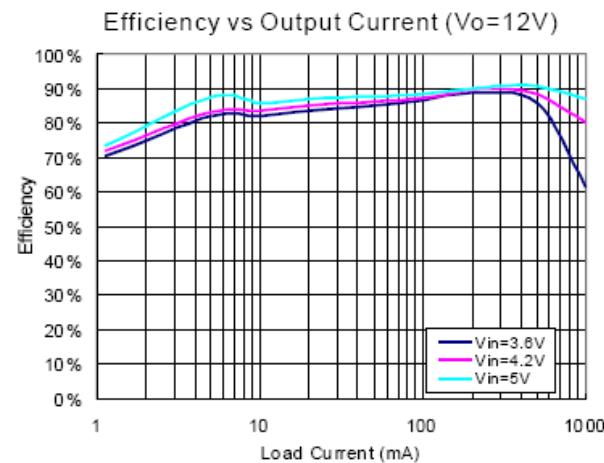

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Parameter	Symbol	Package	Max	Unit
Thermal Resistance (Junction to Case)	$\theta_{JC}$	SOP-8(EP)	75	°C/W
Thermal Resistance (Junction to Ambient)	$\theta_{JA}$	SOP-8(EP)	180	°C/W

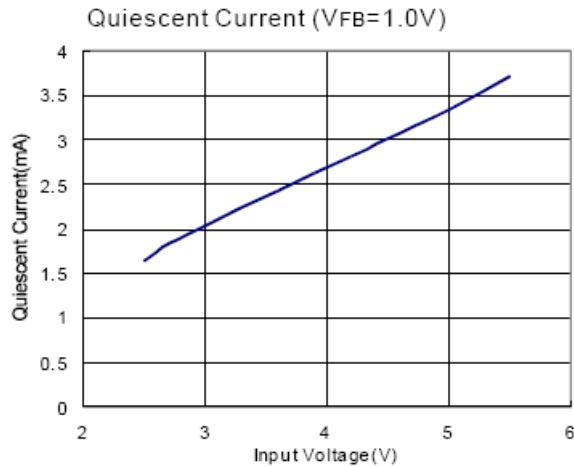
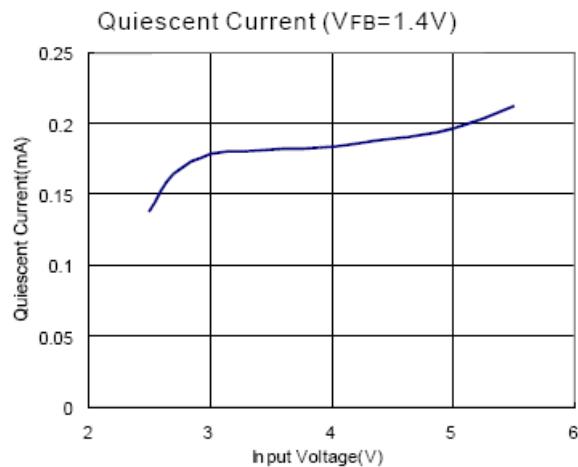
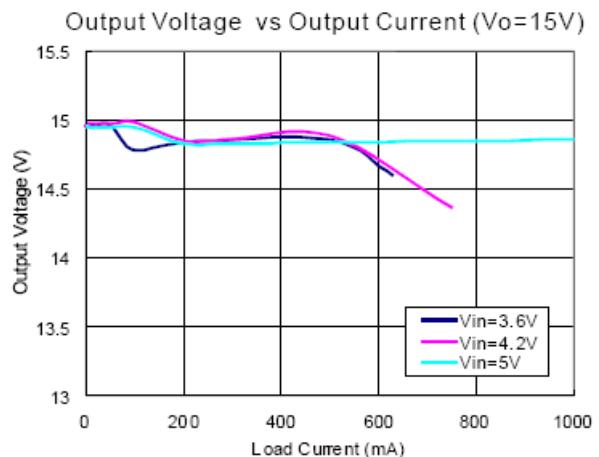
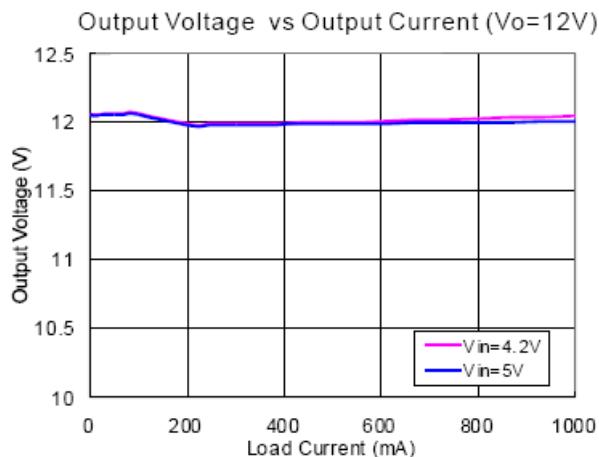
**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ ,  $V_{IN} = V_{EN} = 5\text{V}$ , unless otherwise specified.)

Parameter	Test Conditions		Min	Typ	Max	Units
Input Voltage			2.7		5.5	V
Under Voltage Lockout Threshold	$V_{IN}$ Rising			2.5		V
Under Voltage Lockout Hysteresis				200		mV
Quiescent Supply	$V_{FB} = 1.4\text{V}$ , Not Switching			0.25	0.35	mA
	$V_{FB} = 1.0\text{V}$ , Switching			3	5	
Supply Current in Shutdown	$EN = \text{Ground}$			0.1	1.0	$\mu\text{A}$
Switching Frequency				520		kHz
Maximum Duty Cycle			85			%
FB Feedback Voltage			1.237	1.262	1.287	V
FB Input Current				0	80	nA
FB Voltage Line Regulation	$V_{FB}$ from 2.7V to 5.5V			0.05	0.15	%/V
Switch Current Limit	PAM2421	$V_{IN} = 3.2\text{V}$ Duty Cycle = 70%	3			A
	PAM2422		4.5			
	PAM2421		5.5			
Switching On Resistance	PAM2421			130		$\text{m}\Omega$
	PAM2422			100		
	PAM2423			70		
Switch Leakage Current	$V_{SW} = 10\text{V}$ , $EN = \text{Ground}$				15	$\mu\text{A}$
Soft Start Pin Bias Current	$V_{SS} = 1.2\text{V}$		2.0	4.5	7.0	$\mu\text{A}$
Logic High Threshold	$EN$ Pin		1.4			V
Logic Low Threshold	$EN$ Pin				0.4	V
EN Pull Down Resistor				1		$\text{m}\Omega$
Thermal Shutdown Temperature					160	$^\circ\text{C}$
Thermal Shutdown Hysteresis					40	$^\circ\text{C}$

**Typical Operating Characteristics** (@ $T_A = +25^\circ\text{C}$ ,  $V_{IN} = V_{EN} = 5\text{V}$ ,  $V_{FREQ} = 0\text{V}$ , unless otherwise specified.)



**Typical Performance Characteristics** (cont.) (@ $T_A = +25^\circ\text{C}$ ,  $V_{IN} = V_{EN} = 5\text{V}$ ,  $V_{FREQ} = 0\text{V}$ , unless otherwise specified.)



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## Application Information

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The PAM242x are highly efficient step-up DC/DC converters that employ a current mode, fixed frequency pulse-width modulation (PWM) architecture with excellent line and load regulation. The PAM242x operate at constant switching frequency under medium to high load current conditions. At light loads, these devices operate in a pulse-skipping mode in order to improve light-load efficiency.

### Soft-Start

The PAM242x both offer a programmable soft-start function which minimizes inrush current during startup. The soft-start period is programmed by connecting a 10nF capacitor between SS and Ground. Operation of the soft-start function is as follows: when the IC is disabled, SS is actively discharged to Ground. Upon enabling the IC, CSS is charged with a 4.5 $\mu$ A current so that the voltage at SS increases in a controlled manner. The peak inductor current is limited by the voltage at SS, so that the input current is limited until the soft-start period expires, and the regulator can achieve its full output current rating.

### Setting the Output Voltage

The both feature external adjustable output voltages of up to 24V. To program the output voltage, simply connect a resistive voltage divider between the output, FB, and GND, with resistors set according to the following equation:

$$R1 = R2 \left[ \left( \frac{V_{OUT}}{V_{FB}} \right) - 1 \right]$$

Where  $V_{FB}$  is 1.262V.

### Rectifier Selection

For optimal performance, the rectifier should be a Schottky rectifier that is rated to handle both the output voltage as well as the peak switch current.

### Open Voltage Protection

The features internal automatic over-voltage protection, when feedback voltage is higher than 115%.

### Open Protection

Once the outputs achieve regulation, if the voltage at FB falls below 0.2V the controller will automatically disable, preventing the controller from running open-loop and potentially damaging the IC and load.

### Shutdown

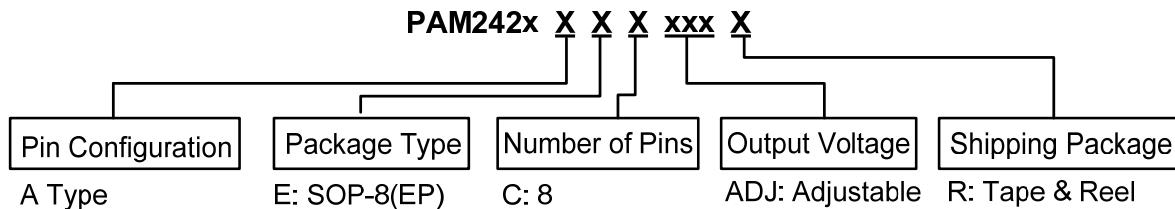
Drive EN low to disable the IC and reduce the supply current to just 0.1 $\mu$ A. As with all nonsynchronous step-up DC/DC converters, the external Schottky diode provides a DC path from the input to the output in shutdown. As a result, the output drops to one diode voltage drop below the input in shutdown.

The PAM242x both feature integrated thermal overload protection. Both devices are automatically disabled when their junction temperatures exceed +160°C, and automatically re-enable when the die temperature decreases by 40°C.

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## Ordering Information

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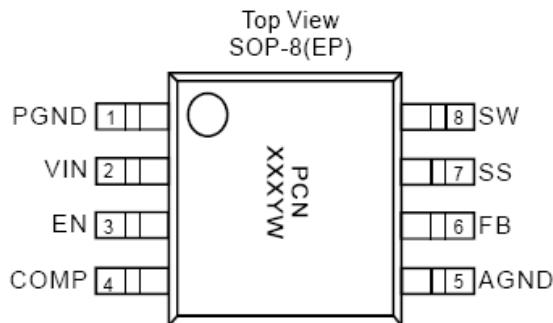


Part Number	Peak Current	Package Type	Standard Package
PAM2421AECADJR	3A	SOP-8(EP)	2500 Units/Tape & Reel
PAM2422AECADJR	4.5A	SOP-8(EP)	2500 Units/Tape & Reel
PAM2423AECADJR	5.5A	SOP-8(EP)	2500 Units/Tape & Reel

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## Marking Information

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PCN: Product Code of PAM242x

XXX: Internal Code

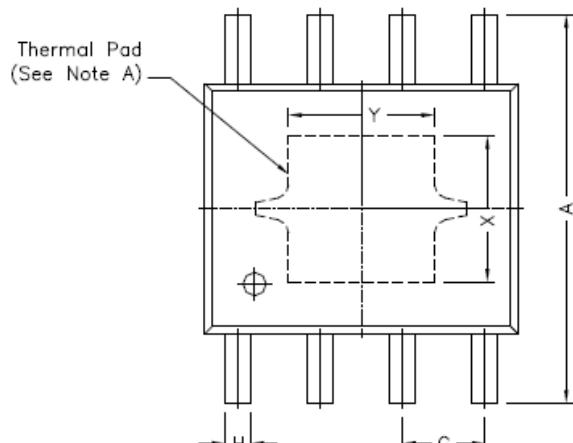
V: Voltage Code

Y: Year

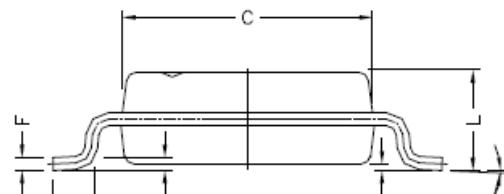
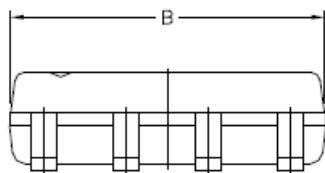
W: Week

**Package Outline Dimensions** (cont.) (All dimensions in mm.)

SOP-8(EP)



REF.	DIMENSIONS	
	Millimeters	
	Min.	Max.
A	5.80	6.20
B	4.80	5.00
C	3.80	4.00
D	0°	8°
E	0.40	0.90
F	0.19	0.25
M	0	0.15
H	0.35	0.49
L	1.35	1.75
G	1.27 TYP.	
Option1	X	2.28
	Y	2.28
Option2	X	2.41
	Y	3.30



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