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PC410L0NIP0F Series

High Speed 10Mb/s, High CMR Mini-flat Package *OPIC Photocoupler



■ Description

PC410L0NIP0F Series contains a LED optically coupled to an OPIC chip.

It is packaged in a 5 pin mini-flat.

Input-output isolation voltage(rms) is 3.75 kV.

High speed response(TYP. 10 Mb/s) and CMR is MIN. 10 kV/ μ s.

■ Features

- 1. 5 pin Mini-flat package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. High noise immunity due to high instantaneous common mode rejection voltage (CM_H : MIN.10 kV/ μ s, CM_L : MIN. –10 kV/ μ s)
- 4. High speed response

(t_{PHL}: TYP. 48 ns, t_{PLH}: TYP. 50 ns)

- 5. High isolation voltage between input and output $(V_{iso(rms)}: 3.75 \text{ kV})$
- 6. Lead-free and RoHS directive compliant

■ Agency approvals/Compliance

- Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. PC410L)
- 2. Approved by VDE, DIN EN60747-5-2^(*) (as an option), file No. 40009162 (as model No. **PC410L**)
- 3. Package resin: UL flammability grade (94V-0)

(*) DIN EN60747-5-2: successor standard of DIN VDE0884

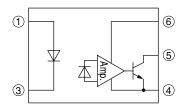
■ Applications

- 1. Programmable controllers
- 2. Inverter

^{* &}quot;OPIC"(Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and a signal-processing circuit integrated onto a single chip.



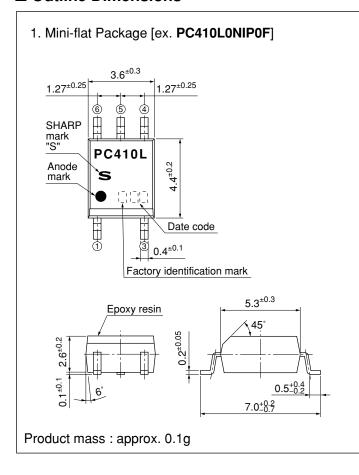
■ Internal Connection Diagram

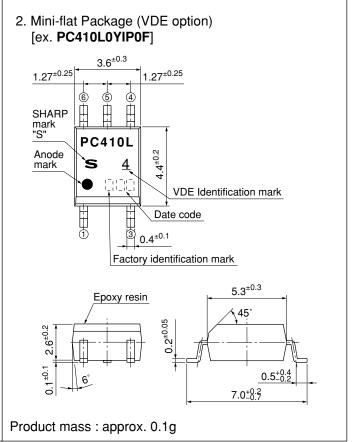


- 1 Anode
- 3 Cathode
- 4 GND
- ⑤ V_O (Open collector)
- $^{\circ}$ V_{CC}

■ Outline Dimensions

(Unit: mm)





Plating material: SnCu (Cu: TYP. 2%)



Date code (2 digit)

1st digit				2nd digit		
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin	
no mark	T	
	Japan	
	Indonesia	
_	China	

^{*} This factory marking is for identification purpose only. Please contact the local SHARP sales representative to see the actual status of the production.

Rank mark

There is no rank mark indicator.



■ Absolute Maximum Ratings

	Absolute Maximum Ratings $(T_a=25^{\circ}C)$					
	Parameter	Symbol	Rating	Unit		
_	Forward current	I_F	20	mA		
Input	Reverse voltage	V_R	5	V		
_	Power dissipation	P	40	mW		
	Supply voltage	V_{CC}	7	V		
Output	High level output voltage	V _{OH}	7	V		
Out	Low level output current	I_{OL}	50	mA		
	Collector power dissipation	P _C	85	mW		
Operating temperature		Topr	-40 to +85	°C		
Storage temperature		T_{stg}	-40 to +125	°C		
*1 Isolation voltage		V _{iso (rms)}	3.75	kV		
*2 Soldering temperature		T _{sol}	260	°C		

^{*1 40} to 60%RH, AC for 1minute, f=60Hz

■ Electro-optical Characteristics

(unless otherwise specified T_a=-40 to 85°C)

	Parameter			Conditions		MIN.	TYP.	MAX.	Unit
	Input Reverse current		V_F	$T_a=25^\circ$	$C, I_F=10mA$	-	1.6	1.9	V
Input			I_R	T _a =25	$5^{\circ}\text{C}, \text{V}_{\text{R}}=5\text{V}$	_	_	10	μΑ
	Tei	rminal capacitance	C_{t}	$T_a=25$ °C, V=0, f=1MHz		-	60	150	pF
	Lo	w level output voltage	V_{OL}	I_{OL} =13mA, V_{CC} =5.5V, I_{F} =5mA		-	0.4	0.6	V
Output	Hig	gh level output current	I_{OH}	$V_{CC}=V_{O}=$	5.5V, I _F =250μA	-	0.02	100	μΑ
Output	Lo	w level supply current	I_{CCL}	V _{CC} =5	$5V, I_F=10mA$	-	7	13	mA
	Hig	gh level supply current	I_{CCH}	V_{CC} =5.5V, I_{F} =0		_	5	10	mA
	"High→Low" input threshold current		I_{FHL}	$V_{CC}=5V, V_{O}=0.8V, R_{L}=350\Omega$		_	2.5	5	mA
	Isolation resistance		R_{ISO}	T _a =25°C, DC500V, 40 to 60%RH		5×10 ¹⁰	10^{11}	_	Ω
	Floating capacitance		C_{f}	$T_a=25$ °C, V=0, f=1MHz		-	0.6	-	pF
	"High→Low" propagation delay time "Low→High" propagation delay time *3 Distortion of pulse width Rise time		t_{PHL}	T_a =25°C, V_{CC} =5V, I_F =7.5mA,		25	48	75	ns
Transfer			t_{PLH}			25	50	75	ns
charac-			$\Delta t_{\rm w}$			_	-	35	ns
teristics	esb	Rise time	t_r	$R_L=350\Omega$, $C_L=15pF$,		_	20	_	ns
	8	Fall time					10	_	ns
reje	nstantaneous common mode ejection voltage High level output)	CM_H	$I_F = 0 \\ V_{O(MIN.)} = 2V$	$T_a=25^{\circ}\text{C}, V_{CC}=5\text{V}$ $V_{CM}=1\text{kV}_{(P,P)},$	10	20	_	kV/μs	
	Instantaneous common mode rejection voltage (Low level output)		CM_{L}	$I_F = 5mA$ $V_{O(MAX.)} = 0.8V$	$R_L=350\Omega$	-10	-20	_	kV/μs

^{*3} Distortion of pulse width $\Delta t_w = |t_{PHL} - t_{PLH}|$

^{*2} For 10s



■ Model Line-up

Doolsooo	Taping		
Package	3 000pcs/reel		
DIN EN60747-5-2		Approved	
Model No.	PC410L0NIP0F	PC410L0YIP0F	

Please contact a local SHARP sales representative to inquire about production status.

Sheet No.: D2-A04802EN



Fig.1 Test Circuit for Propagation Delay Time and Rise Time, Fall Time

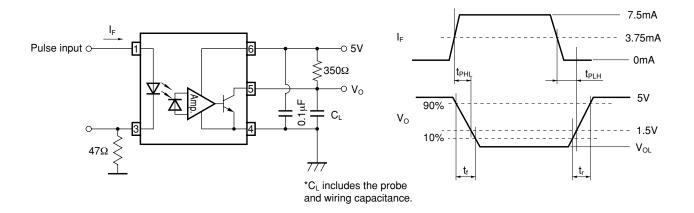


Fig.2 Test Circuit for Instantaneous Common Mode Rejection Voltage

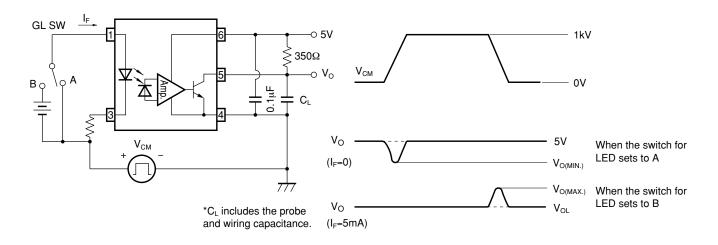


Fig.3 Forward Current vs. Ambient Temperature

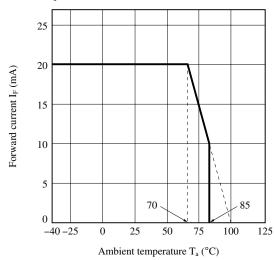


Fig.4 Collector Power Dissipation vs. Ambient Temperature

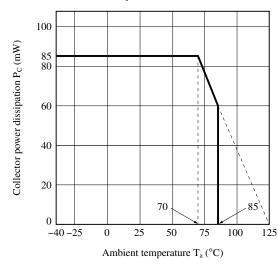




Fig.5 Forward Current vs. Forward Voltage

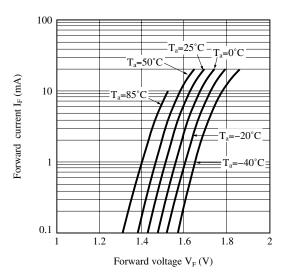


Fig.7 Low Level Output Voltage vs.
Ambient Temperature

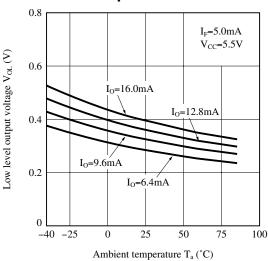


Fig.9 Input Threshold Current vs.
Ambient Temperature

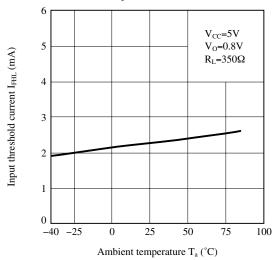


Fig.6 High Level Output Current vs.
Ambient Temperature

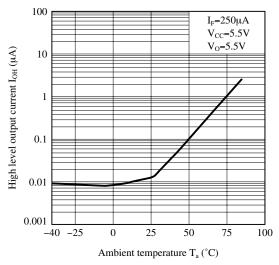


Fig.8 Output Voltage vs. Forward Current

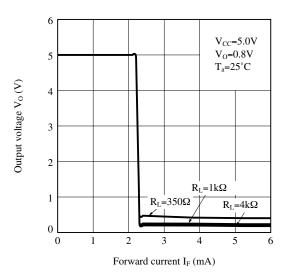


Fig.10 Propagation Delay time vs. Forward Current

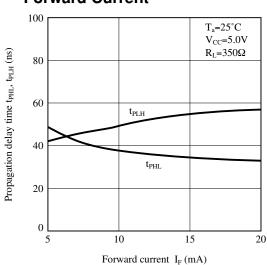
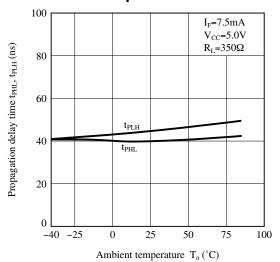




Fig.11 Propagation Delay Time vs. Ambient Temperature



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Recommended operating conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low level input current	I_{FL}	0	-	250	μΑ
High level input current	I_{FH}	8	_	15	mA
Supply voltage	V_{CC}	4.5	_	5.5	V
Fan out (TTL load)	N	_	_	5	_
Operating temperature	Topr	-40	-	+70	°C

Notes about static electricity

Transistor of detector side in bipolar configuration may be damaged by static electricity due to its minute design.

When handling these devices, general countermeasure against static electricity should be taken to avoid breakdown of devices or degradation of characteristics.

Design guide

In order to stabilize power supply line, we should certainly recommend to connect a by-pass capacitor of $0.01\mu F$ or more between V_{CC} and GND near the device.

In case that some sudden big noise caused by voltage variation is provided between primary and secondary terminals of photocoupler some current caused by it is floating capacitance may be generated and result in false operation since current may go through LED or current may change.

If the photocoupler may be used under the circumstances where noise will be generated we recommend to use the bypass capacitors at the both ends of LED.

The detector which is used in this device, has parasitic diode between each pins and GND.

There are cases that miss operation or destruction possibly may be occurred if electric potential of any pin becomes below GND level even for instant.

Therefore it shall be recommended to design the circuit that electric potential of any pin does not become below GND level.

This product is not designed against irradiation and incorporates non-coherent LED.

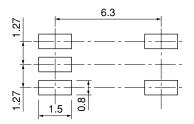
Degradation

In general, the emission of the LED used in photocouplers will degrade over time.

In the case of long term operation, please take the general LED degradation (50% degradation over 5 years) into the design consideration.

Please decide the input current which become 2 times of MAX. I_{FHL}.

Recommended Foot Print (reference)



(Unit: mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

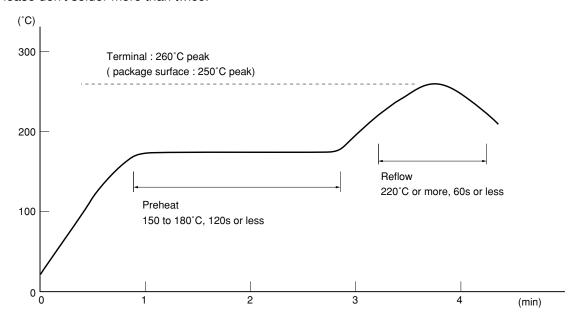
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3 minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

•Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).



■ Package specification

● Tape and Reel package

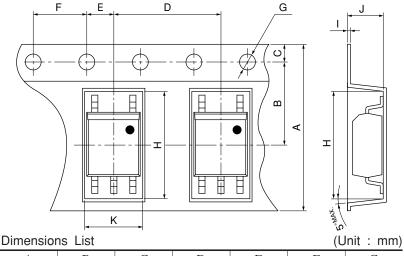
Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

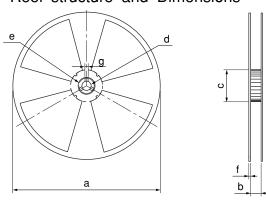
Reel: PS

Carrier tape structure and Dimensions



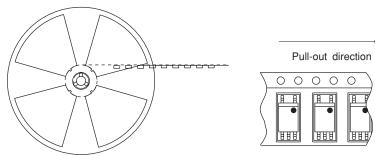
A	В	С	D	Е	F	G
12.0 ^{±0.3}	5.5 ^{±0.1}	1.75 ^{±0.1}	8.0 ^{±0.1}	2.0 ^{±0.1}	4.0 ^{±0.1}	φ1.5 + 8.1
Н	I	J	K			
7.4 ^{±0.1}	0.3 ^{±0.05}	3.1 ^{±0.1}	4.0 ^{±0.1}			

Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	c	d	
370	13.5 ^{±1.5}	80 ^{±1.0}	13 ^{±0.5}	
e	f	g		
21 ^{±1.0}	2.0 ^{±0.5}	2.0 ^{±0.5}		

Direction of product insertion



[Packing: 3 000pcs/reel]



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 - --- Consumer electronics
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- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
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[E250] Sheet No.: D2-A04802EN