Chipsmall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

Contact us

SHARP

PC910L0NSZ0F Series

High Speed 10Mb/s, High CMR DIP 8 pin *OPIC Photocoupler
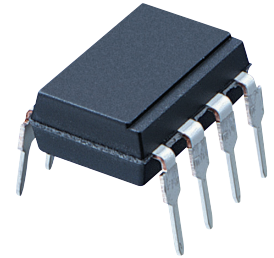
■ Description

PC910L0NSZ0F Series contains a LED optically coupled to an OPIC chip.

It is packaged in a 8 pin DIP.

Input-output isolation voltage(rms) is 5.0kV, High speed response (TYP. 10 Mb/s) and CMR is MIN. 10 kV/μs.

■ Features

1. DIP 8 pin package
2. Double transfer mold package
   (Ideal for Flow Soldering)
3. High speed response
   (t_{PHL} : TYP. 48 ns, t_{PLH} : TYP. 50 ns)
4. Low input current drive (I_{FHL} : MAX. 5 mA)
5. Instantaneous common mode rejection voltage
   (CM_H : MIN.10 kV/μs, CM_L : MIN. −10 kV/μs)
6. TTL and LSTTL compatible output
7. High isolation voltage between input and output
   ($V_{iso(rms)}$: 5.0 kV)
8. Lead-free and RoHS directive compliant

■ Agency approvals/Compliance

1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC910L**)
2. Approved by VDE, DIN EN60747-5-2[*] (as an option), file No. 40008898 (as model No. **PC910L**)
3. Package resin : UL flammability grade (94V-0)

[*]DIN EN60747-5-2 : successor standard of DIN VDE0884
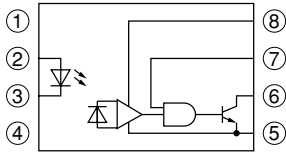
■ Applications

1. High speed interfaces for computer peripherals
2. Programmable controllers
3. Inverter

* "OPIC"(Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and a signal-processing circuit integrated onto a single chip.

■ Internal Connection Diagram

① NC   ⑤ GND
② Anode   ⑥ V_O (Open collector)
③ Cathode   ⑦ V_E (Enable)
④ NC   ⑧ V_{CC}

■ Truth table

Input	Enable	Output
H	H	L
L	H	H
H	L	H
L	L	H

L : Logic (0)
H : Logic (1)

■ Outline Dimensions

(Unit : mm)

1. Through-Hole [ex. **PC910L0NSZ0F**]

1.2$^{±0.3}$
0.85$^{±0.2}$

SHARP mark "S"

P C 9 1 0 L

6.5$^{±0.5}$

9.66$^{±0.5}$

Primary side mark   Date code

3.5$^{±0.5}$
0.5$^{TYP.}$
3.4$^{±0.5}$
2.54$^{±0.25}$
0.5$^{±0.1}$

7.62$^{±0.3}$

Epoxy resin
0.26$^{±0.1}$
θ
θ:0 to 13˚

Product mass : approx. 0.55g

2. Through-Hole (VDE option) [ex. **PC910L0YSZ0F**]

1.2$^{±0.3}$
0.85$^{±0.2}$

SHARP mark "S"

P C 9 1 0 L
4

6.5$^{±0.5}$

9.66$^{±0.5}$

VDE Identification mark

Primary side mark   Date code

3.5$^{±0.5}$
0.5$^{TYP.}$
3.4$^{±0.5}$
2.54$^{±0.25}$
0.5$^{±0.1}$

7.62$^{±0.3}$

Epoxy resin
0.26$^{±0.1}$
θ
θ:0 to 13˚

Product mass : approx. 0.55g

Plating material : SnCu (Cu : TYP. 2%)

Date code (2 digit)

1st digit				2nd digit	
Year of production				Month of production	
A.D.	Mark	A.D	Mark	Month	Mark
1990	A	2002	P	January	1
1991	B	2003	R	February	2
1992	C	2004	S	March	3
1993	D	2005	T	April	4
1994	E	2006	U	May	5
1995	F	2007	V	June	6
1996	H	2008	W	July	7
1997	J	2009	X	August	8
1998	K	2010	A	September	9
1999	L	2011	B	October	O
2000	M	2012	C	November	N
2001	N	⋮	⋮	December	D

repeats in a 20 year cycle

Country of origin
Japan

Rank mark
There is no rank mark indicator.

■ Absolute Maximum Ratings

(T$_a$=25°C)

	Parameter	Symbol	Rating	Unit
Input	Forward current	I$_F$	20	mA
	Reverse voltage	V$_R$	5	V
	Power dissipation	P	40	mW
Output	Supply voltage	V$_{CC}$	7	V
	*1 Enable voltage	V$_E$	5.5	V
	High level output voltage	V$_{OH}$	7	V
	Low level output current	I$_{OL}$	50	mA
	Collector power dissipation	P$_C$	85	mW
	Operating temperature	T$_{opr}$	−40 to +85	°C
	Storage temperature	T$_{stg}$	−55 to +125	°C
*2	Isolation voltage	V$_{iso (rms)}$	5.0	kV
*3	Soldering temperature	T$_{sol}$	270	°C

*1 Shall not exceed 500mV from supply voltage (V$_{CC}$)
*2 40 to 60%RH, AC for 1minute, f=60Hz
*3 For 10s

■ Electro-optical Characteristics*4

(unles otherwise specified T$_a$=−40 to 85°C)

	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage	V$_F$	T$_a$=25°C, I$_F$=10mA	–	1.6	1.9	V
	Reverse current	I$_R$	T$_a$=25°C, V$_R$=5V	–	–	10	μA
	Terminal capacitance	C$_t$	T$_a$=25°C, V=0, f=1MHz	–	60	150	pF
Output	High level output voltage	I$_{OH}$	V$_{CC}$=V$_O$=5.5V, V$_E$=2.0V, I$_F$=250μA	–	0.02	100	μA
	Low level output voltage	V$_{OL}$	V$_{CC}$=5.5V, V$_E$=2.0V, I$_F$=5mA, I$_{OL}$=13mA	–	0.4	0.6	V
	High level enable current	I$_{EH}$	V$_{CC}$=5.5V, V$_E$=2.0V	–	−0.5	−1.6	mA
	Low level enable current	I$_{EL}$	V$_{CC}$=5.5V, V$_E$=0.5V	–	−0.7	−1.6	mA
	High level supply current	I$_{CCH}$	V$_{CC}$=5.5V, V$_E$=2.0V, I$_F$=0	–	5	10	mA
			V$_{CC}$=5.5V, V$_E$=0.5V, I$_F$=0	–	5	–	mA
	Low level supply current	I$_{CCL}$	V$_{CC}$=5.5V, V$_E$=2.0V, I$_F$=10mA	–	7	13	mA
			V$_{CC}$=5.5V, V$_E$=0.5V, I$_F$=10mA	–	5.5	–	mA
Transfer characteristics	"High→Low" input threshold current	I$_{FHL}$	V$_{CC}$=5V, V$_E$=2.0V, V$_O$=0.8V, R$_L$=350Ω	–	2.5	5	mA
	Isolation resistance	R$_{ISO}$	T$_a$=25°C, DC500V, 40 to 60%RH	5×10^{10}	10^{11}	–	Ω
	Floating capacitance	C$_f$	T$_a$=25°C, V=0, f=1MHz	–	0.6	5	pF
	Response time — "High→Low" propagation delay time	t$_{PHL}$	T$_a$=25°C, I$_F$=7.5mA, V$_{CC}$=5V, R$_L$=350Ω, C$_L$=15pF,	25	48	75	ns
	"Low→High" propagation delay time	t$_{PLH}$		25	50	75	ns
	Rise time	t$_r$		–	10	–	ns
	Fall time	t$_f$		–	20	–	ns
	*5 Distortion of pulse width	Δt$_w$		–	–	35	ns
	"High→Low" enable propagation delay time	t$_{EHL}$	T$_a$=25°C, I$_F$=7.5mA, V$_{CC}$=5V, R$_L$=350Ω, C$_L$=15pF, V$_{EH}$=3V V$_{EL}$=0.5V	–	15	–	ns
	"Low→High" enable propagation delay time	t$_{ELH}$		–	10	–	ns
	Instantaneous common mode rejection voltage (High level output)	CM$_H$	T$_a$=25°C, I$_F$=0, V$_{CC}$=5V, V$_{CM}$=1kV$_{(P-P)}$, R$_L$=350Ω, V$_{O (Min)}$=2V	10	20	–	kV/μs
	Instantaneous common mode rejection voltage (Low level output)	CM$_L$	T$_a$=25°C, I$_F$=5mA, V$_{CC}$=5V, V$_{CM}$=1kV$_{(P-P)}$, R$_L$=350Ω, V$_{O (Max)}$=0.8V	−10	−20	–	kV/μs

*4 It shall connect a by-pass capacitor of 0.01μF or more between V$_{CC}$ (pin ⑧) and GND (pin ⑤) near the device, when it measures the transfer characteristics and the output side characteristics
*5 Distortion of pulse width Δt$_w$= | t$_{PHL}$−t$_{PLH}$ |

■ Model Line-up

Lead Form	Through-Hole		SMT Gullwing	
Package	Sleeve		Taping	
	50pcs/sleeve		1 000pcs/reel	
DIN EN60747-5-2	———	Approved	———	Approved
Model No.	**PC910L0NSZ0F**	**PC910L0YSZ0F**	———	**PC910L0YIP0F**

Please contact a local SHARP sales representative to inquire about production status.

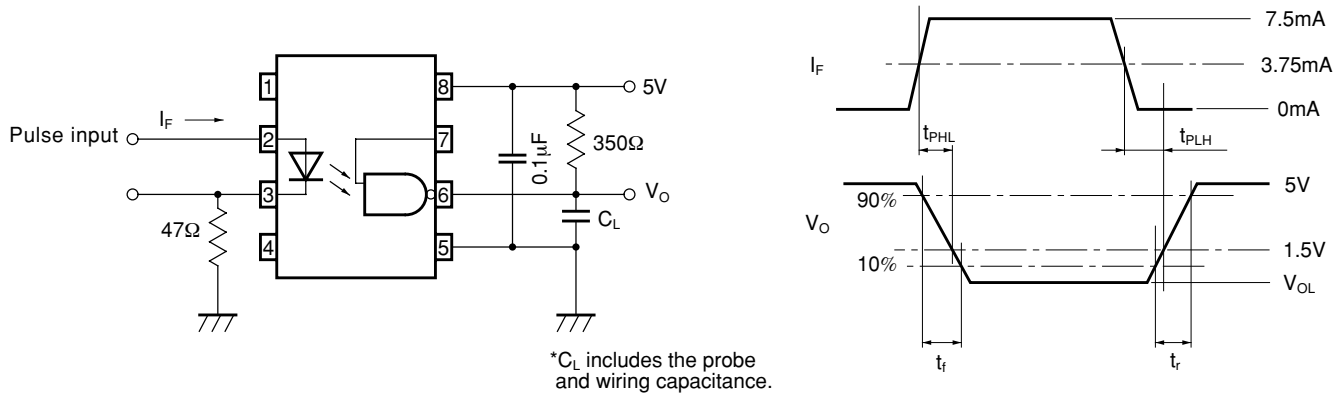## Fig.1 Test Circuit for Propagation Delay Time and Rise Time, Fall Time

*C_L includes the probe
and wiring capacitance.

Fig.2 Test Circuit for Enable Propagation Delay Time

*C_L includes the probe
and wiring capacitance.
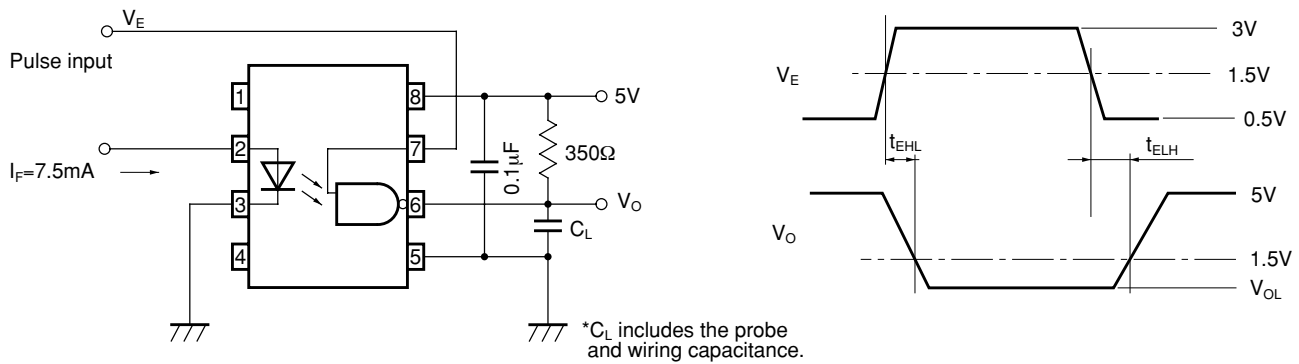
Fig.3 Test Circuit for Instantaneous Common Mode Rejection Voltage

*C_L includes the probe and wiring capacitance.
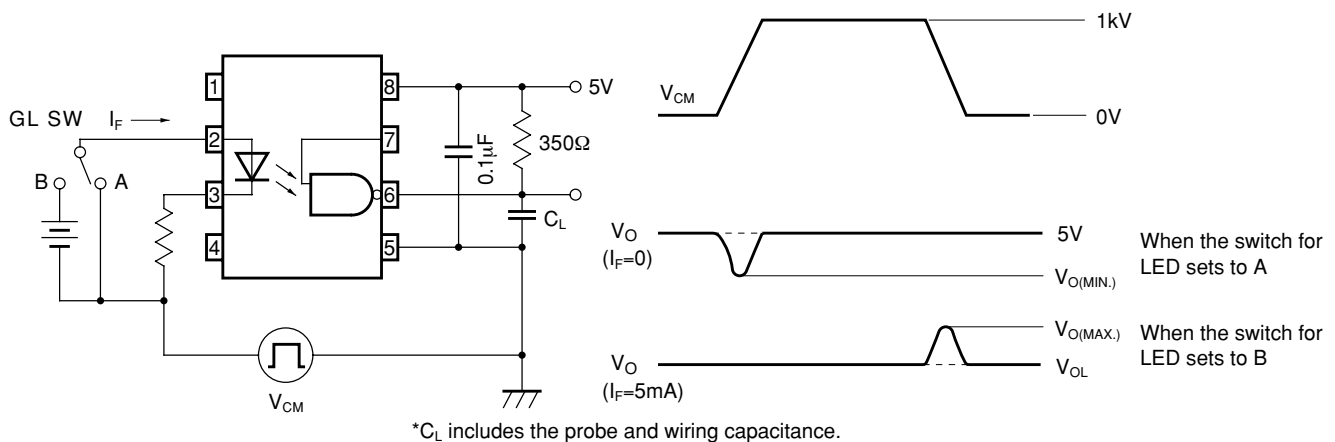
Fig.4 Forward Current vs. Ambient Temperature

Forward current I_F (mA) vs Ambient temperature T_a (°C)
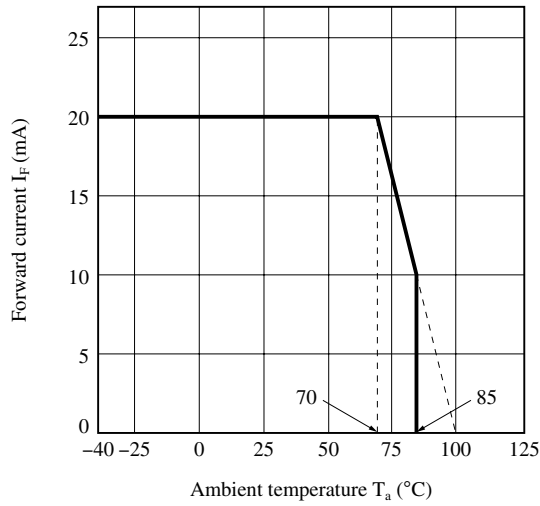
Fig.5 Collector Power Dissipation vs. Ambient Temperature

Collector power dissipation P_C (mW) vs Ambient temperature T_a (°C)
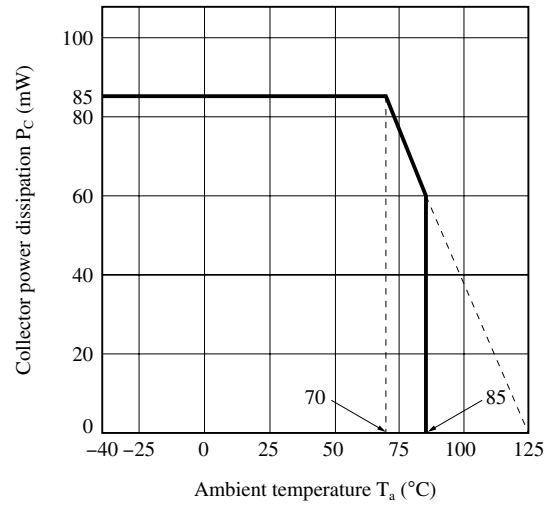
Fig.6 Forward Current vs. Forward Voltage

Forward current I_F (mA) vs Forward voltage V_F (V)
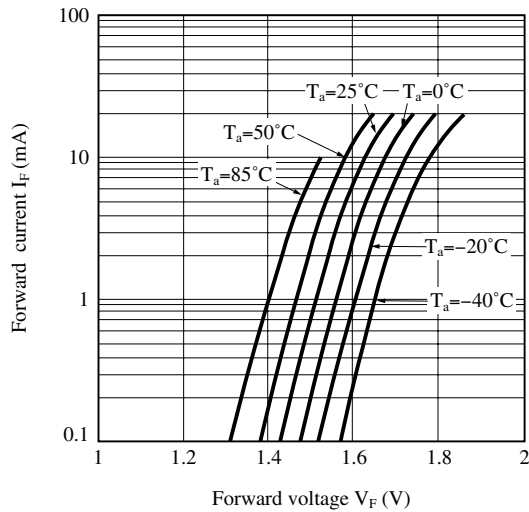
Fig.7 High Level Output Current vs. Ambient Temperature

High level output current I_{OH} (µA) vs Ambient temperature T_a (°C)

$I_F=250µA$
$V_{CC}=5.5V$
$V_O=5.5V$
$V_E=2.0V$
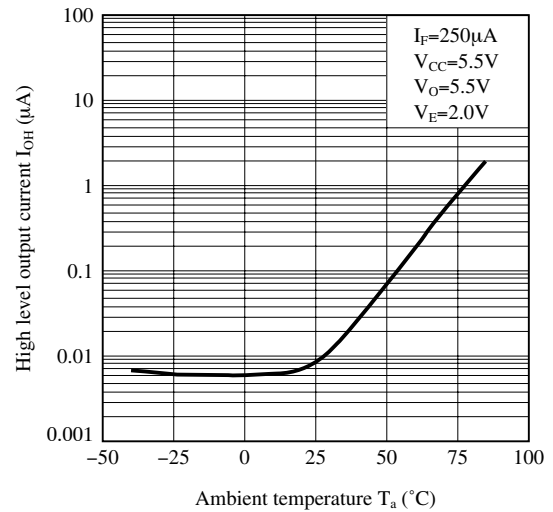
Fig.8 Low Level Output Voltage vs. Ambient Temperature

Low level output voltage V_{OL} (V) vs Ambient temperature T_a (°C)

$I_F=5.0mA$
$V_{CC}=5.5V$
$V_E=2.0V$
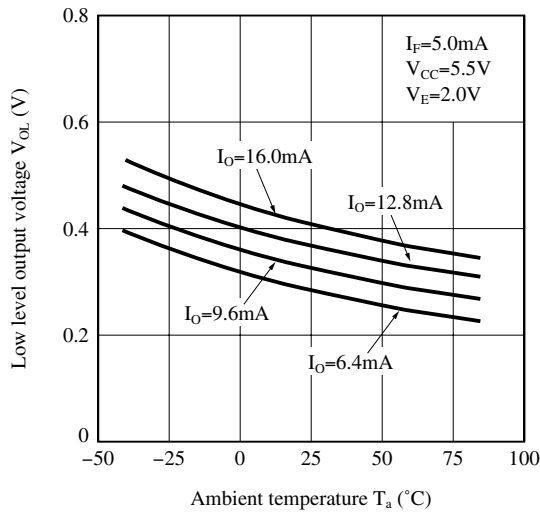
Fig.9 Input Threshold Current vs. Ambient Temperature

Input threshold current I_{FHL} (mA) vs Ambient temperature T_a (°C)

$V_{CC}=5V$
$V_O=0.8V$
$V_E=2.0V$
$R_L=350Ω$
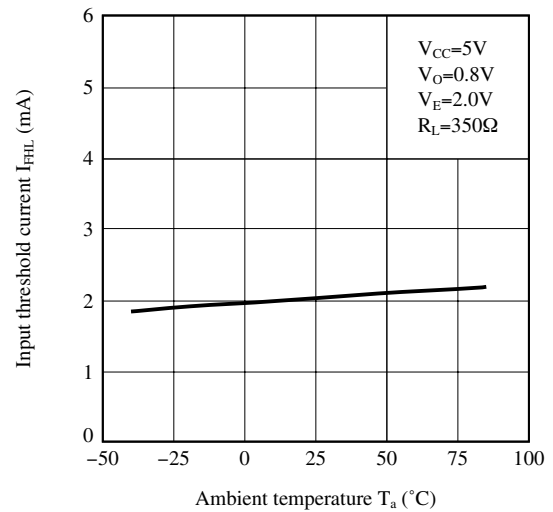
Fig.10 Output Voltage vs. Forward Current

Conditions in graph:
$V_{CC}=5.0V$
$V_O=0.8V$
$V_E=2.0V$
$T_a=25°C$

Output voltage V_O (V)

Forward current I_F (mA)

$R_L=1kΩ$
$R_L=350Ω$
$R_L=4kΩ$
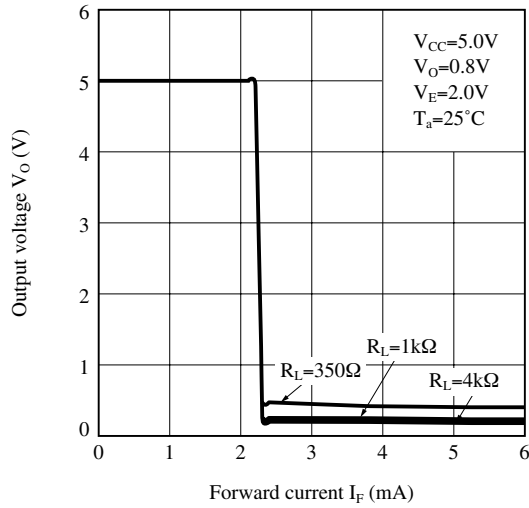
Fig.11 Propagation Delay Time vs. Forward Current

Conditions in graph:
$T_a=25°C$
$V_{CC}=5.0V$
$R_L=350Ω$

Propagation delay time t_{PHL}, t_{PLH} (ns)

Forward current I_F (mA)

t_{PLH}
t_{PHL}
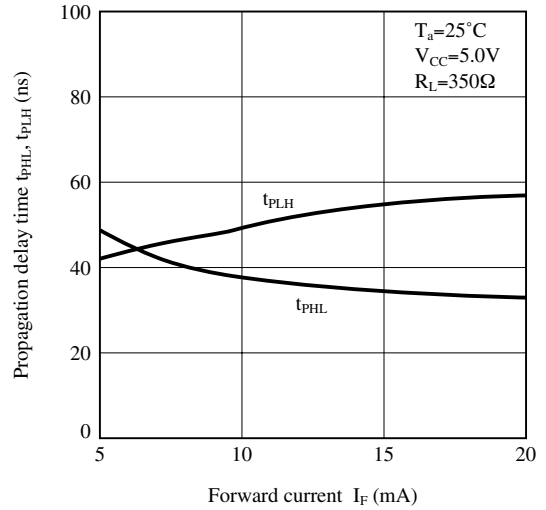
Fig.12 Propagation Delay Time vs. Ambient Temperature

Conditions in graph:
$I_F=7.5mA$
$V_{CC}=5.0V$
$R_L=350Ω$

Propagation delay time t_{PHL}, t_{PLH} (ns)

Ambient temperature T_a (°C)

t_{PLH}
t_{PHL}
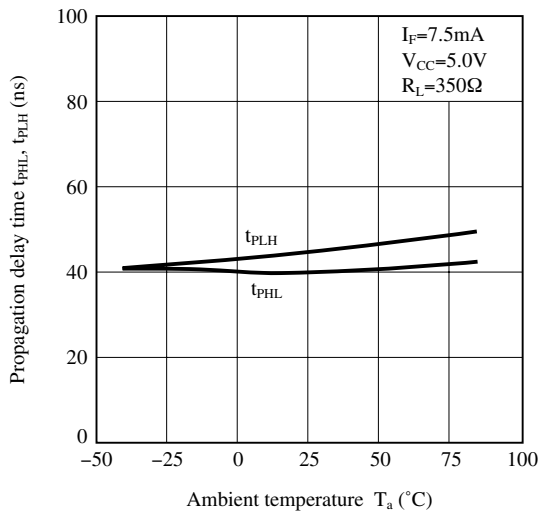
Remarks : Please be aware that all data in the graph are just for reference and not for guarantee.

■ Design Considerations

● Recommended operating conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low level input current	I_{FL}	0	–	250	μA
High level input current	I_{FH}	8	–	15	mA
High level enable input voltage	V_{EH}	2.0	–	V_{CC}	V
Low level enable input voltage	V_{EL}	0	–	0.8	V
Supply voltage	V_{CC}	4.5	–	5.5	V
Fan out (TTL load)	N	–	–	8	–
Operating temperature	T_{opr}	–40	–	70	°C

● Notes about static electricity

Transistor of detector side in bipolar configuration may be damaged by static electricity due to its minute design.

When handling these devices, general countermeasure against static electricity should be taken to avoid breakdown of devices or degradation of characteristics.

● Design guide

In order to stabilize power supply line, we should certainly recommend to connect a by-pass capacitor of 0.01μF or more between V_{CC} and GND near the device.

In case that some sudden big noise caused by voltage variation is provided between primary and secondary terminals of photocoupler some current caused by it is floating capacitance may be generated and result in false operation since current may go through LED or current may change.

If the photocoupler may be used under the circumstances where noise will be generated we recommend to use the bypass capacitors at the both ends of LED.

The detector which is used in this device, has parasitic diode between each pins and GND.

There are cases that miss operation or destruction possibly may be occurred if electric potential of any pin becomes below GND level even for instant.

Therefore it shall be recommended to design the circuit that electric potential of any pin does not become below GND level.

This product is not designed against irradiation and incorporates non-coherent LED.

● Degradation

In general, the emission of the LED used in photocouplers will degrade over time.

In the case of long term operation, please take the general LED degradation (50% degradation over 5 years) into the design consideration.

Please decide the input current which become 2 times of MAX. I_{FHL}.

☆ For additional design assistance, please review our corresponding Optoelectronic Application Notes.

■ Manufacturing Guidelines
● Soldering Method
Flow Soldering :

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.
Preheating is within the bounds of 100 to 150°C and 30 to 80s.
Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.
Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.

● Cleaning instructions
Solvent cleaning:
Solvent temperature should be 45°C or below Immersion time should be 3 minutes or less

Ultrasonic cleaning:
The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.
Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:
Ethyl alcohol, Methyl alcohol and Isopropyl alcohol
In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

● Presence of ODC
This product shall not contain the following materials.
And they are not used in the production process for this product.
Regulation substances : CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).
• Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).

■ Package specification

● Sleeve package

Package materials

Sleeve : HIPS (with anti-static material)

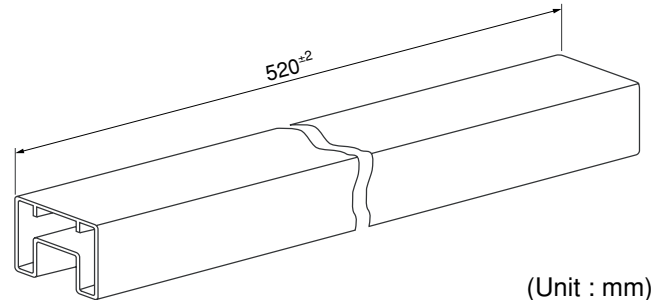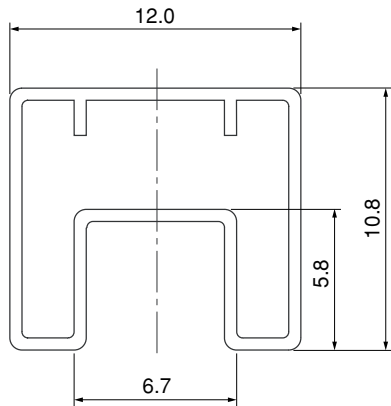Stopper : Styrene-Elastomer

Package method

MAX. 50 pcs. of products shall be packaged in a sleeve.

Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions

12.0

10.8

5.8

6.7

$520^{\pm 2}$

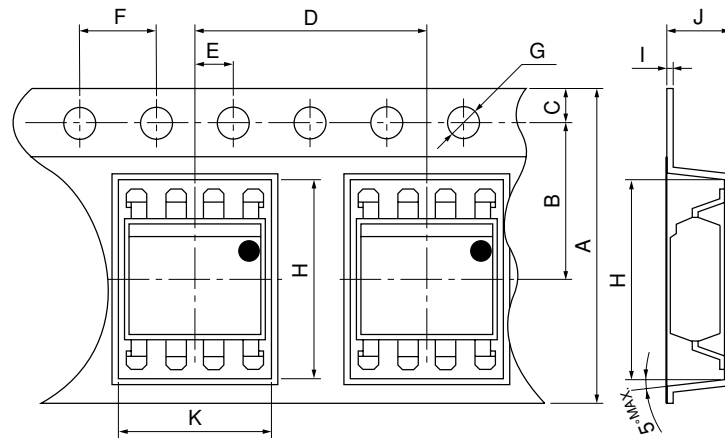(Unit : mm)

● **Tape and Reel package**

Package materials

  Carrier tape : A-PET (with anti-static material)
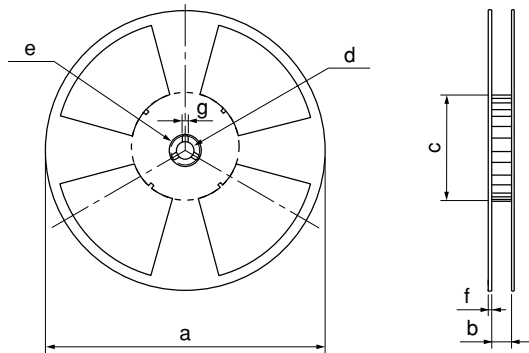
  Cover tape : PET (three layer system)

  Reel : PS

Carrier tape structure and Dimensions

Dimensions List   (Unit : mm)

A	B	C	D	E	F	G
$16.0^{\pm0.3}$	$7.5^{\pm0.1}$	$1.75^{\pm0.1}$	$12.0^{\pm0.1}$	$2.0^{\pm0.1}$	$4.0^{\pm0.1}$	$\phi1.5^{+0.1}_{-0}$
H	I	J	K			
$10.4^{\pm0.1}$	$0.4^{\pm0.05}$	$4.2^{\pm0.1}$	$10.2^{\pm0.1}$			

Reel structure and Dimensions

Dimensions List   (Unit : mm)

a	b	c	d
330	$17.5^{\pm1.5}$	$100^{\pm1.0}$	$13^{\pm0.5}$
e	f	g	
$23^{\pm1.0}$	$2.0^{\pm0.5}$	$2.0^{\pm0.5}$	

Direction of product insertion

Pull-out direction
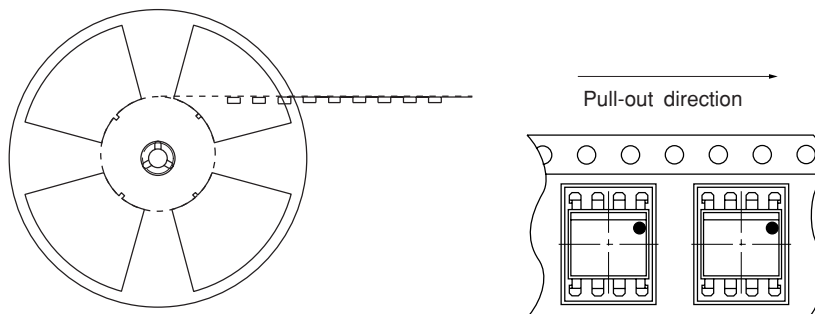
[Packing : 1 000pcs/reel]

■ Important Notices

· The circuit application examples in this publication are provided to explain representative applications of SHARP devices and are not intended to guarantee any circuit design or license any intellectual property rights. SHARP takes no responsibility for any problems related to any intellectual property right of a third party resulting from the use of SHARP's devices.

· Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device. SHARP reserves the right to make changes in the specifications, characteristics, data, materials, structure, and other contents described herein at any time without notice in order to improve design or reliability. Manufacturing locations are also subject to change without notice.

· Observe the following points when using any devices in this publication. SHARP takes no responsibility for damage caused by improper use of the devices which does not meet the conditions and absolute maximum ratings to be used specified in the relevant specification sheet nor meet the following conditions:
(i) The devices in this publication are designed for use in general electronic equipment designs such as:
--- Personal computers
--- Office automation equipment
--- Telecommunication equipment [terminal]
--- Test and measurement equipment
--- Industrial control
--- Audio visual equipment
--- Consumer electronics
(ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection with equipment that requires higher reliability such as:
--- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
--- Traffic signals
--- Gas leakage sensor breakers
--- Alarm equipment
--- Various safety devices, etc.
(iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:
--- Space applications
--- Telecommunication equipment [trunk lines]
--- Nuclear power control equipment
--- Medical and other life support equipment (e.g., scuba).

· If the SHARP devices listed in this publication fall within the scope of strategic products described in the Foreign Exchange and Foreign Trade Law of Japan, it is necessary to obtain approval to export such SHARP devices.

· This publication is the proprietary product of SHARP and is copyrighted, with all rights reserved. Under the copyright laws, no part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, for any purpose, in whole or in part, without the express written permission of SHARP. Express written permission is also required before any use of this publication may be made by a third party.

· Contact and consult with a SHARP representative if there are any questions about the contents of this publication.