imall

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PC912L0NSZ0F Series

High Speed 25Mb/s, High CMR type DIP 8 pin *OPIC Photocoupler



Description

PC912L0NSZ0F Series contains a LED optically coupled to an OPIC chip.

It is packaged in a 8 pin DIP, available in SMT gullwing lead-form option.

Input-output isolation voltage(rms) is 5.0 kV. Data transfer rate is MAX. 25 Mb/s and CMR is MIN. 20 kV/ $\mu s.$

Features

- 1. DIP 8 pin package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. High speed response
 - $(t_{\mathsf{PHL}}:\mathsf{MAX}.\ 40ns,\ t_{\mathsf{PLH}}:\mathsf{MAX}.\ 40ns)$
- 4. High noise immunity due to high instantaneous common mode rejection voltage
 - $(CM_{H}:MIN.~20~kV/\mu s,~CM_{L}:MIN.~-20~kV/\mu s)$
- 5. High isolation voltage between input and output $(V_{iso(rms)}: 5.0 \; kV)$
- 6. Lead-free and RoHS directive compliant

Agency approvals/Compliance

- 1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC912L**)
- 2. Approved by VDE (DIN EN60747-5-2^(*)) (as an option), file No. 40008898 (as model No. **PC912L**)
- 3. Package resin : UL flammability grade (94V-0)

(*) DIN EN60747-5-2 : successor standard of DIN VDE0884

Applications

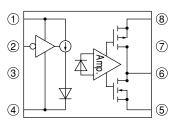
1. FA equipment

* "OPIC"(Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and a signal-processing circuit integrated onto a single chip.

Notice The content of data sheet is subject to change without prior notice. In the absence of confirmation by device specification sheets, SHARP takes no responsibility for any defects that may occur in equipment using any SHARP devices shown in catalogs, data books, etc. Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device.



Internal Connection Diagram



① V _{CC1}	5 GND2
2 V _{IN}	6 V _o
* ③ NC	⑦ NC
④ GND1	(8) V_{CC2}

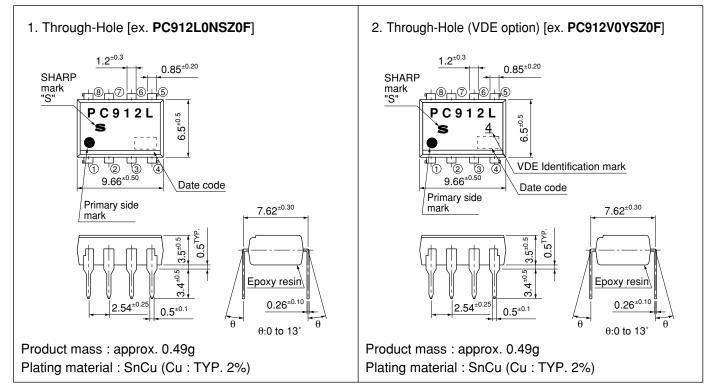
*pin (3) and (7) are not allowed external connection

■ Truth table

Input	LED	Output	
L	ON	L	L : Logic (0)
Н	OFF	Н	H : Logic (1)

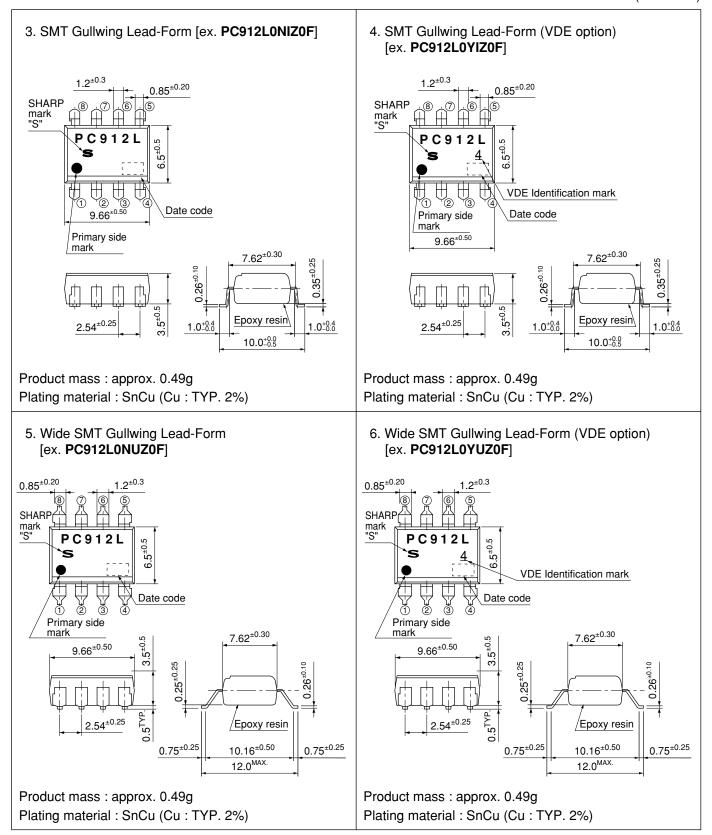
Outline Dimensions

(Unit : mm)





(Unit : mm)





Date code (2 digit)

	1st o	digit		2nd digit		
		roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	А	2002	Р	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	Т	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	Х	August	8	
1998	K	2010	А	September	9	
1999	L	2011	В	October	0	
2000	М	2012	С	November	Ν	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Country of origin

Japan

Rank mark

There is no rank mark indicator.

■ Absolute Maximum Ratings

	(Unless otherwise specified $T_a=T_{opt}$)					
	(Onless otherwise specified $T_a = T_{opr}$)					
	Parameter	Symbol	Rating	Unit		
Innut	Supply voltage	V _{CC1}	0 to 5.5	V		
Input	Input voltage	V_{IN}	-0.5 to V _{CC1} +0.5	V		
	Supply voltage	V _{CC2}	0 to 5.5	V		
Output	High level output voltage	Vo	–0.5 to V_{CC2} +0.5	V		
Low level output current		Io	10	mA		
*1 Isola	ation voltage	V _{iso (rms)}	5.0	kV		
Ope	rating temperature	T _{opr}	-40 to +85	°C		
Storage temperature		T _{stg}	-55 to +125	°C		
*2 Soldering temperature		T _{sol}	270	°C		

*1 40 to 60%RH, AC for 1minute, f=60Hz

*2 For 10s

■ Electro-optical Characteristics*3

(Unless otherw	vise specifi	ied $T_a=T_{opr}$)
----------------	--------------	---------------------

	Parameter Symbol Conditions		MIN.	TYP.	MAX.	Unit		
	Lov	v level supply current	I _{CC1L}	V _{IN} =0V	-	6.0	10.0	mA
Input			I _{CC1H}	VIN=VCCI	_	0.8	3.0	mA
1		ut current	I _{IN}	V _{CC1} =5V	-10	_	10	μA
	Hig	h level supply current	I _{CC2H}	V _{IN} =5V	-	2.5	5.0	mA
	Lov	v level supply current	I _{CC2L}	V _{IN} =0V	-	2.0	4.0	mA
	11:-	1. 1		I_{O} =-20 μ A, V_{IN} =5V	4.4	5.0	_	V
Output	Hig	h level output voltage	V _{OH}	$I_O = -4mA$, $V_{IN} = 5V$	4.0	4.8	-	V
				$I_0=20\mu A, V_{IN}=0V$	-	0	0.1	V
	Lov	v level output voltage	V _{OL}	I_{O} =400 μ A, V_{IN} =0V	-	-	0.1	V
				$I_{O}=4mA, V_{IN}=0V$	-	0.2	1.0	V
	Isolation resistance		R _{ISO}	DC500V, 40 to 60%RH	5×10 ¹⁰	1011	-	Ω
		"High→Low" propagation delay time	t _{PHL}	L		23	40	ns
	"Low \rightarrow High" propagation delay time Pulse width distortion $ t_{PHL}-t_{PLH} $		t _{PLH}	C _L =15pF, CMOS Logic level	-	22	40	ns
			Δtw	$V_{IN}=0 \rightarrow 5V$	-	-	6	ns
	esponse	Propagation delay skew	T _{PSK}	$t_r = t_f < 1ns$	_	-	20	ns
Transfer	espe	Data transfer rate	Т	Pulse width 40ns	-	_	25	Mb/s
charac-	Ř	Rise time	t _r	Duty 50%	-	4	-	ns
teristics		Fall time	t _f		-	3	_	ns
	Instantaneous common mode rejection voltage "Output : High level"	CM _H	$V_{IN}=V_{CC1}, V_O < 0.8 \times V_{CC2}$ $V_{CM}=1 k V$	20	_	_	kV/µs	
	reje	antaneous common mode ction voltage utput : Low level"	CML	V _{IN} =0, V _O <0.8V V _{CM} =1kV	-20	-	_	kV/µs

*3 When measuring output and transfer characteristics, connect a by-pass capacitor (0.01µF or more) between V_{CC1} (pin ①) and GND₁ (pin ④), between V_{CC2} (pin ⑧) and GND₂ (pin ⑤) near the device.

All typical values:at $T_a=25^{\circ}C$, $V_{CC1}=V_{CC2}=5V$



■ Model Line-up

Lead Form	Through-Hole		SMT G	ullwing	Wide SMT Gullwing		
Dealtage	Sleeve		Sleeve		Sleeve		
Package	50pcs/sleeve		50pcs/sleeve		50pcs/sleeve		
DIN EN60747-5-2		Approved		Approved		Approved	
Model No.	PC912L0NSZ0F	PC912L0YSZ0F	PC912L0NIZ0F	PC912L0YIZ0F	PC912L0NUZ0F	PC912L0YUZ0F	

Please contact a local SHARP sales representative to inquire about production status.

Fig.1 Test Circuit for Propagation Delay Time and Rise Time, Fall Time

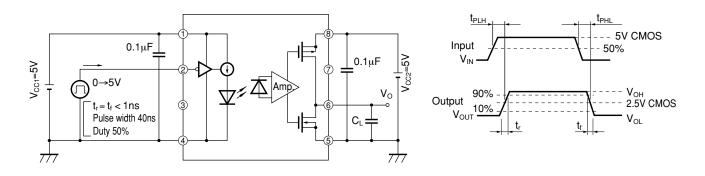
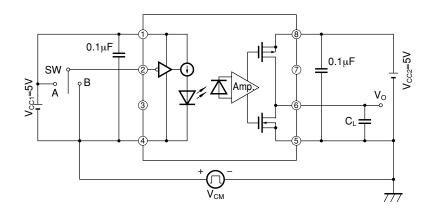


Fig.2 Test Circuit for Instantaneous Common Mode Rejection Voltage



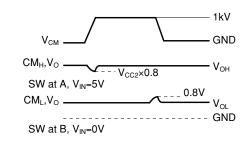


Fig.3 Output Voltage vs. Input Voltage

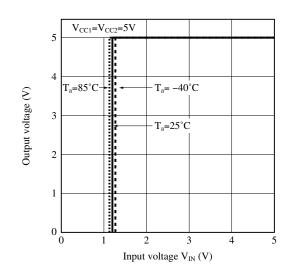


Fig.4 Input Threshold Voltage vs. Input Supply Voltage

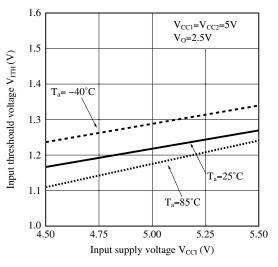
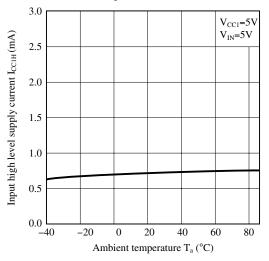
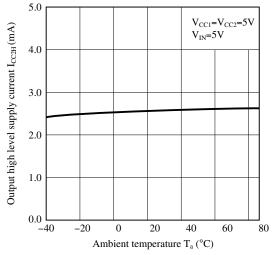




Fig.5 Input High Level Supply Current vs. Ambient Temperature









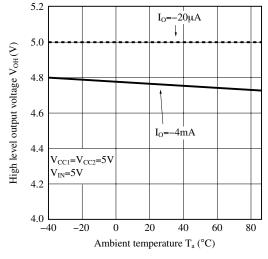


Fig.6 Input Low Level Supply Current vs. Ambient Temperature

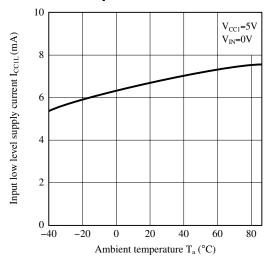


Fig.8 Output Low Level Supply Current vs. Ambient Temperature

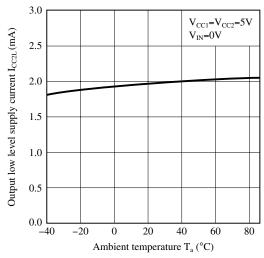


Fig.10 Low Level Output Voltage vs. Ambient Temperature

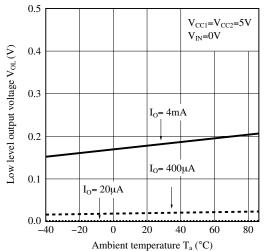
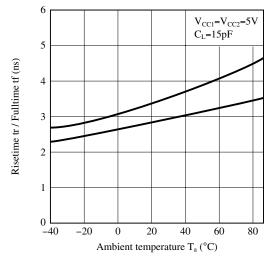
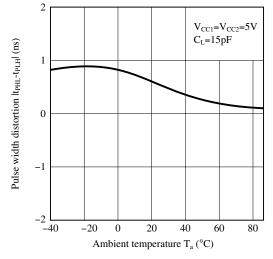




Fig.11 Rise Time/Fall Time vs. Ambient Temperature









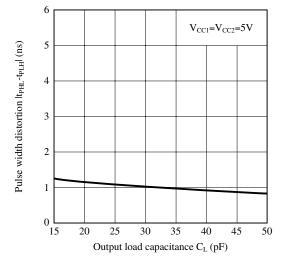


Fig.12 Propagation Delay Time vs. Ambient Temperature

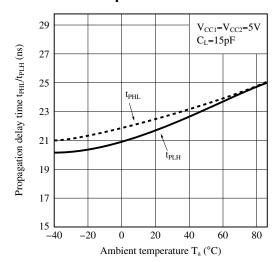
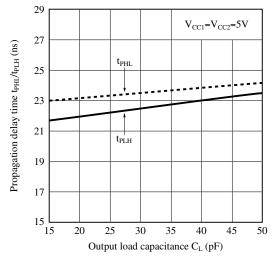


Fig.14 Propagation Delay Time vs. Output Load Capacitance



Remarks : Please be aware that all data in the graph are just for reference and not for guarantee.

Design Considerations

Recommended operating conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply voltage	V _{CC1}	4.5	-	5.5	V
Supply voltage	V _{CC2}	4.5	-	5.5	V
Low level input voltage	V _{IL}	0	-	0.8	V
High level input voltage	V _{IH}	2.0	-	V _{CC1}	V
Operating temperature	T _{opr}	-40	_	+70	°C

Notes about static electricity

Transistor of detector side in CMOS configuration may be damaged by static electricity due to its minute design.

When handling these devices, general countermeasure against static electricity should be taken to avoid breakdown of devices or degradation of characteristics.

• Design guide

In order to stabilize power supply line, we should certainly recommend to connect a by-pass capacitor of 0.01 μ F or more between V_{CC1}-GND and V_{CC2}-GND near the device.

The detector which is used in this device, has parasitic diode between each pins and GND.

There are cases that miss operation or destruction possibly may be occurred if electric potential of any pin becomes below GND level even for instant.

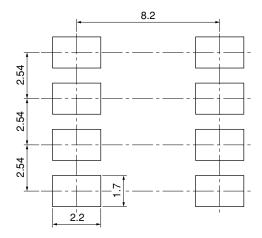
Therefore it shall be recommended to design the circuit that electric potential of any pin does not become below GND level.

This product is not designed against irradiation and incorporates non-coherent LED.

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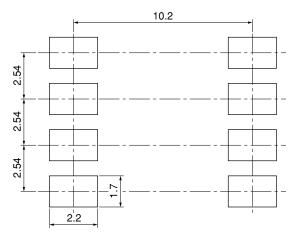
• Recommended Foot Print (reference)

SMT Gullwing Lead-form



(Unit : mm)

Wide SMT Gullwing Lead-form



(Unit : mm)

☆ For additional design assistance, please review our corresponding Optoelectronic Application Notes.

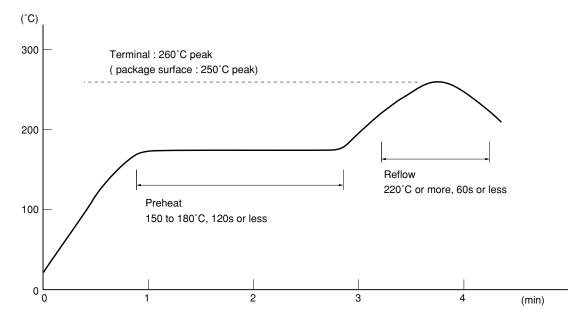


Manufacturing Guidelines

Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below. Soldering should not exceed the curve of temperature profile and time. Please don't solder more than twice.



Flow Soldering :

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s. Preheating is within the bounds of 100 to 150°C and 30 to 80s. Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C. Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



• Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials. And they are not used in the production process for this product. Regulation substances : CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBB and PBDE are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive.
Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).



Package specification

• Sleeve package

1. Through-Hole or SMT Gullwing

Package materials

Sleeve : HIPS (with anti-static material) Stopper : Styrene-Elastomer

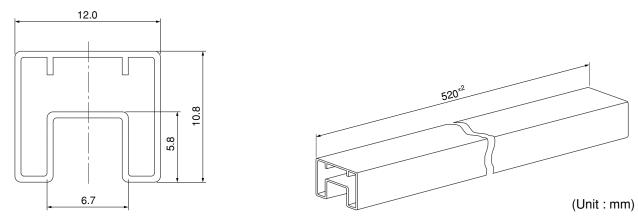
Package method

MAX. 50 pcs. of products shall be packaged in a sleeve.

Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side. MAX. 20 sleeves in one case.

Sleeve outline dimensions



2. Wide SMT Gullwing

Package materials Sleeve : HIPS (with anti-static material) Stopper : Styrene-Elastomer

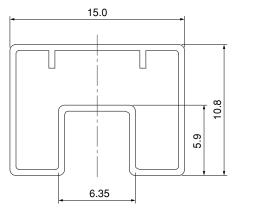
Package method

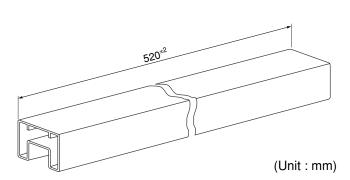
MAX. 50 pcs. of products shall be packaged in a sleeve.

Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side. MAX. 20 sleeves in one case.

Sleeve outline dimensions





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(i) The devices in this publication are designed for use in general electronic equipment designs such as:

- --- Personal computers
- --- Office automation equipment
- --- Telecommunication equipment [terminal]
- --- Test and measurement equipment
- --- Industrial control
- --- Audio visual equipment
- --- Consumer electronics

(ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection with equipment that requires higher reliability such as:

- --- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.

(iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:

- --- Space applications
- --- Telecommunication equipment [trunk lines]
- --- Nuclear power control equipment
- --- Medical and other life support equipment (e.g., scuba).

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