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# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









# PC923L0NSZ0F Series

# High Speed, Gate Drive DIP 8 pin \*OPIC Photocoupler



### ■ Description

**PC923L0NSZ0F Series** contains a LED optically coupled to an OPIC chip.

It is packaged in a 8 pin DIP, available in SMT gullwing lead form option.

Input-output isolation voltage(rms) is 5.0 kV, High speed response ( $t_{PHL}$ ,  $t_{PLH}$  : MAX. 0.5  $\mu$ s).

#### ■ Features

- 1. 8 pin DIP package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. Built-in direct drive circuit for MOSFET / IGBT drive (I<sub>O1P</sub>, I<sub>O2P</sub>: 0.6 A)
- 4. High speed response (t<sub>PHL</sub>, t<sub>PLH</sub>: MAX. 0.5 μs)
- 5. Wide operating supply voltage range (V<sub>CC</sub>=15 to 30 V)
- High noise immunity due to high instantaneous common mode rejection voltage (CM<sub>H</sub>: MIN. – 15kV/μs, CM<sub>L</sub>: MIN. 15kV/μs)
- 7. High isolation voltage between input and output  $(V_{iso(rms)}: 5.0 \text{ kV})$
- 8. Lead-free and RoHS directive compliant

### ■ Agency approvals/Compliance

- 1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC923L**)
- 2. Approved by VDE, DIN EN60747-5-2<sup>(\*)</sup> (as an option), file No. 40008898 (as model No. **PC923L**)
- 3. Package resin: UL flammability grade (94V-0)

#### ■ Applications

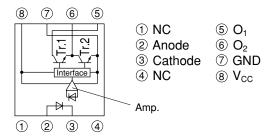
1. IGBT/MOSFET gate drive for inverter control

<sup>(\*)</sup> DIN EN60747-5-2: successor standard of DIN VDE0884

<sup>\* &</sup>quot;OPIC"(Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and a signal-processing circuit integrated onto a single chip.



## ■ Internal Connection Diagram

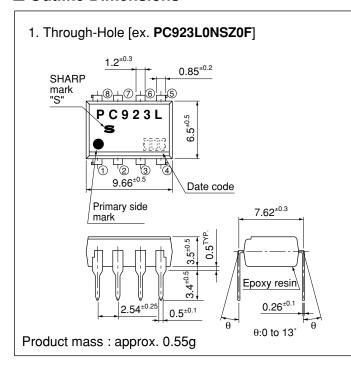


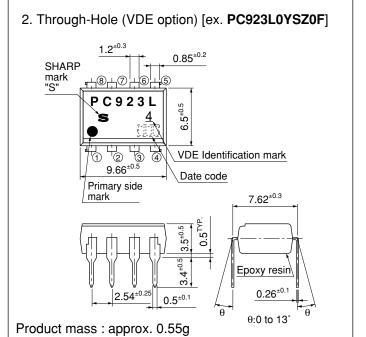
#### **■** Truth table

Input	O <sub>2</sub> Terminal output	Tr. 1	Tr. 2
ON	High level	ON	OFF
OFF	Low level	OFF	ON

#### **■** Outline Dimensions

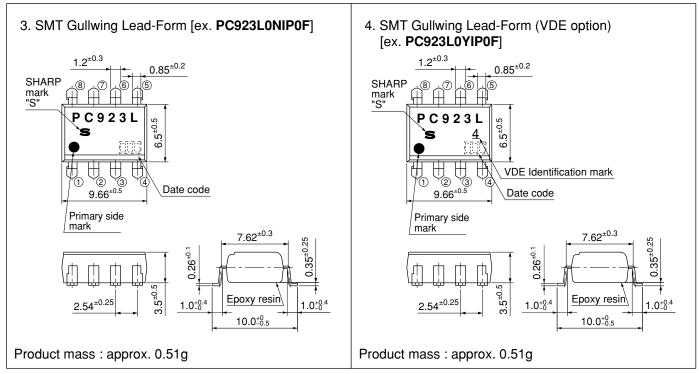
(Unit: mm)







(Unit: mm)



Plating material: SnCu (Cu: TYP. 2%)



# Date code (3 digit)

1st digit				2nd digit		3rd digit	
	Year of production		Month of production		Week of production		
A.D.	Mark	A.D	Mark	Month	Mark	Week	Mark
1990	A	2002	P	January	1	1st	1
1991	В	2003	R	February	2	2nd	2
1992	С	2004	S	March	3	3rd	3
1993	D	2005	T	April	4	4th	4
1994	Е	2006	U	May	5	5.6th	5
1995	F	2007	V	June	6		
1996	Н	2008	W	July	7		
1997	J	2009	X	August	8		
1998	K	2010	A	September	9		
1999	L	2011	В	October	0		
2000	M	2012	С	November	N		
2001	N	:	:	December	D		

repeats in a 20 year cycle

Country of origin Japan

## Rank mark

There is no rank mark indicator.



■ Absolute Maximum Ratings

				$(1_{a}-2_{a}\mathbf{C})$
	Parameter		Rating	Unit
Innut	*1 Forward current	$I_F$	20	mA
Input	Reverse voltage	$V_R$	5	V
	Supply voltage	$V_{CC}$	35	V
	O <sub>1</sub> output current	$I_{O1}$	0.1	A
	*2 O <sub>1</sub> Peak output current	I <sub>O1P</sub>	0.6	A
Output	O <sub>2</sub> output current	$I_{O2}$	0.1	A
	*2 O <sub>2</sub> Peak output current	$I_{O2P}$	0.6	A
	O <sub>1</sub> output voltage	V <sub>O1</sub>	35	V
	*3 Power dissipation	Po	500	mW
*4 Total	*4 Total power dissipation		550	mW
*5 Isolation voltage		$P_{tot} \\ V_{iso  (rms)}$	5.0	kV
Operating temperature		Topr	-40 to +85	°C
Storage temperature		$T_{stg}$	-55 to +125	°C
*6 Solde	ring temperature	$T_{sol}$	270	°C

 $(T_a=25^{\circ}C)$ 

# ■ Electro-optical Characteristics\*7

(Unless otherwise specified T<sub>a</sub>=T<sub>opr</sub>)

_		D	C1 1	C1'4'				TT:4
			Symbol	Conditions	MIN.	TYP.	MAX.	Unit
		Forward voltage	$V_{F1}$	$T_a=25$ °C, $I_F=10$ mA	_	1.6	1.75	V
Input		1 of ward voltage	$V_{F2}$	$T_a=25^{\circ}C, I_F=0.2mA$	1.2	1.5	-	V
In		Reverse current	$I_R$	$T_a=25^{\circ}C, V_R=5V$	_	-	10	μΑ
		Terminal capacitance	$C_t$	$T_a=25$ °C, V=0, f=1MHz	-	60	150	pF
		Supply voltage	$V_{CC}$	_	15	-	30	V
		O <sub>1</sub> Low level output voltage	$V_{\text{O1L}}$	$V_{CC1}=12V, V_{CC2}=-12V, I_{O1}=0.1A, I_F=5mA$	-	0.2	0.4	V
		O <sub>2</sub> High level output voltage	$V_{O2H}$	$V_{CC}=V_{O1}=24V, I_{O2}=-0.1A, I_{F}=5mA$	20	22	-	V
Output		O <sub>2</sub> Low level output voltage	$V_{O2L}$	$V_{CC}$ =24V, $I_{O2}$ =0.1A, $I_{F}$ =0	-	0.5	0.8	V
Out		O <sub>1</sub> leak current	I <sub>O1L</sub>	$V_{CC}=V_{O1}=35V, I_{F}=0$	-	-	500	μΑ
	O <sub>2</sub> leak current  *9 High level supply current  *9 Low level supply current		$I_{O2L}$	$V_{CC} = V_{O2} = 35V, I_F = 5mA$	_	_	500	μΑ
			$I_{CCH}$	V <sub>CC</sub> =24V, I <sub>F</sub> =5mA	-	1.3	3.0	mA
			$I_{CCL}$	V <sub>CC</sub> =24V, I <sub>F</sub> =0	-	1.3	3.0	mA
	*8 "Low→High" input threshold current		т	T <sub>a</sub> =25°C, V <sub>CC</sub> =24V	0.3	1.5	3.0	mA
			$I_{FLH}$	V <sub>CC</sub> =24V	0.2	-	5.0	mA
	]	Isolation resistance	R <sub>ISO</sub>	T <sub>a</sub> =25°C, DC500V, 40 to 60%RH	5×10 <sup>10</sup>	10 <sup>11</sup>	-	Ω
SS	time	"Low→High" propagation delay time	$t_{PLH}$		-	0.3	0.5	μs
risti	se	"High→Low" propagation delay time	$t_{PHL}$	$T_a=25^{\circ}C, \ V_{CC}=24V, I_F=5mA$	_	0.3	0.5	μs
acte	Respon	Rise time	t <sub>r</sub>	$R_G=47\Omega, C_G=3~000pF$	_	0.2	0.5	μs
har	Fall time		$t_{\mathrm{f}}$		_	0.2	0.5	μs
fer c	Thigh → Low → High → Low is propagation delay time    High → Low is propagation delay time			$T_a=25^{\circ}C, V_{CM}=1.5kV_{(p-p)}$				
ans			$CM_H$	$I_F=5\text{mA}, V_{CC}=24\text{V},$	-15	_	_	kV/μs
Ţ				$\Delta V_{O2H}=2.0V$				·
	Instantaneous common mode			$T_a=25^{\circ}C, V_{CM}=1.5kV_{(p-p)}$				
	1	rejection voltage	$CM_L$	$I_{F}=0, V_{CC}=24V,$	15	_	_	kV/μs
	(	Low level output)		$\Delta V_{O2L}$ =2.0V				
	_						•	

<sup>\*7</sup> It shall connect a by-pass capacitor of  $0.01\mu F$  or more between  $V_{CC}$  (pin 8) and GND (pin 7) near the device, when it measures the transfer characteristics and the output side characteristics

 $<sup>\</sup>ast 1$  The derating factors of a absolute maximum ratings due to ambient temperature are shown in Fig.10

<sup>\*2</sup> Pulse width≤0.15µs, Duty ratio: 0.01

<sup>\*3, 4</sup> The derating factors of a absolute maximum ratings due to ambient temperature are shown in Fig.11

<sup>\*5</sup> AC for 1minute, 40 to 60 %RH, f=60Hz

<sup>\*6</sup> For 10s

<sup>\*8</sup>  $I_{FLH}$  represents forward current when output goes from "Low" to "High" \*9  $O_2$  output terminal is set open



# **■** Model Line-up

Lead Form	Throug	h-Hole	SMT Gullwing				
Doolsooo	Sleeve				Taping		
Package	50pcs/sleeve				1 000pcs/reel		
DIN EN60747-5-2		Approved		Approved		Approved	
Model No.	PC923L0NSZ0F	PC923L0YSZ0F	PC923L0NIZ0F	PC923L0YIZ0F	PC923L0NIP0F	PC923L0YIP0F	

Please contact a local SHARP sales representative to inquire about production status.



Fig.1 Test Circuit for O<sub>1</sub> Low Level Output Voltage

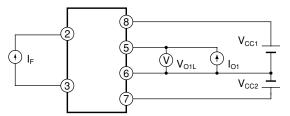


Fig.2 Test Circuit for O<sub>2</sub> High Level Output Voltage

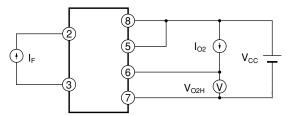


Fig.3 Test Circuit for O<sub>2</sub> Low Level Output Voltage

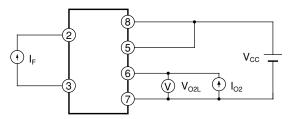


Fig.4 Test Circuit for O<sub>1</sub> Leak Current

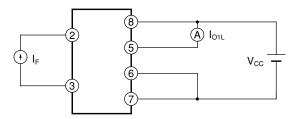


Fig.5 Test Circuit for O2 Leak Current

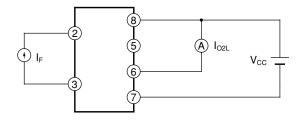
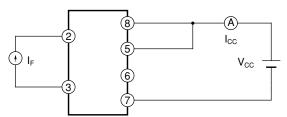
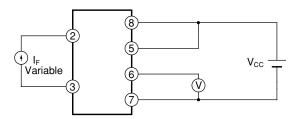


Fig.6 Test Circuit for High Level / Low Level Supply Current

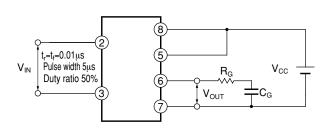


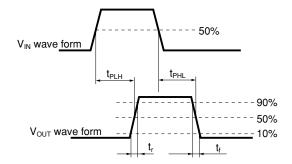


# Fig.7 Test Circuit for "Low→High" Input Threshold Current

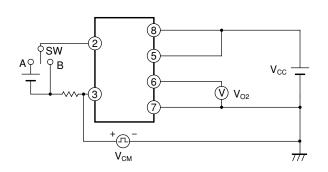


# Fig.8 Test Circuit for Response Time





# Fig.9 Test Circuit for Instantaneous Common Mode Rejection Voltage



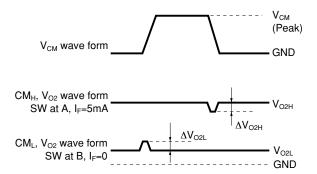




Fig.10 Forward Current vs. Ambient Temperature

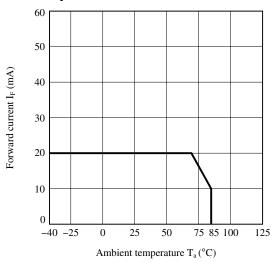


Fig.12 Forward Current vs. Forward Voltage

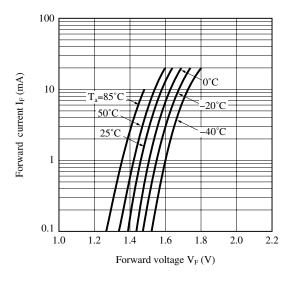


Fig.14 "Low→High" Relative Input Threshold Current vs. Ambient Temperature

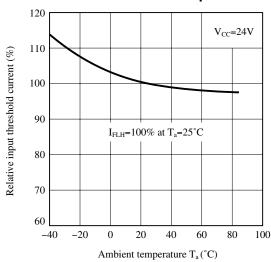


Fig.11 Power Dissipation vs. Ambient Temperature

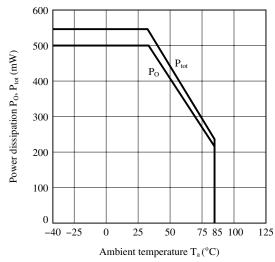


Fig.13 "Low→High" Relative Input Threshold Current vs. Supply Voltage

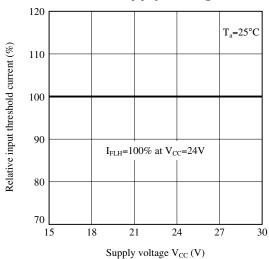


Fig.15 O<sub>1</sub> Low Level Output Voltage vs. O<sub>1</sub> Output Current

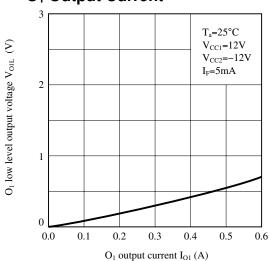




Fig.16 O<sub>1</sub> Low Level Output Voltage vs. Ambient Temperature

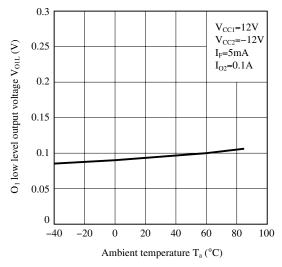


Fig.18 O<sub>2</sub> High Level Output Voltage vs. Supply Voltage

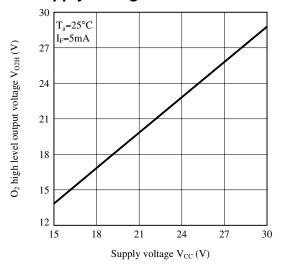


Fig.20 O<sub>2</sub> Low Level Output Voltage vs. O<sub>2</sub> Output Current

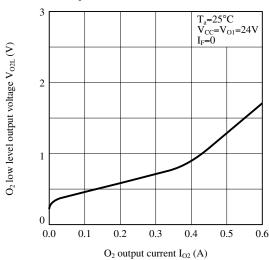


Fig.17 O<sub>2</sub> Output Voltage Drop vs. O<sub>2</sub> Output Current

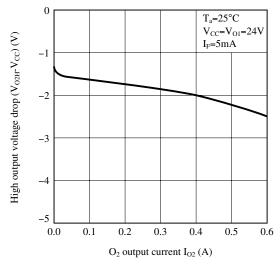


Fig.19 O<sub>2</sub> High Level Output Voltage vs. Ambient Temperature

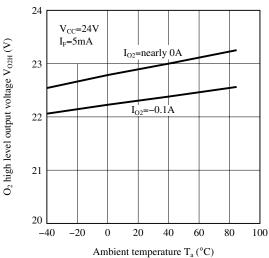


Fig.21 O<sub>2</sub> Low Level Output Voltage vs. Ambient Temperature

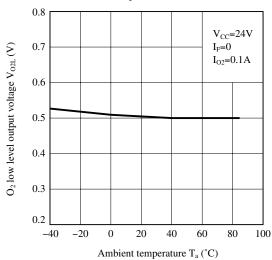




Fig.22 High Level Supply Current vs. Supply Voltage

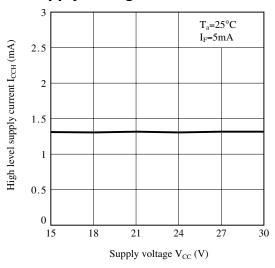


Fig.24 High Level Supply Current vs.
Ambient Temperature

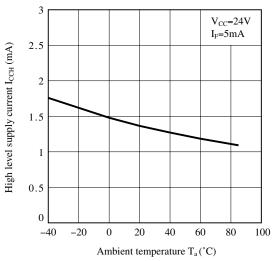


Fig.26 Propagation Delay Time vs. Forward Current

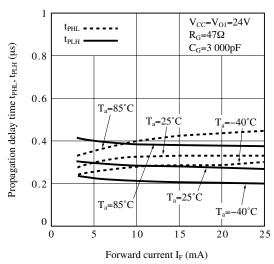


Fig.23 Low Level Supply Current vs. Supply Voltage

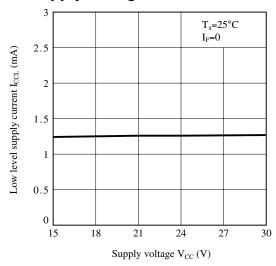


Fig.25 Low Level Supply Current vs. Ambient Temperature

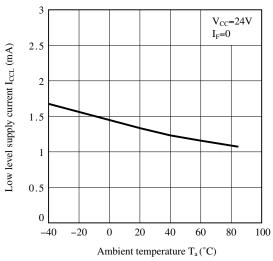
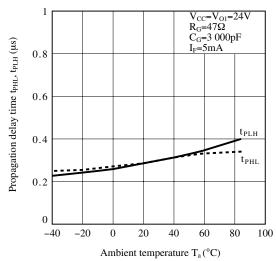


Fig.27 Propagation Delay Time vs. Ambient Temperature



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



#### ■ Design Considerations

### Recommended operating conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Forward current	$I_F$	10	_	20	mA
Supply voltage	V <sub>CC</sub>	15	-	30	V
Operating temperature	Topr	-40	-	70	°C

### Notes about static electricity

Transistor of detector side in bipolar configuration may be damaged by static electricity due to its minute design.

When handling these devices, general countermeasure against static electricity should be taken to avoid breakdown of devices or degradation of characteristics.

## Design guide

In order to stabilize power supply line, we should certainly recommend to connect a by-pass capacitor of  $0.01\mu F$  or more between  $V_{CC}$  and GND near the device.

In case that some sudden big noise caused by voltage variation is provided between primary and secondary terminals of photocoupler some current caused by it is floating capacitance may be generated and result in false operation since current may go through LED or current may change.

If the photocoupler may be used under the circumstances where noise will be generated we recommend to use the bypass capacitors at the both ends of LED.

The detector which is used in this device, has parasitic diode between each pins and GND.

There are cases that miss operation or destruction possibly may be occurred if electric potential of any pin becomes below GND level even for instant.

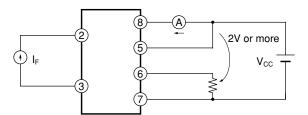
Therefore it shall be recommended to design the circuit that electric potential of any pin does not become below GND level.

This product is not designed against irradiation and incorporates non-coherent LED.

This photocoupler is dedicated to the use for IGBT or MOSFET Gate Drive.

Please do not use this for the other application.

As mentioned below, when the input is on, if DC load (resistor etc.) is connected between  $O_2$  output pin 6 and GND pin 7 and if the electric potential  $V_{O2}$  goes approx. 2V below than electric potential  $V_{CC}$  pin 8 continuously, supply current  $I_{CC}$  may flow more than usually and go beyond power dissipation.





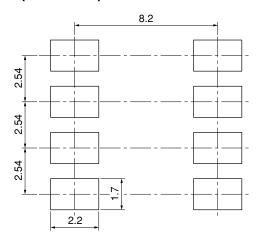
# Degradation

In general, the emission of the LED used in photocouplers will degrade over time.

In the case of long term operation, please take the general LED degradation (50% degradation over 5 years) into the design consideration.

Please decide the input current which become 2 times of MAX. I<sub>FLH</sub>.

# Recommended Foot Print (reference)



(Unit:mm)

<sup>☆</sup> For additional design assistance, please review our corresponding Optoelectronic Application Notes.



### ■ Manufacturing Guidelines

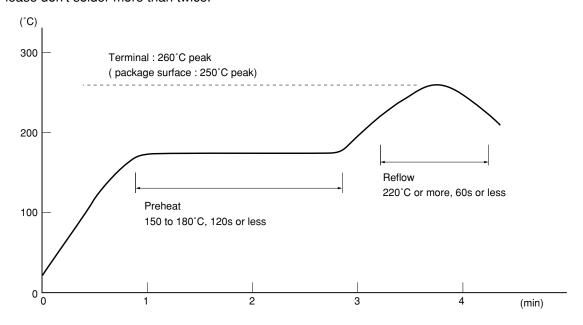
## Soldering Method

#### Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



### Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

#### Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

#### Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



### Cleaning instructions

#### Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3 minutes or less

#### Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

#### Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

#### Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

•Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).



## ■ Package specification

## Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

## Package method

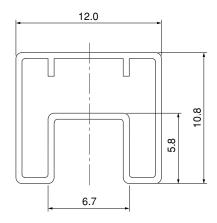
MAX. 50 pcs. of products shall be packaged in a sleeve.

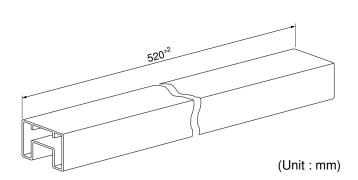
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side.

MAX. 20 sleeves in one case.

#### Sleeve outline dimensions







## ● Tape and Reel package

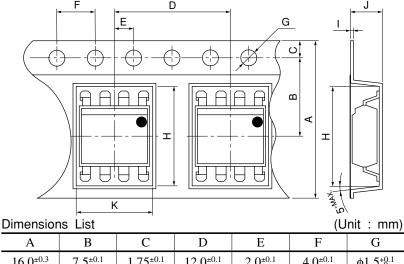
Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

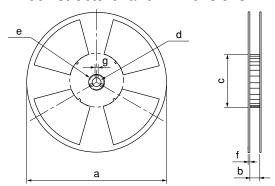
Reel: PS

Carrier tape structure and Dimensions



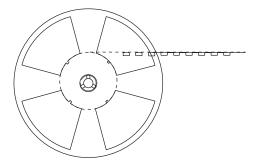
					( -	,
A	В	C	D	Е	F	G
16.0 <sup>±0.3</sup>	7.5 <sup>±0.1</sup>	1.75 <sup>±0.1</sup>	12.0 <sup>±0.1</sup>	2.0 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>	φ1.5 <del>+</del> 8.1
Н	I	J	K			
10.4 <sup>±0.1</sup>	0.4±0.05	4.2 <sup>±0.1</sup>	10.2 <sup>±0.1</sup>			

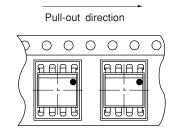
#### Reel structure and Dimensions



Dimensio	ns List	(U	nit: mm)
a	b	c	d
330	17.5 <sup>±1.5</sup>	100±1.0	13±0.5
e	f	g	
23±1.0	2.0±0.5	2.0±0.5	

# Direction of product insertion





[Packing: 1 000pcs/reel]



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