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PC929 Series

High Speed, Built-in Short Protection Circuit, Gate Drive SMD 14 pin *OPIC Photocoulper



■ Description

PC929 Series contains an IRED optically coupled to an OPIC chip.

It is packaged in a Mini-flat, Half pitch type (14 pin). Input-output isolation voltage(rms) is 4.0kV. High speed responce (t_{PLH} , t_{PHL} : MAX. 0.5 μ s).

■ Features

- 1. 14 pin Half pitch type (Lead pitch: 1.27 mm)
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. Built-in IGBT shortcircuit protector circuit
- 4. Built-in direct drive circuit for IGBT drive (Peak output current : I_{O1P}, I_{O2P} : MAX. 0.4 A)
- 5. High speed responce (t_{PLH}, t_{PHL}: MAX. 0.5 μs)
- 6. High isolation voltage (V_{iso(rms)}: 4.0 kV)

■ Agency approvals/Compliance

- Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. PC929)
- 2. Approved by VDE (VDE0884) (as an option) file No. 94626 (as model No. **PC929**)
- 3. Package resin: UL flammability grade (94V-0)

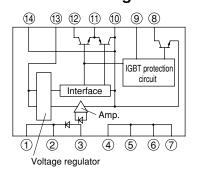
■ Applications

1. Inverter

^{* &}quot;OPIC"(Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and a signal-processing circuit integrated onto a single chip.



■ Internal Connection Diagram



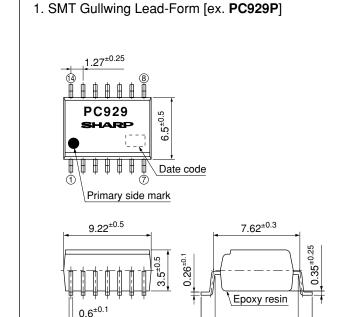
- 1) Cathode 8 FS
- ② Cathode 9 C
- 3 Anode 10 GND 4 NC* ① O₂
- ⑤ NC* 12 O₁
- 6 NC* V_{CC}
- 7 NC* 14 GND
- * No. 4 to 7 pin shall be shorted in the device.

■ Truth table

Input	C input-output	O ₂ output	FS output	
ON	Low level	High level	High level	
ON	High level	Low level	Low level	At operating protection function
OFF	Low level	Low level	High level	
OFF	High level	Low level	High level	

■ Outline Dimensions

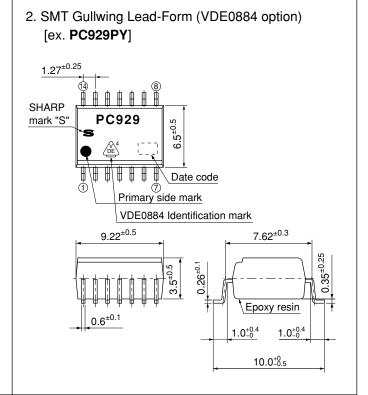
(Unit: mm)



 $1.0^{+0.4}_{-0}$

 $1.0^{+0.4}_{-0}$

 $10.0^{+0}_{-0.5}$



Product mass: approx. 0.47g



Date code (2 digit)

1st digit				2nd digit		
Year of production				Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Country of origin Japan



■ Absolute Maximum Ratings

(unless otherwise specified T_a=T_{opr})

(unicss otherwise specified 1 _a -1 _c						
Parameter		Symbol	Rating	Unit		
Input	*1Forward current	I_F	20	mA		
InI	*2Reverse voltage	V_R	6	V		
	Supply voltage	V_{CC}	35	V		
	O ₁ output current	I_{O1}	0.1	A		
	*3O ₁ peak output current	I_{O1P}	0.4	A		
	O ₂ output current	I_{O2}	0.1	A		
Ħ	*3O2 peak output current	I_{O2P}	0.4	A		
Output	O ₁ output voltage	V_{O1}	35	V		
0	*4Power dissipation	Po	500	mW		
	Overcurrent detection voltage	V _C	V_{CC}	V		
	Overcurrent detection current	I_{C}	30	mA		
	Error signal output voltage	V_{FS}	V _{CC}	V		
	Error signal output current	I_{FS}	20	mA		
*5Total power dissipation		P _{tot}	550	mW		
*6 Isolation voltage		V _{iso (rms)}	4.0	kV		
Operating temperature		Topr	-25 to +80	°C		
-5	Storage temperature	T_{stg}	-55 to +125	°C		
*75	Soldering temperature	T_{sol}	260	°C		

The derating factors of a absolute maximum ratings due to ambient temperature are shown in Fig.15

■ Electro-optical Characteristics

(unless otherwise specified $T_a=T_{opr}$)

Parameter		Symbol	Conditions *8	MIN.	TYP.	MAX.	Unit
Input	Enground violence	V_{F1}	$T_a=25$ °C, $I_F=10$ mA	_	1.6	1.75	V
	Forward voltage	V_{F2}	$T_a=25^{\circ}C, I_F=0.2mA$	1.2	1.5	-	V
	Reverse current I _R		$T_a=25^{\circ}C, V_R=5V$	1	_	10	μΑ
	Terminal capacitance	C_t	$T_a=25$ °C, V=0, f=1kHz	ľ	30	250	pF
	Supply voltage	V _{CC}	$T_a=-10 \text{ to } +60^{\circ}\text{C}$	15	-	30	V
			_	15	_	24	V
	O ₁ Low level output voltage	V _{O1L}	V _{CC1} =12V, V _{CC2} =-12V, I _{O1} =0.1A, I _F =5mA*9	ľ	0.2	0.4	V
	O ₂ High level output voltage	V_{O2H}	$V_{CC}=V_{O1}=24V, I_{O2}=-0.1A, I_{F}=5mA$ *9	20	22	-	V
Output	O ₂ Low level output voltage	V_{O2L}	V_{CC} =24V, I_{O2} =0.1A, I_{F} =0 *9		1.2	2.0	V
Omi	O ₁ leak current	I _{O1L}	$T_a=25^{\circ}C, V_{CC}=V_{O1}=35V, I_F=0$ *9		_	500	μΑ
	High level supply current	I _{CCH}	$T_a=25$ °C, $V_{CC}=V_{O1}=24V$, $I_F=5mA*9$	1	10	17	mA
			$V_{CC}=V_{O1}=24V, I_{F}=5mA$ *9	ľ	-	19	mA
	L avvilavial assembly assembly	I _{CCL}	$T_a=25$ °C, $V_{CC}=V_{O1}=24V$, $I_F=0$ *9	-	11	18	mA
	Low level supply current		$V_{CC}=V_{O1}=24V, I_{F}=0$ *9	_	_	20	mA

^{*8} It shall connect a by-pass capacitor of $0.01~\mu F$ or more between V_{CC} (pin (3)) and GND (pin, (10), (41)) near the device, when it measures the transfer characteristics and the output side characteristics. *9 FS=OPEN, V_C=0

 $T_a=25^{\circ}C$

^{*3} Pulse width≤0.15µs, Duty ratio : 0.01

^{*4.5} The derating factors of a absolute maximum ratings due to ambient temperature are shown in Fig.16

AC for 1minute, 40 to 60 %RH, T_a=25°C, f=60Hz

For 10s



(unless otherwise specified T_a=T_{opr})

	(unless otherwise specified							1 1 _a =1 _{opr})
Parameter		Symbol	Conditions *10	MIN.	TYP.	MAX.	Unit	
	*1	*11 "17 TT: -1-" :		T _a =25°C,V _{CC} =V _{O1} =24V, FS=OPEN, V _C =0	0.3	1.5	3.0	mA
	*11 "Low→High" input threshold current		I_{FLH}	V _{CC} =V _{O1} =24V, FS=OPEN, V _C =0	0.2	_	5.0	mA
	Isolation resistance		R _{ISO}	T _a =25°C, DC=500V, 40 to 60%RH	5×10 ¹⁰	1011	_	Ω
ics	me	$\stackrel{\text{\tiny E}}{=}$ "Low \rightarrow High" propagation delay time t_{PLH} T_a =25°C,		T _a =25°C,	_	0.3	0.5	μs
rist	Response time	"High→Low" propagation delay time	t _{PHL}	$V_{CC}=V_{O1}=24V, I_F=5mA,$	_	0.3	0.5	μs
acte		Rise time	t _r	$R_G=47\Omega$, $C_G=3~000pF$	_	0.2	0.5	μs
har	Res	Fall time	t_{f}	FS=OPEN, V_C =0	_	0.2	0.5	μs
Transfer characteristics		Instantaneous common mode rejection voltage (High level output)	СМн	$\begin{split} &T_a=25^{\circ}C,V_{CM}=600V(p\text{-}p)\\ &I_F=5\text{mA},V_{CC}=V_{O1}=24V,\\ &\Delta V_{O2H}=2.0V,FS=OPEN,V_C=0 \end{split}$	-1.5	_	ı	kV/μs
		Instantaneous common mode rejection voltage (Low level output)	antaneous common mode T_a =25°C, V_{CM} =600V(p-p) ction voltage CM_L I_F =0, V_{CC} = V_{O1} =24V,		1.5	-	1	kV/μs
Overcurrent detection	*1	² Overcurrent detection voltage	V _{CTH}	T_a =25°C V_{CC} = V_{O1} =24V	V _{CC} -6.5	V _{CC} -6	V _{CC} -5.5	V
		Overcurrent detection voltage hysteresis width	V _{CHIS}	I_F =5mA, R_G =47 Ω C_G =3 000pF, FS=OPEN	1	2	3	V
utput		O ₂ "High→Low" propagation delay time at overcurrent protection	t _{PCOHL}	T_a =25°C V_{CC} = V_{O1} =24V	-	4	10	μs
Protection output		O ₂ Fall time at overcurrent protection	t _{PCOtf}	$I_F = 5mA,$ $R_G = 47\Omega, C_G = 3000pF,$	2	5	_	μs
Prote		O ₂ "High→Low" output voltage at overcurrent protection	V _{OE}	R_{C} =1k Ω , C_{P} =3 000pF FS=OPEN	_	-	2	V
ut		Low level error signal voltage	$ m V_{FSL}$	T_a =25°C, I_F =5mA V_{CC} = V_{O1} =24V I_{FS} =10mA, R_G =47 Ω C_G =3 000pF, C=OPEN	-	0.2	0.4	V
Error signal output		High level error signal voltage	${ m I_{FSH}}$	$T_a = 25^{\circ}C$ $V_{CC} = V_{O1} = 24V, I_F = 5mA$ $V_{FS} = 24V, R_G = 47\Omega$ $C_G = 3 000pF, V_C = 0$	-	-	100	μΑ
		Error signal "High→Low" propagation delay time	t _{PCFHL}	T_a =25°C, V_{CC} = V_{OI} =24 V I_F =5mA, R_{FS} =1.8k Ω	_	1	5	μs
		Error signal output pulse width	Δt_{FS}	R_G =47 Ω , R_C =1 $k\Omega$ C_G =3 000pF, C_P =1 000pF	20	35	_	μs

^{*10} It shall connect a by-pass capacitor of 0.01 µF or more between V_{CC} (pin ③) and GND (pin ⑥, ④) near the device, when it measures the overcurrent characteristics, Protection output characteristics, and Error signal output characteristics.

*11 I_{FLH} represents forward current when output goes from "Low" to "High"

*12 V_{CTH} is the of C(pin ⑨) voltage when output becomes from "High" to "Low"



■ Model Line-up

Lead Form		SMT Gullwing				
Doolsooo	Sle	eve	Taping			
Package	50pcs/	/sleeve	1 000pcs/reel			
VDE0884	VDE0884 ——			Approved		
Model No.	PC929 PC929Y		PC929P	PC929PY		

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.



Fig.1 Test Circuit for O₁ Low Level Output Voltage

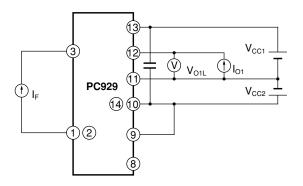


Fig.2 Test Circuit for O₂ High Level Output Voltage

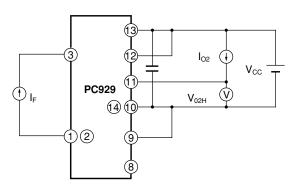


Fig.3 Test Circuit for O₂ Low Level Output Voltage

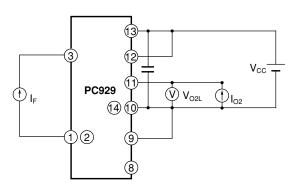


Fig.4 Test Circuit for O₁ Leak Current

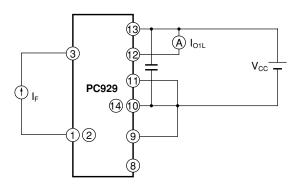


Fig.5 Test Circuit for "Low→High" Input Threshold Current

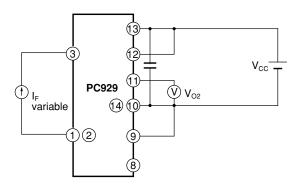


Fig.6 Test Circuit for High Level / Low Level Supply Current

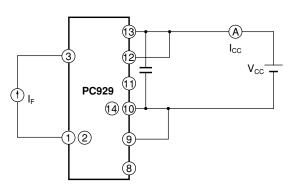




Fig.7 Test Circuit for Instantaneous Common Mode Rejection Voltage

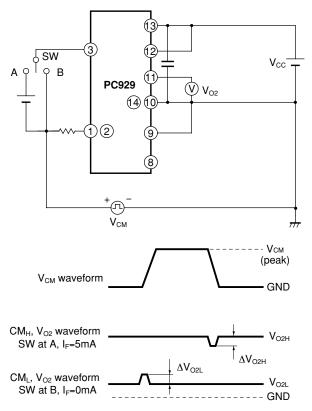


Fig.8 Test Circuit for Response Time

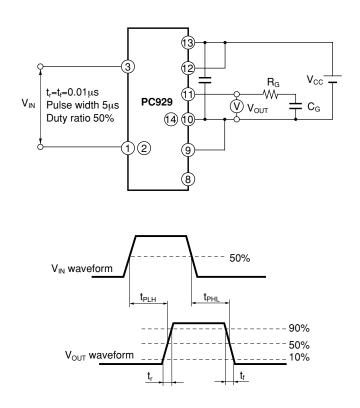


Fig.9 Test Circuit for Overcurrent Detection Voltage, Overcurrent Detection Voltage Hysteresis

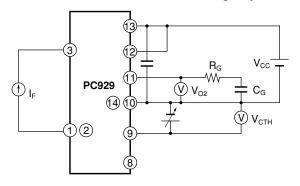


Fig.10 Test Circuit for O₂ Output Voltage at Overcurrent Protection

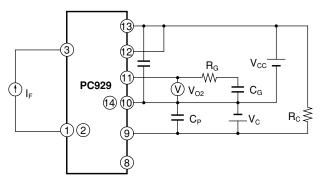




Fig.11 Test Circuit for O₁ Low Level Error Signal Voltage

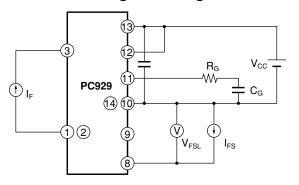


Fig.12 Test Circuit for High Level Error Signal Current

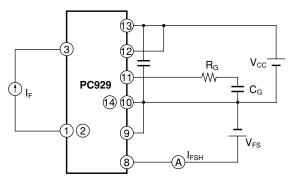


Fig.13 Test Circuit for O₂ "High→Low" Propagation Delay Time at Overcurrent Protection, O₂ Fall Time at Overcurrent Protection

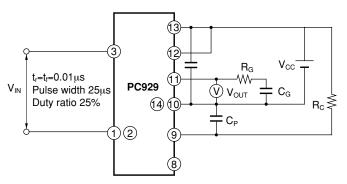
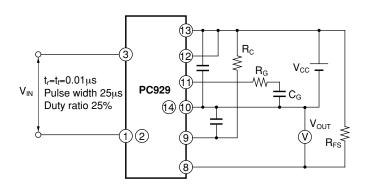


Fig.14 Error Signal "High→Low" propagation Delay Time, Error Signal Output Pulse Width



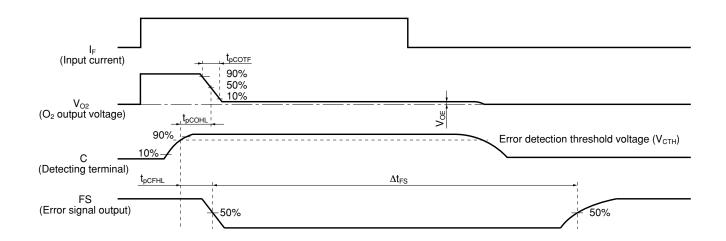




Fig.15 Forward Current vs. Ambient Temperature

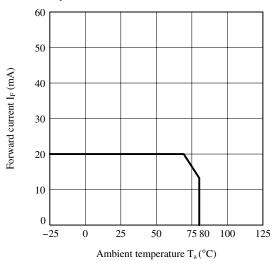


Fig.17 Forward Current vs. Forward Voltage

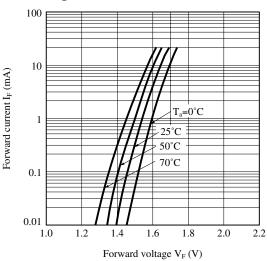


Fig.19 "Low→High" Relative Input Threshold Current vs. Ambient Temperature

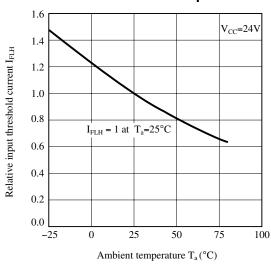


Fig.16 Power Dissipation vs. Ambient Temperature

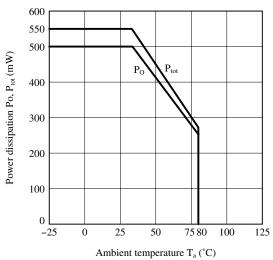


Fig.18 "Low→High" Relative Input Threshold Current vs. Supply Voltage

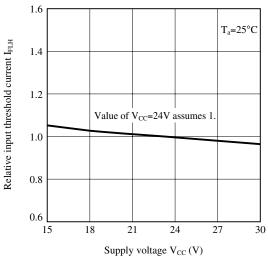


Fig.20 O₁ Low Level Output Voltage vs. O₁ Output Current

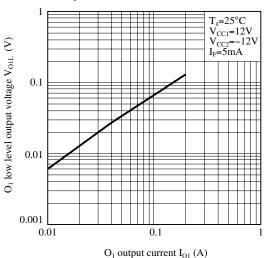




Fig.21 O₁ Low Level Output Voltage vs. Ambient Temperature

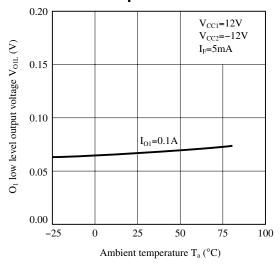


Fig.23 O₂ High Level Output Voltage vs. Supply Voltage

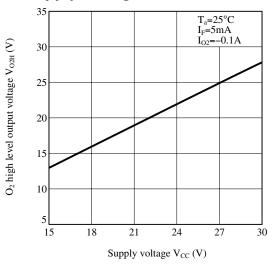


Fig.25 O₂ Low Level Output Voltage vs. O₂ Output Current

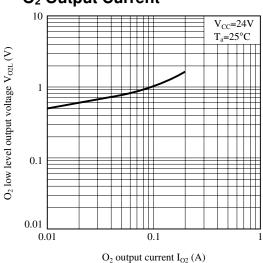


Fig.22 O₁ Leak Current vs. Ambient Temperature

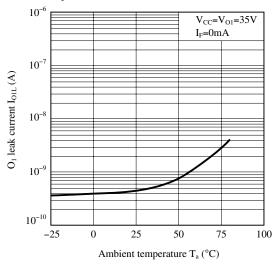


Fig.24 O₂ High Level Output Voltage vs. Ambient Temperature

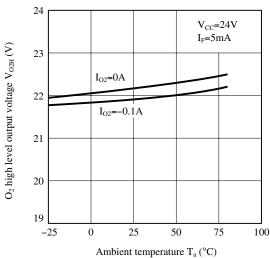


Fig.26 O₂ Low Level Output Voltage vs. Ambient Temperature

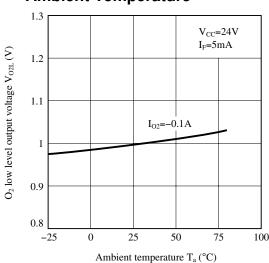




Fig.27 High Level Supply Current vs. Supply Voltage

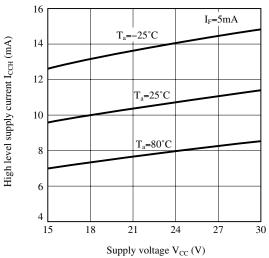


Fig.29 Propagation Delay Time vs. Forward Current

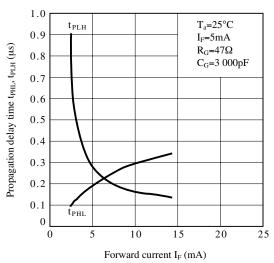


Fig.31 Overcurrent Detecting Voltage vs.
Ambient Temperature

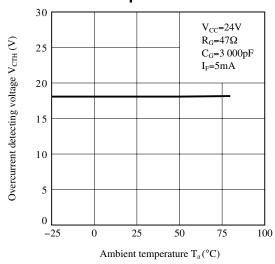


Fig.28 Low Level Supply Current vs. Supply Voltage

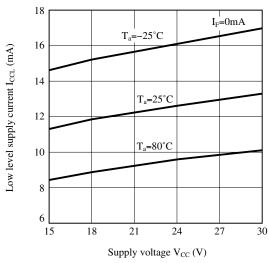


Fig.30 Propagation Delay Time vs. Ambient Temperature

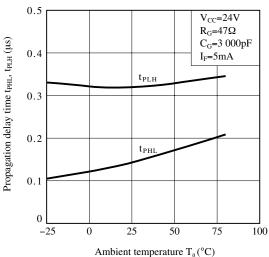


Fig.32 O₂ Output Fall Time at Protection from Overcurrent/O₂ "High-Low" Propagation Delay Time at Protection from Overcurrent vs. Ambient Temperature

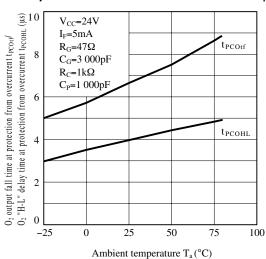




Fig.33 Error Signal "High-Low" Propagation Delay Time vs. Ambient Temperature

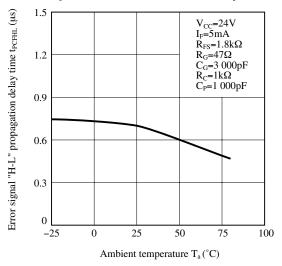


Fig.35 Low Level Error Signal Voltage vs. Ambient Temperature

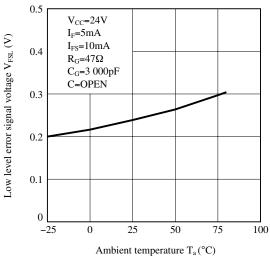


Fig.37 Error signal output pulse width vs. Ambient Temperature

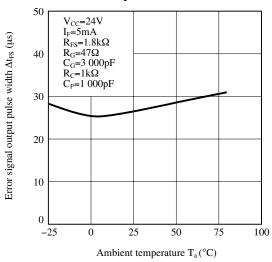


Fig.34 O₂ Output Voltage at Protection from Overcurrent vs. Ambient Temperature

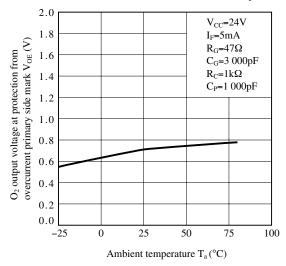


Fig.36 High Level Error Signal Current vs. Ambient Temperature

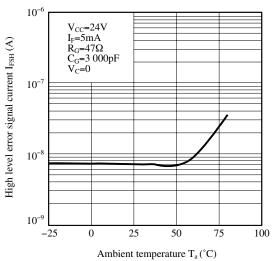


Fig.38 Overcurrent Detecting Voltage vs. Supply Voltage

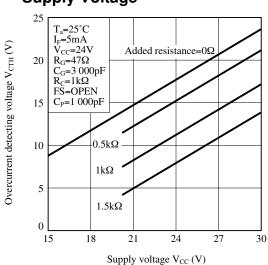




Fig.39 Overcurrent Detecting Voltage - Supply Voltage Characteristics Test Circuit

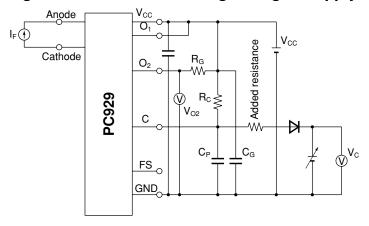
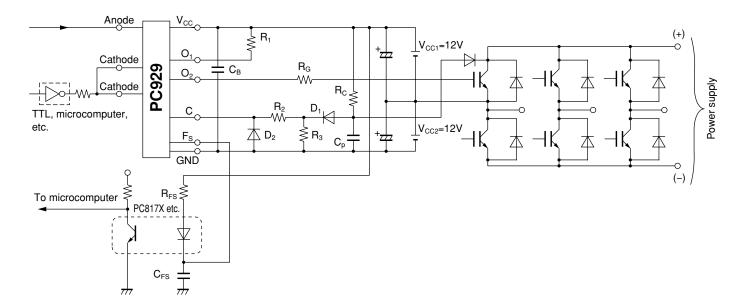


Fig.40 Example of The Application Circuit (IGBT Drive for Inverter)

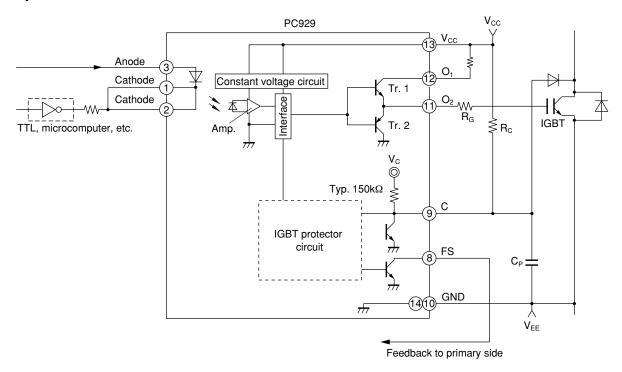


- In order to stabilize the power supply line, we recommend to locate a bypass capacitor C_B (0.01 μ F or more) between V_{CC} and GND near photocoupler.
- In order to stabilize the detecting voltage of pin-C, we recommend to locate a capacitor C_P (approximately 1 000pF) between pin-C and GND, and a resistor R_C (approximately 1.0k Ω) between V_{CC} and pin-C. However, the rise time of the detection voltage at Pin-C varies along with the time constants of C_P and R_C . So, please make sure the device works properly in actual conditions.
- For the diode D, which is located between pin-C and collector of IGBT, we recommend to use a diode that has the withstand voltage characteristic equivalent to IGBT and also has little leak current.
- In order to prevent the failure mode or breakdown of pin-C from V_{CE} variation of IGBT, we recommend to locate a resistor R₂ (approximately 10kΩ) and a diode D1 at near pin-C, and a resistor R₃ (approximately 50kΩ) and a diode D₂ at between pin-C and GND.

This application circuit shows the general example of a circuit, and is not a design guarantee for right operation.



Fig.41 Operations of Shortcircuit Protector Circuit



- 1. Detection of increase in V_{CE(sat)} of IGBT due to overcurrent by means of C terminal (pin (9))
- 2. Reduction of the IGBT gate voltage, and suppression of the collector current
- 3. Simultaneous output of signals to indicate the shortcircuit condition (FS signal) from FS (pin (8)) terminal to the microcomputer
- 4. Judgement and processing by the microcomputer
 In the case of instantaneous shortcircuit, run continues.

 At fault, input to the photocoupler is cut off, and IGBT is turned OFF.

Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Notes about static electricity

Transistor of detector side in bipolar configuration may be damaged by static electricity due to its minute design.

When handling these devices, general countermeasure against static electricity should be taken to avoid breakdown of devices or degradation of characteristics.

Design guide

In order to stabilize power supply line, we should certainly recommend to connect a by-pass capacitor of $0.01\mu F$ or more between V_{CC} and GND near the device.

We recommed to use approximately 1 000pF of capacitor between C-pin and GND in order to prevent miss opration by noise.

In the case that capacitor is used approximately $1k\Omega$ of resistance shall be recommended to use between V_{CC} and C-pin However, the rise time of C-pin shall be changed by time constant of added CR, so that please use this device after confirmation.

In case that some sudden big noise caused by voltage variation is provided between primary and secondary terminals of photocoupler some current caused by it is floating capacitance may be generated and result in false operation since current may go through LED or current may change.

If the photocoupler may be used under the circumstances where noise will be generated we recommend to use the bypass capacitors at the both ends of LED.

The detector which is used in this device, has parasitic diode between each pins and GND.

There are cases that miss operation or destruction possibly may be occurred if electric potential of any pin becomes below GND level even for instant.

Therefore it shall be recommended to design the circuit that electric potential of any pin does not become below GND level.

This product is not designed against irradiation and incorporates non-coherent LED.



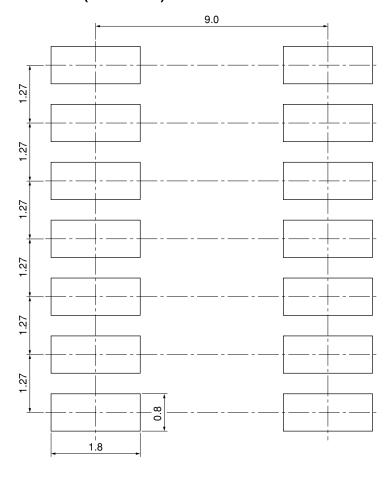
Degradation

In general, the emission of the LED used in photocouplers will degrade over time.

In the case of long term operation, please take the general LED degradation (50% degradation over 5years) into the design consideration.

Please decide the input current which become 2times of MAX. I_{FLH}.

Recommended Foot Print (reference)



(Unit:mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

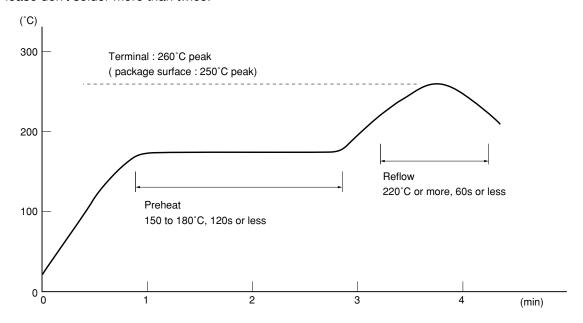
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.



■ Package specification

Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

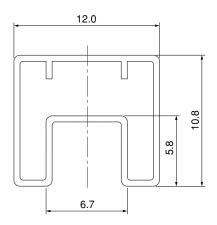
MAX. 50 pcs. of products shall be packaged in a sleeve.

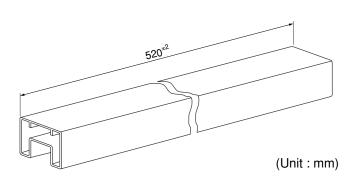
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







● Tape and Reel package

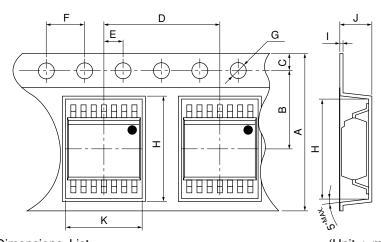
Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

Reel: PS

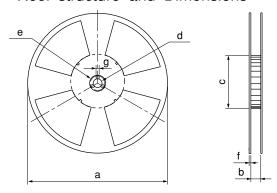
Carrier tape structure and Dimensions



Dimensions List (Unit: mm) C D Е F 1.75^{±0.1} $7.5^{\pm0.1}$ 12.0^{±0.1} $2.0^{\pm0.1}$ $4.0^{\pm0.1}$ $16.0^{\pm0.3}$

 $\phi 1.5 \stackrel{+0.1}{-0}$ Н K 4.2^{±0.1} $10.4^{\pm0.1}$ $0.4^{\pm0.05}$ $9.7^{\pm0.1}$

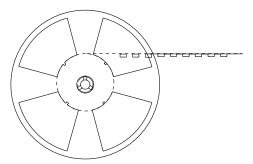
Reel structure and Dimensions

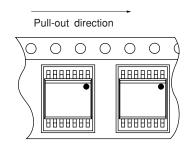


Dimensio	ns List	(Unit: mm)			
a	b	c	d		
330	330 17.5 ^{±1.5} e f		13 ^{±0.5}		
e					
23±1.0	2.0±0.5	2.0±0.5			

G

Direction of product insertion





[Packing: 1 000pcs/reel]



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 - --- Office automation equipment
 - --- Telecommunication equipment [terminal]
 - --- Test and measurement equipment
 - --- Industrial control
 - --- Audio visual equipment
 - --- Consumer electronics
- (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

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- --- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
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