imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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Contact us

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PEM2-S24-D12-S ADE IN CHINA 130

SERIES: PEM2-S | DESCRIPTION: DC-DC CONVERTER

FEATURES

- 2 W isolated output
- smaller package
- single/dual unregulated output
- 3,000 Vdc isolation
- short circuit protection
- extended temperature range (-40~105°C)
- antistatic protection up to 8kV
- UL 60950-1 approval
- high efficiency at light load
- efficiency up to 86%



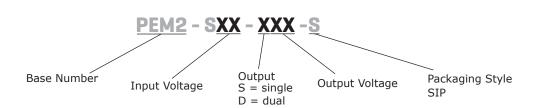
		output		•	output	ripple and noise ²	efficiency
typ	range	_	min	max	max	typ	typ (%)
5	4.5~5.5	5	40	400	2	75	82
5	4.5~5.5	12	17	167	2	75	82
5	4.5~5.5	15	13	133	2	75	83
5	4.5~5.5	24	8	83	2	75	84
5	4.5~5.5	±5	±20	±200	2	75	80
5	4.5~5.5	±12	±8	±83	2	75	83
5	4.5~5.5	±15	±7	±67	2	75	82
5	4.5~5.5	±24	±4	±42	2	75	84
12	10.8~13.2	5	40	400	2	75	82
12	10.8~13.2	12	17	167	2	75	84
12	10.8~13.2	15	13	133	2	75	85
12	10.8~13.2	±5	±20	±200	2	75	80
12	10.8~13.2	±12	±8	±83	2	75	84
12	10.8~13.2	±15	±7	±67	2	75	84
15	13.5~16.5	5	40	400	2	75	80
15	13.5~16.5	±15	±7	±67	2	75	84
24	21.6~26.4	5	40	400	2	75	80
24	21.6~26.4	12	17	167	2	75	84
24	21.6~26.4	15	13	133	2	75	86
24	21.6~26.4	24	8	83	2	75	86
24	21.6~26.4	±5	±20	±200	2	75	80
24	21.6~26.4	±12	±8	±83	2	75	84
24	21.6~26.4	±15	±7	±67	2	75	84
	typ 15 5 5 5 5 5 5 5 5 12 13 24 24 24 24 24 24	(Vdc) (Vdc) 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 5 4.5~5.5 12 10.8~13.2 12 10.8~13.2 12 10.8~13.2 12 10.8~13.2 12 10.8~13.2 12 10.8~13.2 12 10.8~13.2 12 10.8~13.2 15 13.5~16.5 15 13.5~16.5 24 21.6~26.4 24 21.6~26.4 24 21.6~26.4 24 21.6~26.4 24 21.6~26.4 24 21.6~26.4 24 21.6~26.4 24 21.6~26.4 24	voltagevoltagetyprange (Vdc)(Vdc)5 $4.5 \sim 5.5$ 55 $4.5 \sim 5.5$ 125 $4.5 \sim 5.5$ 155 $4.5 \sim 5.5$ 155 $4.5 \sim 5.5$ ± 5 5 $4.5 \sim 5.5$ ± 12 5 $4.5 \sim 5.5$ ± 24 12 $10.8 \sim 13.2$ 1212 $10.8 \sim 13.2$ ± 5 12 $10.8 \sim 13.2$ ± 12 12 $10.8 \sim 13.2$ ± 12 12 $10.8 \sim 13.2$ ± 15 15 $13.5 \sim 16.5$ 5 15 $13.5 \sim 16.5$ ± 15 24 $21.6 \sim 26.4$ 1224 $21.6 \sim 26.4$ 1524 $21.6 \sim 26.4$ ± 5 24 $21.6 \sim 26.4$ ± 12	voltagevoltagecutyprange(Vdc)min (Vdc) (Vdc)(Vdc)(mA) 5 $4.5 \sim 5.5$ 5 40 5 $4.5 \sim 5.5$ 12 17 5 $4.5 \sim 5.5$ 12 17 5 $4.5 \sim 5.5$ 15 13 5 $4.5 \sim 5.5$ 24 8 5 $4.5 \sim 5.5$ ± 15 ± 20 5 $4.5 \sim 5.5$ ± 12 ± 8 5 $4.5 \sim 5.5$ ± 15 ± 7 5 $4.5 \sim 5.5$ ± 15 ± 7 5 $4.5 \sim 5.5$ ± 24 ± 4 12 $10.8 \sim 13.2$ 12 17 12 $10.8 \sim 13.2$ 12 17 12 $10.8 \sim 13.2$ ± 12 ± 8 12 $10.8 \sim 13.2$ ± 15 ± 7 15 $13.5 \sim 16.5$ 5 40 15 $13.5 \sim 16.5$ ± 15 ± 7 24 $21.6 \sim 26.4$ 12 17 24 $21.6 \sim 26.4$ 15 13 24 $21.6 \sim 26.4$ 24 8 24 $21.6 \sim 26.4$ ± 12 ± 8	voltage typ (Vdc)voltage range 	voltage typrange (Vdc)voltage (Vdc)current min max (mA)power max (mA)5 $4.5 \sim 5.5$ 5 40 400 25 $4.5 \sim 5.5$ 12 17 167 25 $4.5 \sim 5.5$ 12 17 167 25 $4.5 \sim 5.5$ 15 13 133 25 $4.5 \sim 5.5$ 24 8 83 25 $4.5 \sim 5.5$ ± 5 ± 20 ± 200 25 $4.5 \sim 5.5$ ± 12 ± 8 ± 83 25 $4.5 \sim 5.5$ ± 12 ± 8 ± 83 25 $4.5 \sim 5.5$ ± 12 ± 8 ± 83 25 $4.5 \sim 5.5$ ± 15 ± 7 ± 67 25 $4.5 \sim 5.5$ ± 24 ± 4 ± 42 212 $10.8 \sim 13.2$ 5 40 400 212 $10.8 \sim 13.2$ ± 5 ± 20 ± 200 212 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 212 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 212 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 212 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 212 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 215 $13.5 \sim 16.5$ 5 40 400 215 $13.5 \sim 16.5$ ± 15 ± 7 ± 67 224 $21.6 \sim 26.4$ </td <td>voltage typvoltage range (Vdc)voltage (Vdc)current min (mA)power max max (mA)and noise2 max typ (mVp-p)5$4.5 \times 5.5$5$40$$400$2$75$5$4.5 \times 5.5$12$17$$167$2$75$5$4.5 \times 5.5$$12$$17$$167$2$75$5$4.5 \times 5.5$$15$$13$$133$2$75$5$4.5 \times 5.5$$24$$8$$83$2$75$5$4.5 \times 5.5$$\pm 5$$\pm 20$$\pm 200$2$75$5$4.5 \times 5.5$$\pm 12$$\pm 8$$\pm 83$2$75$5$4.5 \times 5.5$$\pm 12$$\pm 8$$\pm 83$2$75$5$4.5 \times 5.5$$\pm 12$$\pm 7$$\pm 67$$2$$75$5$4.5 \times 5.5$$\pm 12$$\pm 7$$\pm 67$$2$$75$5$4.5 \times 5.5$$\pm 12$$\pm 7$$\pm 67$$2$$75$12$10.8 \sim 13.2$$12$$17$$167$$2$$75$12$10.8 \sim 13.2$$\pm 15$$\pm 7$$\pm 67$$2$$75$12$10.8 \sim 13.2$$\pm 15$$\pm 7$$\pm 67$$2$$75$12$10.8 \sim 13.2$$\pm 15$$\pm 7$$\pm 67$$2$$75$12$10.8 \sim 13.2$$\pm 15$$\pm 7$$\pm 67$$2$$75$15$13.5 \sim 16.5$$5$$40$$400$$2$$75$15</td>	voltage typvoltage range (Vdc)voltage (Vdc)current min (mA)power max max (mA)and noise2 max typ (mVp-p)5 4.5×5.5 5 40 400 2 75 5 4.5×5.5 12 17 167 2 75 5 4.5×5.5 12 17 167 2 75 5 4.5×5.5 15 13 133 2 75 5 4.5×5.5 24 8 83 2 75 5 4.5×5.5 ± 5 ± 20 ± 200 2 75 5 4.5×5.5 ± 12 ± 8 ± 83 2 75 5 4.5×5.5 ± 12 ± 8 ± 83 2 75 5 4.5×5.5 ± 12 ± 7 ± 67 2 75 5 4.5×5.5 ± 12 ± 7 ± 67 2 75 5 4.5×5.5 ± 12 ± 7 ± 67 2 75 12 $10.8 \sim 13.2$ 12 17 167 2 75 12 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 2 75 12 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 2 75 12 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 2 75 12 $10.8 \sim 13.2$ ± 15 ± 7 ± 67 2 75 15 $13.5 \sim 16.5$ 5 40 400 2 75 15

Notes: 1. UL approved

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2. Ripple and noise are measured at 20 MHz BW by "parallel cable" method with 1 µF ceramic and 10 µF electrolytic capacitors on the output.

PART NUMBER KEY



INPUT

parameter	conditions/description	min	typ	max	units
	5 Vdc input models	4.5	5	5.5	Vdc
encypting input valte se	12 Vdc input models	10.8	12	13.2	Vdc
operating input voltage	15 Vdc input models	13.5	15	16.5	Vdc
	24 Vdc input models	21.6	24	26.4	Vdc
	for maximum of 1 second				
	5 Vdc input models	-0.7		9	Vdc
surge voltage	12 Vdc input models	-0.7		18	Vdc
5 5	15 Vdc input models	-0.7		21	Vdc
	24 Vdc input models	-0.7		30	Vdc
filter	capacitance filter				

OUTPUT

parameter	conditions/description	min	typ	max	units
line regulation	for Vin change of 1%			±1.2	%
load regulation	measured from 10% load to full load 5 Vdc output models 12 Vdc output models 15 Vdc output models 24 Vdc output models		12 8 7 6		% % %
voltage accuracy	see tolerance envelope curve				
voltage balance	dual output, balanced loads		±0.5	±1	%
switching frequency	100% load, nominal input voltage		100		kHz
temperature coefficient	100% load			±0.03	%/°C

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection ¹				1	S

Notes: 1. The supply voltage must be discontinued at the end of the short circuit duration

SAFETY AND COMPLIANCE

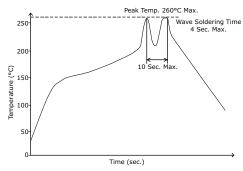
				units	
input to output, for 1 minute at 1 mA max.	3,000			Vdc	
input to output, at 500 Vdc 1,000			-	MΩ	
UL 60950-1					
CISPR22/EN55022 class B (external circuit required, see Figure 1)					
CISPR22/EN55022 class B (external circuit required, see Figure 1)					
as per MIL-HDBK-217F @ 25°C	3,500,000			hours	
2011/65/EU					
-	input to output, at 500 Vdc UL 60950-1 CISPR22/EN55022 class B (external circuit requin CISPR22/EN55022 class B (external circuit requin IEC/EN61000-4-2, class B, contact ± 8kV for sing IEC/EN61000-4-2, class B, contact ± 6kV for dua as per MIL-HDBK-217F @ 25°C	input to output, at 500 Vdc1,000UL 60950-1	input to output, at 500 Vdc1,000UL 60950-1CISPR22/EN55022 class B (external circuit required, see Figure 1)CISPR22/EN55022 class B (external circuit required, see Figure 1)IEC/EN61000-4-2, class B, contact ± 8kV for single outputs IEC/EN61000-4-2, class B, contact ± 6kV for dual outputsas per MIL-HDBK-217F @ 25°C3,500,000	input to output, at 500 Vdc1,000UL 60950-1CISPR22/EN55022 class B (external circuit required, see Figure 1)CISPR22/EN55022 class B (external circuit required, see Figure 1)IEC/EN61000-4-2, class B, contact ± 8kV for single outputs IEC/EN61000-4-2, class B, contact ± 6kV for dual outputsas per MIL-HDBK-217F @ 25°C3,500,000	

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		105	°C
storage temperature		-55		125	°C
storage humidity	non-condensing			95	%
temperature rise	at full load, Ta = 25°C		25		°C

SOLDERABILITY

parameter	conditions/description	min	typ	max	units
hand soldering	1.5 mm from case for 10 seconds			300	°C
wave soldering	see wave soldering profile			260	°C



MECHANICAL

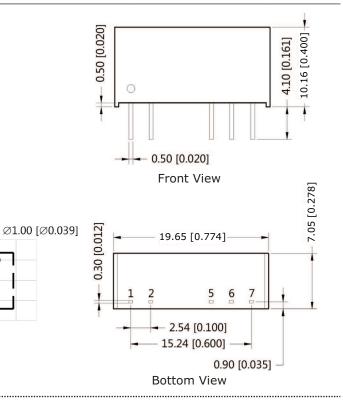
parameter	conditions/description	min	typ	max	units
dimensions	19.65 x 7.05 x 10.16 (0.774 x 0.278 x 0.400 inch)				mm
case material	plastic (UL94-V0)				
weight			2.4		g

MECHANICAL DRAWING

units: mm[inch] tolerance: $\pm 0.25[\pm 0.010]$ pin section tolerance: $\pm 0.10[\pm 0.004]$

PIN CONNECTIONS						
PIN	Single Output	Dual Output				
1	Vin	Vin				
2	GND	GND				
5	0V	-Vo				
6	No Pin	0V				
7	+Vo	+Vo				

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Ø 0

5

0

7

Grid Size: 2.54mm x 2.54mm

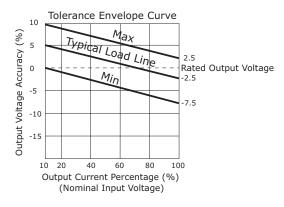
PCB Layout

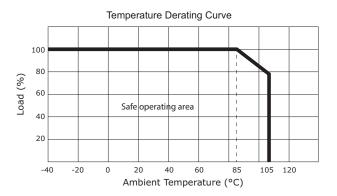
Top View

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DERATING CURVES





EMC RECOMMENDED CIRCUIT

Figure 1

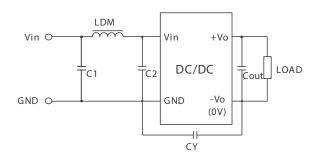


Table 1

Recommended external circuit components						
Vin (Vdc)	C1, C2	CY	LDM			
5	4.7µF/50V	-	6.8µH			
12	4.7µF/50V	-	6.8µH			
15	4.7µF/50V	-	6.8µH			
24	4.7µF/50V	1nF/3kV	6.8µH			

Note: 1. See Table 3 for Cout values.

TEST CONFIGURATION



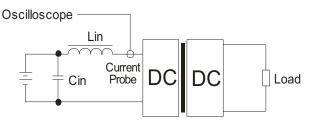


Table 2

External components				
Lin	4.7µH			
Cin	220μF, ESR < 1.0Ω at 100 kHz			

Note: Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.

APPLICATION NOTES

Output load requirement 1.

To ensure this module can operate efficiently and reliably, the minimum output load may not be less than 10% of the full load during operation. If the actual output power is low, connect a resistor at the output end in parallel to increase the load.

2. **Overload Protection**

Under normal operating conditions, the output circuit of this product has no protection against overload. The simplest method to add this is to add a circuit breaker to the circuit.

Recommended circuit 3.

If you want to further decrease the input/output ripple, you can increase the capacitance accordingly or choose capacitors with low ESR (see Figure 3 & Table 3). However, the capacitance of the output filter capacitor must be appropriate. If the capacitance is too high, a startup problem might arise. For every channel of the output, to ensure safe and reliable operation, the maximum capacitance must be less than the maximum capacitive load (see Table 4).

Figure 3

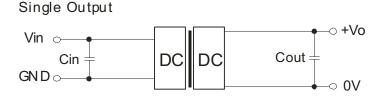


Table 3

Vin (Vdc)	Cin (µF)	Single Vo (Vdc)	Cout (µF)	Dual Vo (Vdc)	Cout (µF)
5	4.7	5	10	±5	4.7
12	2.2	12	2.2	±12	1
15	2.2	15	1	±15	0.47
24	1	24	1	±24	0.47

Note: It's not recommended to connect any external capacitors in applications with less than 0.5 watt output.



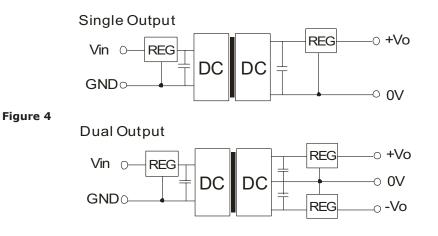
Table 4

Single Vout (Vdc)	Max. Capacitive Load (µF)	Dual Vout (Vdc)	Max. Capacitive Load ¹ (μF)
5	220	5	100
12	220	12	100
15	220	15	100
24	220	24	100
Note: 1. For	each output.		

1. For each output.

Output Voltage Regulation and Over-voltage Protection Circuit 4.

The device for output voltage regulation, over-voltage and over-current protection is a linear regulator and a capacitor filtering network with overheat protection, which can be connected to the input or output end in series (see Figure 4). The recommended capacitance of its filter capacitor (see Table 1), and the linear regulator is based on the actual voltage and current required.



1. Operation under minimum load will not damage the converter; however, they may not meet all specifications listed.

Note

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 Max. capacitive load tested at input voltage range and full load.
All specifications measured at: Ta=25°C, humidity<75%, nominal input voltage and rated output load, unless otherwise specified.

REVISION HISTORY

rev.	description	date
1.0	initial release	03/21/2013
1.01	added UL approval to some models, updated spec	02/26/2015
1.02	added model, updated emc recommendations, updated datasheet	11/19/2015

The revision history provided is for informational purposes only and is believed to be accurate.



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CUI offers a two (2) year limited warranty. Complete warranty information is listed on our website.

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