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Ultra low capacitance double rail-to-rail ESD protection diode 14 December 2017 Product data sheet

1. General description

Ultra low capacitance double rail-to-rail ElectroStatic Discharge (ESD) protection diode in a small SOT457 Surface-Mounted Device (SMD) plastic package. The device is designed to protect two high-speed data lines or high-frequency signal lines from the damage caused by ESD and other transients. The device integrates two ultra low capacitance rail-to-rail diodes and one additional ESD protection diode to ensure signal line protection even if no supply voltage is available.

2. Features and benefits

- ESD protection of two high-speed data lines
- Ultra low capacitance: C_D = 1.3 pF
- ISO 10605 (330 pF, 2 kΩ) up to 15 kV
- ESD protection up to 8 kV
- AEC-Q101 qualified

3. Applications

- 100BASE-T1 / OPEN Allicance BroadR-Reach automotive Ethernet
- Low-Voltage Differential Signaling (LVDS) automotive
- USB 2.0 automotive

4. Quick reference data

Table 1. Quick reference data									
Symbol	Parameter	Conditions		Min	Тур	Max	Unit		
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	5.5	V		
Zener diode	Zener diode								
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	16	-	pF		
Per channel									
C _d	diode capacitance	f = 1 MHz; V_R = 0 V; T_{amb} = 25 °C	[2]	-	1.3	1.5	pF		

[1] Measured from pin 5 to ground.

[2] Measured from pin 4 or 6 to ground.



5. Pinning information

inning inf	ormation		
Symbol	Description	Simplified outline	Graphic symbol
I/O 1	input/output 1		
GND	ground		
I/O 2	input/output 2		
I/O 2	input/output 2	TSOP6 (SOT457)	
V _{CC}	supply voltage	-	
I/O 1	input/output 1	-	006aab349
	Symbol I/O 1 GND I/O 2 I/O 2 VCC	I/O 1input/output 1GNDgroundI/O 2input/output 2I/O 2input/output 2V _{CC} supply voltage	SymbolDescriptionSimplified outlineI/O 1input/output 1

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PESD2ETH-D	TSOP6	plastic, surface-mounted package (SC-74)	SOT457			

7. Marking

Table 4. Marking codes					
	Type number	Marking code			
	PESD2ETH-D	L8			

8. Limiting values

Table 5. Limiting values

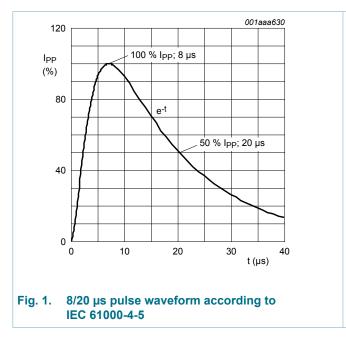
In accordance with the Absolute Maximum Rating System (IEC 60134).

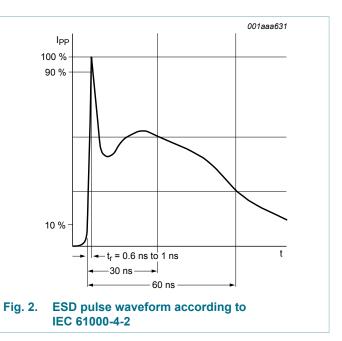
Symbol	Parameter	Conditions		Min	Max	Unit
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-	2.5	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
V _{ESD} electrostatic voltage	electrostatic discharge	IEC 61000-4-2; contact discharge	[2] [3]	-	8	kV
	voltage	MIL-STD-883 (human body model)		-	10	kV

[1] Measured from pin 1,3,4 or 6 to GND.

[2] According to IEC61000-4-5.

[3] Device stressed with ten non-repetitive ESD pulses.





9. Characteristics

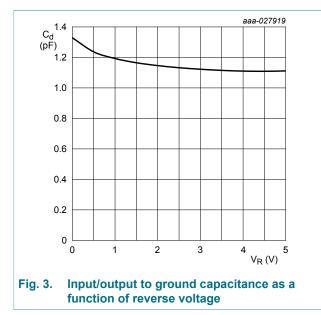
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	5.5	V
V _F	forward voltage	I _F = 1 mA; T _{amb} = 25 °C		-	0.7	-	V
I _{RM}	reverse leakage current	V _R = 5.5 V; T _{amb} = 25 °C	[1]	-	1	100	nA
V _{CL}	clamping voltage	I _{PPM} = 2.5 A; 8/20 μs; T _{amb} = 25 °C	[1]	-	11.8	-	V
R _{dyn}	dynamic resistance	I _R = 10 A; T _{amb} = 25 °C	[1] [2]	-	1.1	-	Ω
Zener diode)		·				
V _{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C	[3]	6	-	9	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[3]	-	16	-	pF
Per channe	I			-	·		
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[4]	-	1.3	1.5	pF

Measured from pin 1,3,4 or 6 to GND. [1]

[2] [3] Non-repetitive current pulse, Transmission Line Pulse (TLP) t_p = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008.

Measured from pin 5 to ground.

[4] Measured from pin 4 or 6 to ground.



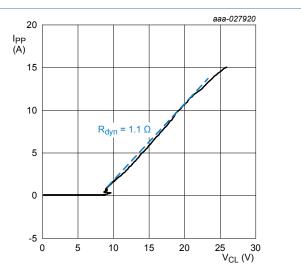
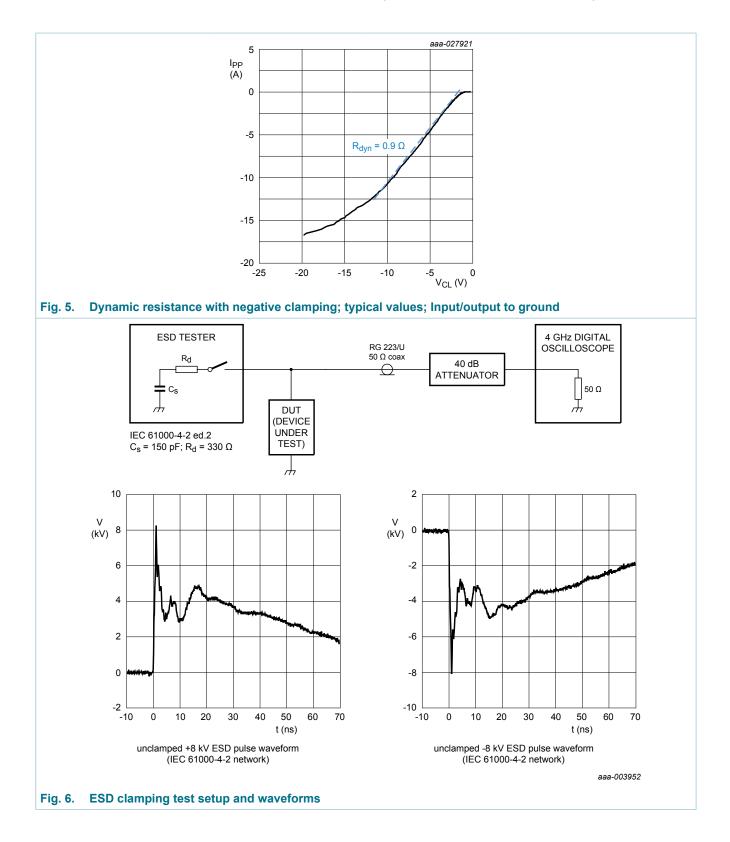


Fig. 4. Dynamic resistance with positive clamping; typical values; Input/output to ground

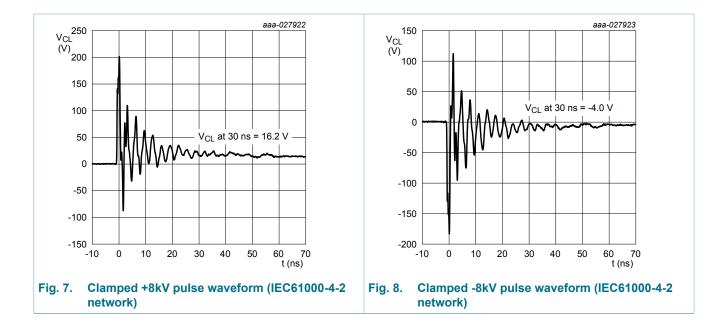
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Ultra low capacitance double rail-to-rail ESD protection diode

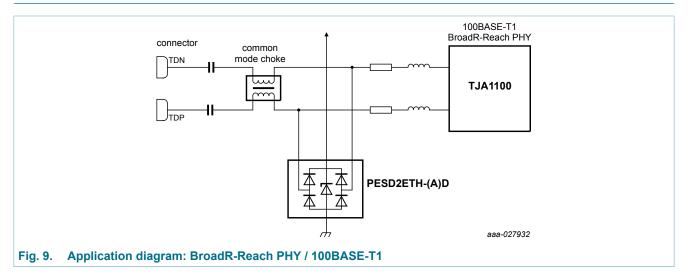


Product data sheet

Ultra low capacitance double rail-to-rail ESD protection diode



10. Application information



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

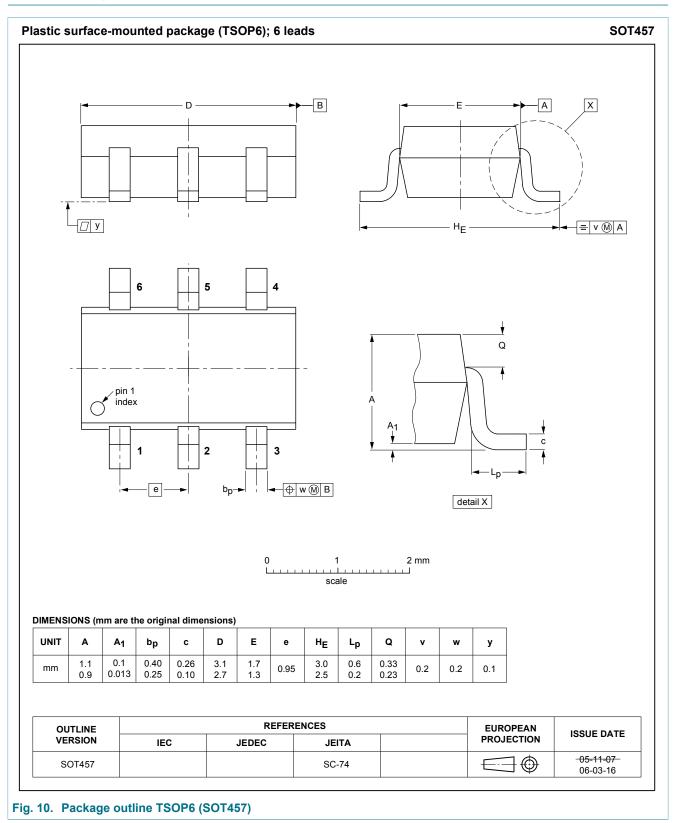
11. Test information

Quality information

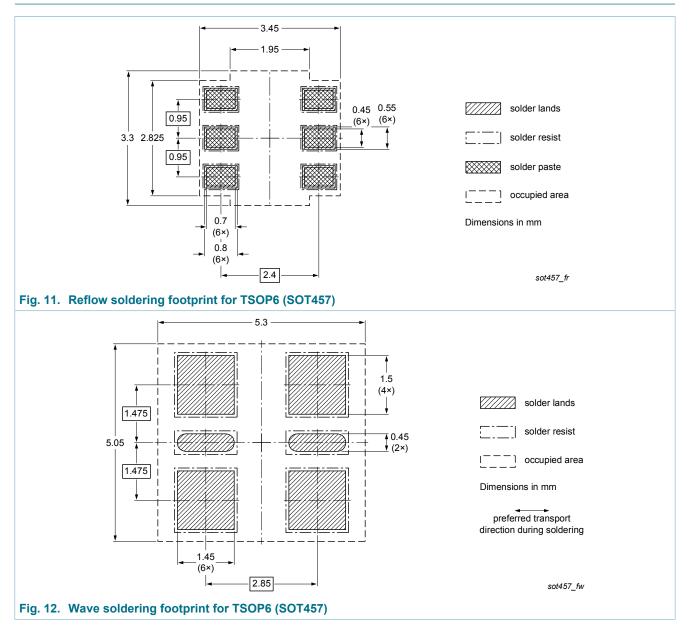
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

Ultra low capacitance double rail-to-rail ESD protection diode

12. Package outline



13. Soldering



14. Revision history

Table 7. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PESD2ETH-D v.1	20171214	Product data sheet	-	-		

Ultra low capacitance double rail-to-rail ESD protection diode

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.nexperia.com</u>.

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Ultra low capacitance double rail-to-rail ESD protection diode

16. Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Characteristics	4
10.	. Application information	6
11.	. Test information	7
12.	. Package outline	8
13.	. Soldering	9
14.	. Revision history	10
15.	. Legal information	11

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