

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









6.5 Gbps SAS2, SATA3, XAUI 2 Differential Channel, 2:1 Mux/DeMux Switch

Features

- SAS2, SATA3, XAUI switch
- 2 Differential Channel, 2:1 Mux/DeMux
- 6.5 Gbps performance
- · Bi-directional operation
- Low Bit-to-Bit Skew, 6ps max
- Low Insertion Loss: -2.3dB@3GHz(6Gbps)
- Low Crosstalk: -43dB@3GHz (6.0Gbps)
- Low Off Isolation: -21dB@3GHz (6.0Gbps)
- V_{DD} Operating Range: 1.5V to 1.8V $\pm 10\%$
- ESD Tolerance 2KV HBM I/O
- Packaging: 28 contact TQFN (ZH, 3.5 × 5.5mm)

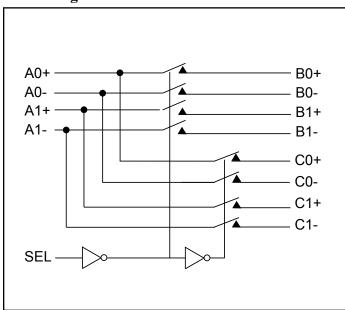
Description

Pericom Semiconductor's PI2DBS6212 is a 4 to 2 bi-directional differential channel multiplexer/demultiplexer switch supporting 6.5 Gbps applications. Due to its low bit-to-bit skew, high channel-to-channel noise isolation and high bandwidth, this product is ideal for switching two sources to a single receiver, or alternatively, one source to two receivers.

Application

- SAS2, SATA3, XAUI, Infiniband, Hype Transport, Rapid I/O
- Computers, servers, storage, instrumentation, telecom, networking.

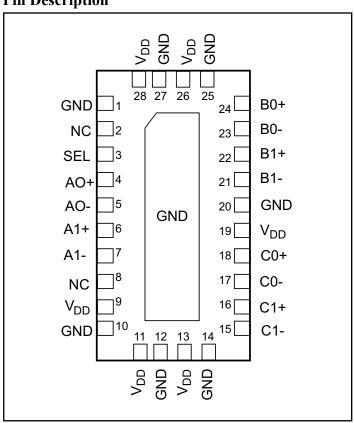
Block Diagram



Truth Table

Function	SEL
A to B	L
A to C	Н

Pin Description



14-0031 1 www.pericom.com 03/26/2014



Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Supply Voltage to Ground Potential	0.5V to +2.5V
DC Input Voltage	0.5V to +V _{DD}
DC Output Current	120mA
Power Dissipation	0.5W

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I_{DD}	Quiescent Power Supply Current	$V_{DD} = Max., V_{IN} = GND \text{ or } V_{DD}$			400	μΑ

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{DD} = 1.8V$, $T_A = 25^{\circ}C$ ambient and maximum loading.

DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}C \text{ to } +85^{\circ}C, V_{DD} = 1.5V \text{ to } 1.8V \pm 10\%)$

Parameter	Description	otion Test Conditions		Тур.	Max.	Units	
V_{IH}	Input HIGH Voltage, SEL input	Guaranteed HIGH level	0.65 x V _{DD}	-	-		
$V_{ m IL}$	Input LOW Voltage, SEL input	Guaranteed LOW level	-0.5	-	0.35 x V _{DD}	V	
V _{IK}	Clamp Diode Voltage, SEL input	$V_{DD} = Max., I_{IN} = -18mA$	-	-0.7	-1.2		
I_{IH}	Input HIGH Current for SEL	$V_{DD} = Max., V_{IN} = V_{DD}$	-10	-	+10		
I_{OZ}	Channel Leakage Current	$V_{DD} = Max., V_{IN} = 1.8V$	-10	-	+10	μΑ	
I_{IL}	Input LOW Current	$V_{DD} = Max., V_{IN} = GND$	-20	-	+10		
V _{IDC} DC Signal Voltag	DC Signal Valtage Dange	$V_O/V_I > 95\%$, $R_L = 10K$	-0.5		2.5	V	
	De Signal voltage Range	$V_O/V_I > 80\%$, $R_L = 50$ -Ohms	-0.4		1.2	'	

Switching Characteristics

 $(T_A = -40^\circ \text{ to } +85^\circ \text{C}, V_{DD} = 1.5 \text{V to } 1.8 \text{V} \pm 10\%)$

Parameter	Description	Min.	Тур.	Max.	Units	
tpZH, tpZL	Line Enable Time - SEL to A _N , B _N , C _N	0.5	-	9.0	na	
tpHZ, tPLZ	Line Disable Time - SEL to A _N , B _N , C _N	0.5	-	9.0	ns	
t _{b-b}	Bit-to-bit skew within the same differential pair			10		
tch-ch	Channel-to-channel skew		15	ps		
tdiff	Differential delay - A _N to B _N or C _N			20		

Notes:

14-0031 2 www.pericom.com 03/26/2014

^{1.} For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.



Dynamic Electrical Characteristics Over the Operating Range (TA= -40° to +85°C, V_{DD} = 1.5V to 1.8V $\pm 10\%$)

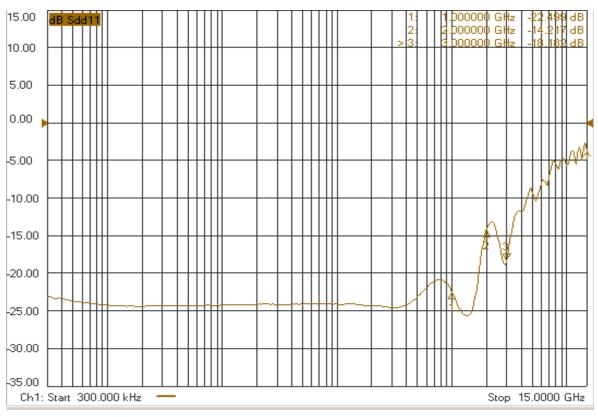
Parameter	Description	Test Conditions	Min.	Тур.	Max.	Units
BW	Bandwidth -3dB			4.1		
		Insertion loss 1.5dB, V _{IN} =0.8Vpp, DC=0V	2.5			GHz
V _z -	Max Signal Frequency	Insertion loss 1.5dB, V _{IN} =0.6Vpp, DC=0.9V	2.5			
V _{IF}	Range	Insertion loss 3dB, V _{IN} =0.8Vpp, DC=0V	4.0			
		Insertion loss 3dB, V _{IN} =0.6Vpp, DC=0.9V	4.0			
	dB 1 dB Compression Input Signal	$R_L = 50$, f=375MHz, sin wave, DC=0V	1.2			Vpp
P-1dB		R _L = 50, f=375MHz, sin wave, DC=0.45V	2.0			
3-8	R _L = 50, f=375MHz, sin wave, DC=0.9V	2.4				
R _{LOSS}	Return Loss	f=3 GHz		-18		
X _{TALK}	Crosstalk	f = 3.0 GHz		-43		
O _{IRR}	OFF Isolation	f = 3.0 GHz		-21		dB
I _{LOSS}	Differential Insertion Loss	f= 3 GHz		-2.3		

Notes:

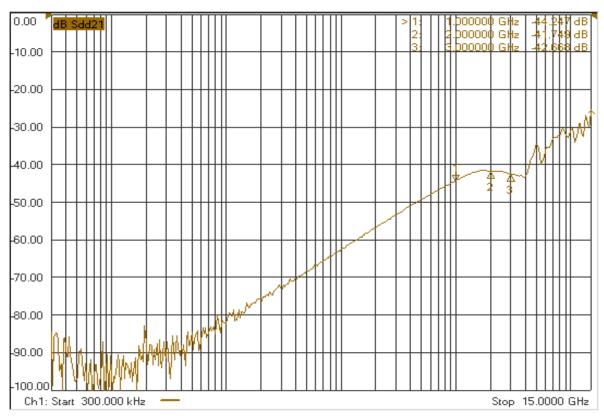
03/26/2014 www.pericom.com 14-0031 3

Guaranteed by design.

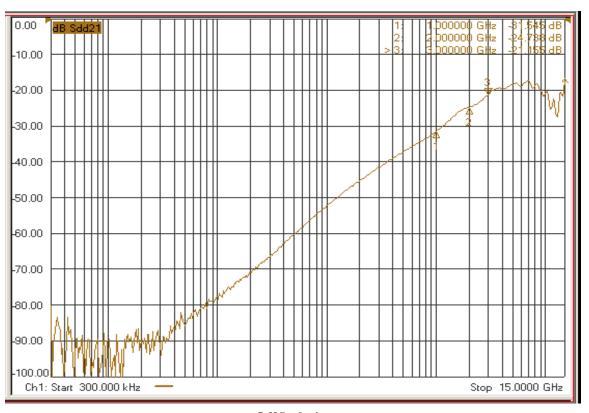




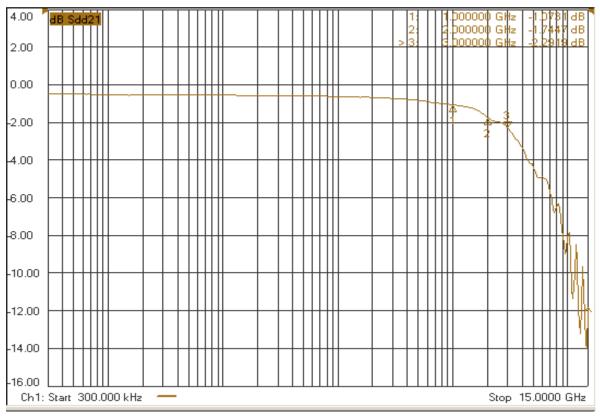
Return Loss



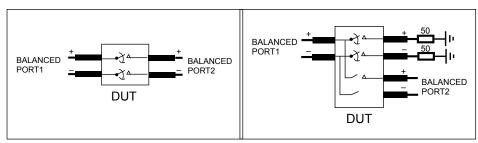


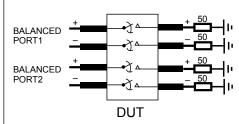


Off Isolation







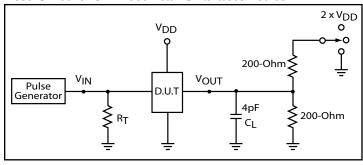


Diff. Insertion Loss and Return Test Circuit

Diff. Off Isolation Test Circuit

Diff. Near End Xtalk Test Circuit

<u>Test Circuit for Electrical Characteristics</u>⁽¹⁻⁵⁾



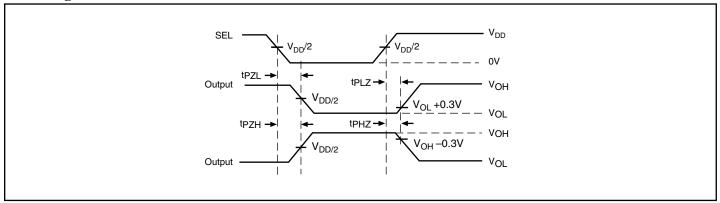
Switch Positions

Test	Switch
t _{PLZ} , t _{PZL}	$2 \times V_{DD}$
t _{PHZ} , t _{PZH}	GND
Prop Delay	Open

Notes:

- 1. C_L = Load capacitance: includes jig and probe capacitance.
- 2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
- Output 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
 output 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. All input impulses are supplied by generators having the following characteristics: $PRR \le MHz$, $Z_Q = 50\Omega$, $t_R \le 2.5$ ns, $t_F \le 2.5$ ns.
- 5. The outputs are measured one at a time with one transition per measurement.

Switching Waveforms

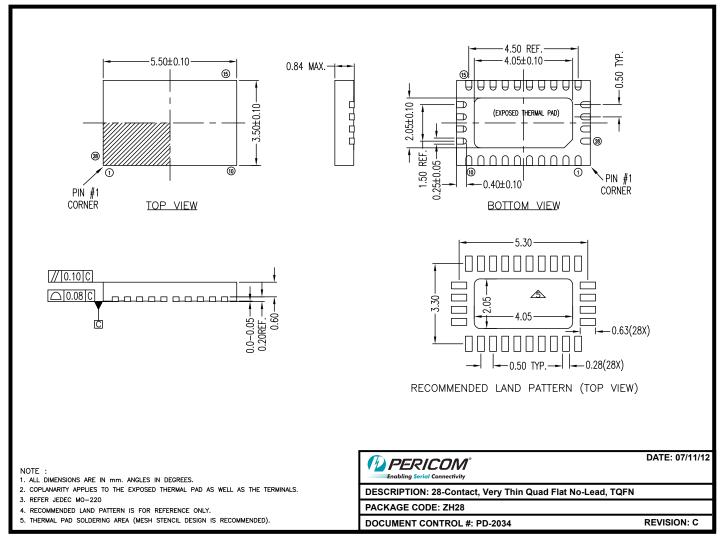


Voltage Waveforms Enable and Disable Times

14-0031 6 www.pericom.com 03/26/2014



Packaging Mechanical: 28-Pin TQFN (ZH)



12-0419

Note:

For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php

Ordering Information

Ordering Code	Package Code	Package Type
PI2DBS6212ZHEX	ZH	28-contact, Very Thin Quad Flat No-Lead (TQFN)

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding an X suffix = Tape/Reel

Pericom Semiconductor Corporation • 1-800-435-2336 • www.pericom.com