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## Product Features

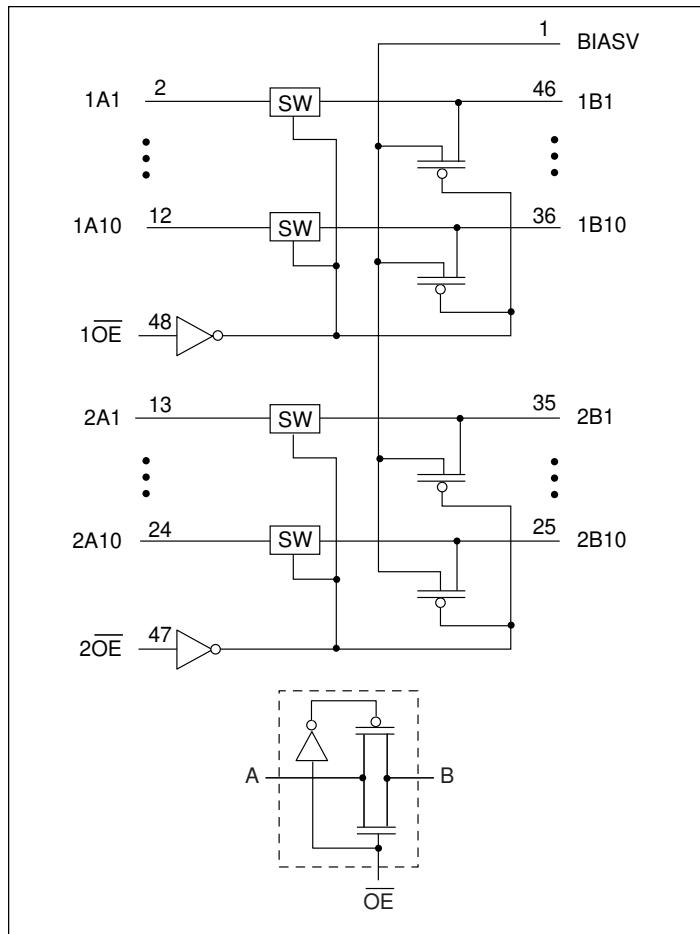
- Near Zero Propagation Delay
- 5-ohm Switches Connect Between Two Ports
- Fast Switching Speed: 4.5ns max.
- B-Port Outputs are precharged by Bias Voltage to minimize signal distortion during live insertion
- Package options include:
  - 48-pin 150-mil wide plastic BQSOP (B)
  - 48-pin 240-mil wide plastic TSSOP (A)
  - 48-pin 300-mil wide plastic SSOP (V)

## Product Description

Pericom Semiconductor's PI3B series of logic circuits are produced using the company's advanced submicron CMOS Technology. The PI3B16215 provides 20-bits of high-speed bus switching. The switches low ON-state resistance allows connections to be made with minimal propagation delay also precharges the B-port to a user-selectable bias voltage (BIASV) to minimize live- insertion noise. The device is organized as dual 10-bit bus switches with individual output-enable (OE) inputs. When OE is LOW, the corresponding 10-bit bus switch is on and port A is connected to port B. When OE is HIGH, the switch is open, a high-impedance state exists between the two ports, and port B is precharged to BIASV through the equivalent of a 10-kohm resistor.

To ensure the high-impedance state on power up or power down, OE should be tied to V<sub>CC</sub> through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver connected to OE.

## Logic Block Diagram



## Product Pin Configuration

48-Pin A,B,V	
BIASV	1
1A1	2
1A2	3
1A3	4
1A4	5
1A5	6
1A6	7
GND	8
1A7	9
1A8	10
1A9	11
1A10	12
2A1	13
2A2	14
V <sub>CC</sub>	15
2A3	16
GND	17
2A4	18
2A5	19
2A6	20
2A7	21
2A8	22
2A9	23
2A10	24
1OE	48
2OE	47
1B1	46
1B2	45
1B3	44
1B4	43
1B5	42
GND	41
1B6	40
1B7	39
1B8	38
1B9	37
1B10	36
2B1	35
2B2	34
2B3	33
GND	32
2B4	31
2B5	30
2B6	29
2B7	28
2B8	27
2B9	26
2B10	25

## Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature Range, T <sub>STG</sub> .....	-65°C to +150°C
Supply Voltage Range, V <sub>CC</sub> .....	-0.5V to +4.6V
Bias Voltage Range, BIASV .....	-0.5V to +4.6V
Input Voltage Range .....	-0.5V to +4.6V
DC Output Current .....	120mA
Power Dissipation .....	0.5W

### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## Electrical Characteristics (Over the Operating Range, T<sub>A</sub> = -40°C to +85°C, V<sub>CC</sub> = 3.0V to 3.6V)

Parameter	Description	Test Conditions		Min.	Typ.	Max.	Units
BIASV	Bias Voltage	V <sub>CC</sub> = 2.7V to 3.6V	V <sub>I</sub> = V <sub>CC</sub> or GND	0		V <sub>CC</sub>	V
V <sub>IH</sub>	High-Level Control Input Voltage			2			
V <sub>IL</sub>	Low-Level Control Input Voltage			-0.5		0.8	
V <sub>IK</sub>	Clamp Diode Voltage	V <sub>CC</sub> = 3.0V	I <sub>I</sub> = -18mA		-0.7	-1.2	
I <sub>I</sub>	Input Current	V <sub>CC</sub> = 3.6V	V <sub>I</sub> = V <sub>CC</sub> or GND			±5	μA
I <sub>OZH</sub>	High Impedance Output Current	V <sub>CC</sub> = 0				10	
I <sub>O</sub>	Output Current	V <sub>CC</sub> = 3.0V	BiasV = 2.4V, V <sub>O</sub> = 0	0.15			mA
I <sub>CC</sub>	Quiescent Power Supply Current	V <sub>CC</sub> = 3.6V	I <sub>O</sub> = 0, V <sub>I</sub> = V <sub>CC</sub> or GND			10	μA
ΔI <sub>CC</sub> <sup>(1)</sup>	Supply Current	V <sub>CC</sub> = 3.6V	One Input at 3V, Other Inputs at V <sub>CC</sub> or GND			750	
C <sub>IN</sub>	Input Capacitance	V <sub>I</sub> = 3.0V or 0			3.0		pF
C <sub>OFF</sub>	A/B Capacitance Switch Off	V <sub>O</sub> = 3.0V or 0	Switch Off		8.5		
R <sub>ON</sub> <sup>(2)</sup>	Switch On Resistance	V <sub>I</sub> = 0	I <sub>I</sub> = 64mA		5	8	Ω
			I <sub>I</sub> = 24mA		5	8	
		V <sub>I</sub> = 2.4V	I <sub>I</sub> = 15mA		10	15	

### Notes:

1. This is the increase in supply current for each input (OE only) that is at the specified voltage level rather than V<sub>CC</sub> or GND.
2. Measured by the voltage drop between the A and B terminals at the indicated current through the switch. On state resistance is determined by the lower of the voltages of the two (A or B) terminals.

## Truth Table

OE	Function
L	A port = B port
H	A port = Z, B Port = BIASV

## Switching Characteristics over Operating Range

(Switching characteristics over recommended operating free-air temperature range unless otherwise noted)

Parameter	Test Conditions	From (Input)	To (Output)	$V_{CC} = 3.3V \pm 10\%$		Units
				Min.	Max.	
$t_{PD}^{(1)}$		A or B	$\overline{OE}$		0.25	ns
$t_{PZH}$	$BIASV = GND$				4.5	
$t_{PZL}$	$BIASV = 3V$				4.5	
$t_{PHZ}$	$BIASV = GND$				5.0	
$t_{PLZ}$	$BIASV = 3V$				5.0	

### Note:

1. The propagation delay is the calculated RC time of the typical on-state resistance of the switch and the specified load capacitance, when driven by an ideal voltage source (zero output impedance).

## Parameter Measurements ( $V_{CC} = 2.7$ and $3.3V \pm 10\%$ )

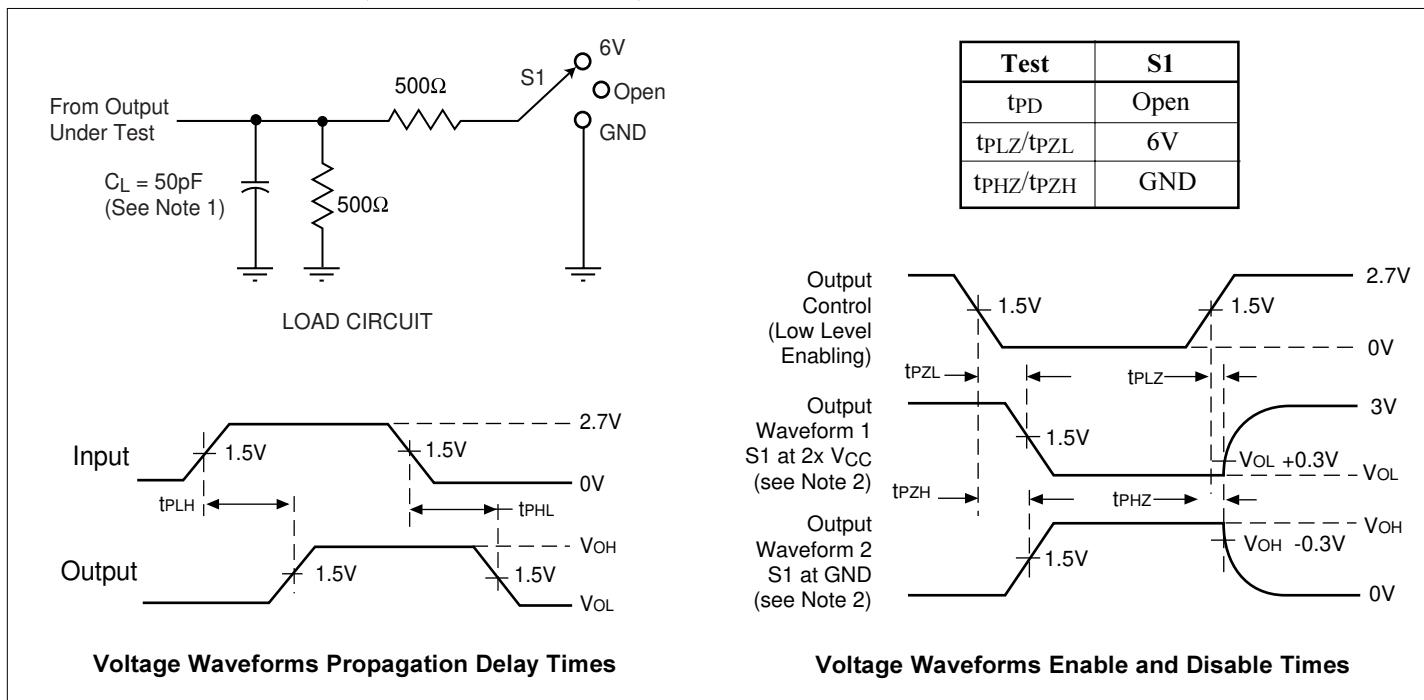
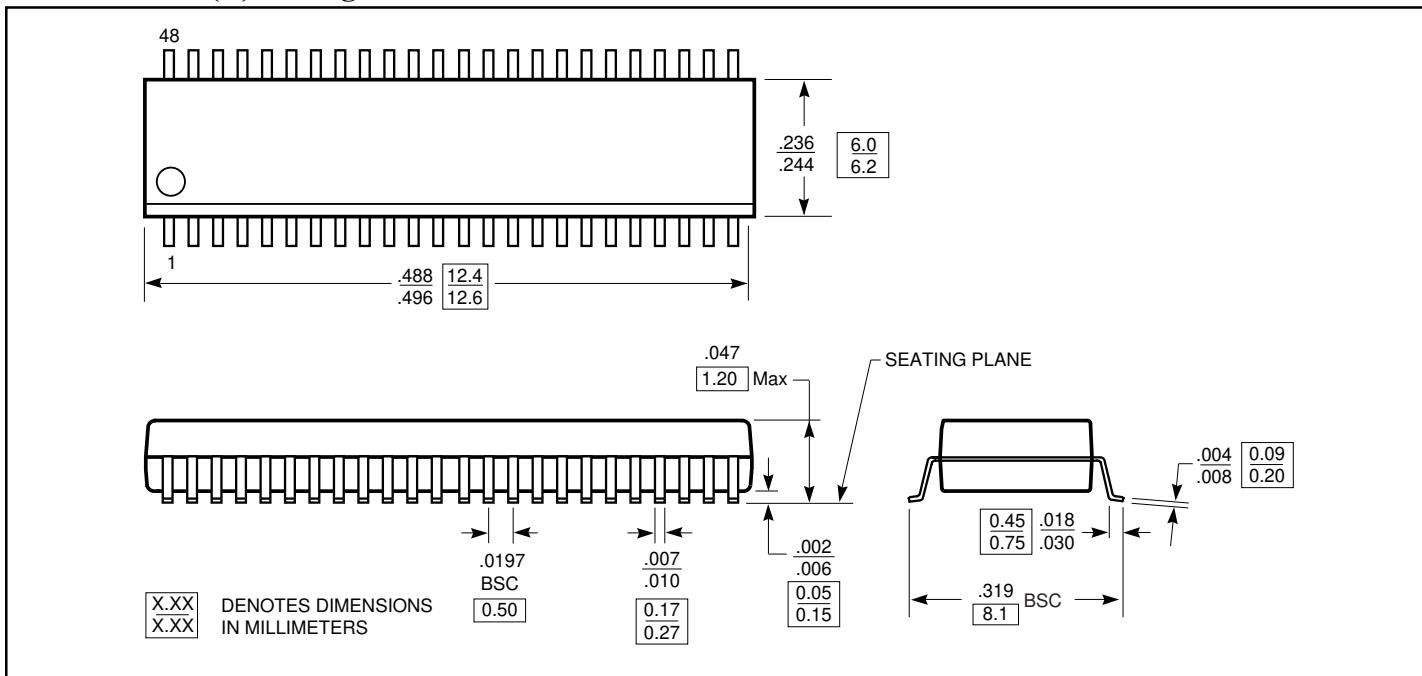


Figure 1. Load Circuit and Voltage Waveforms

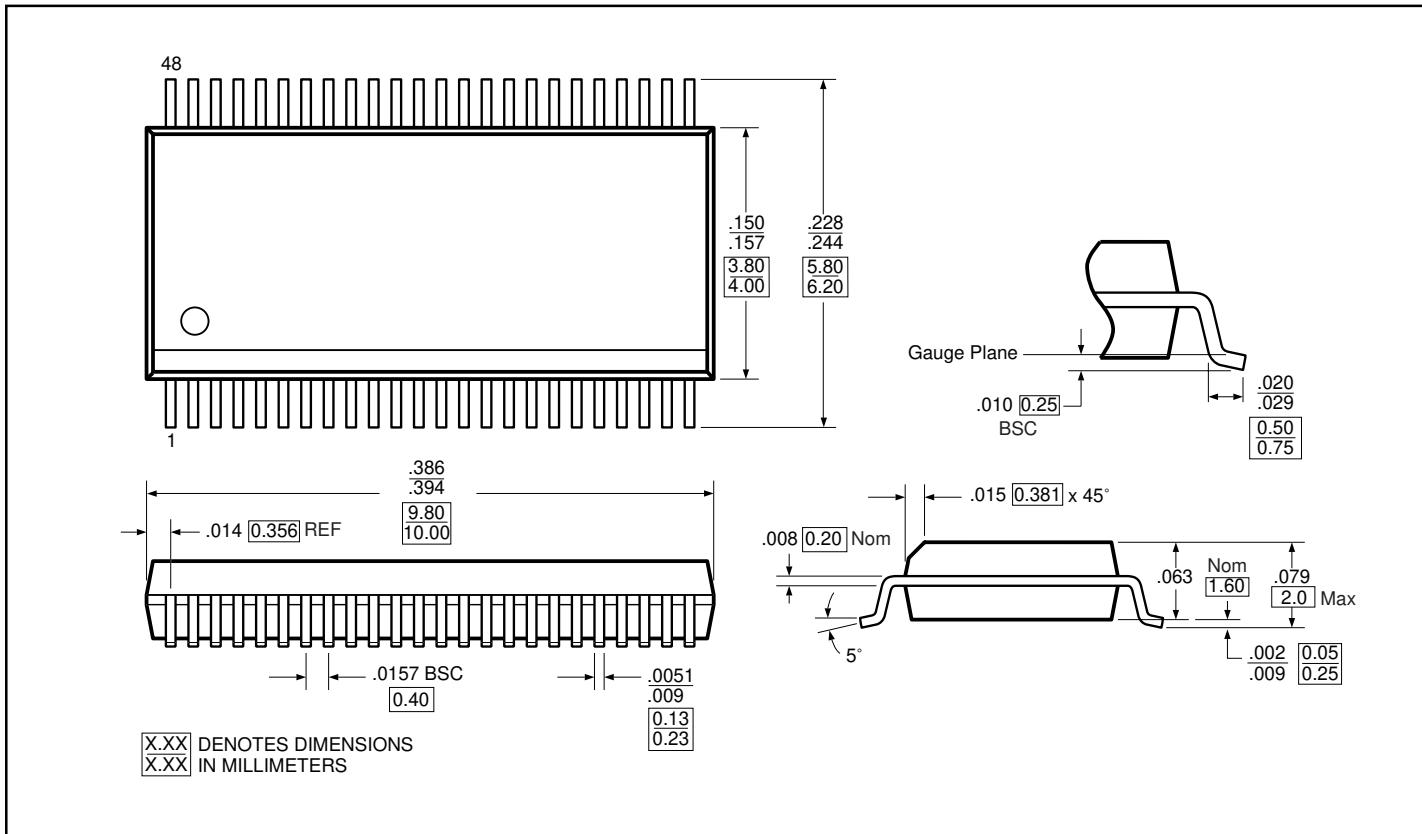
### Notes:

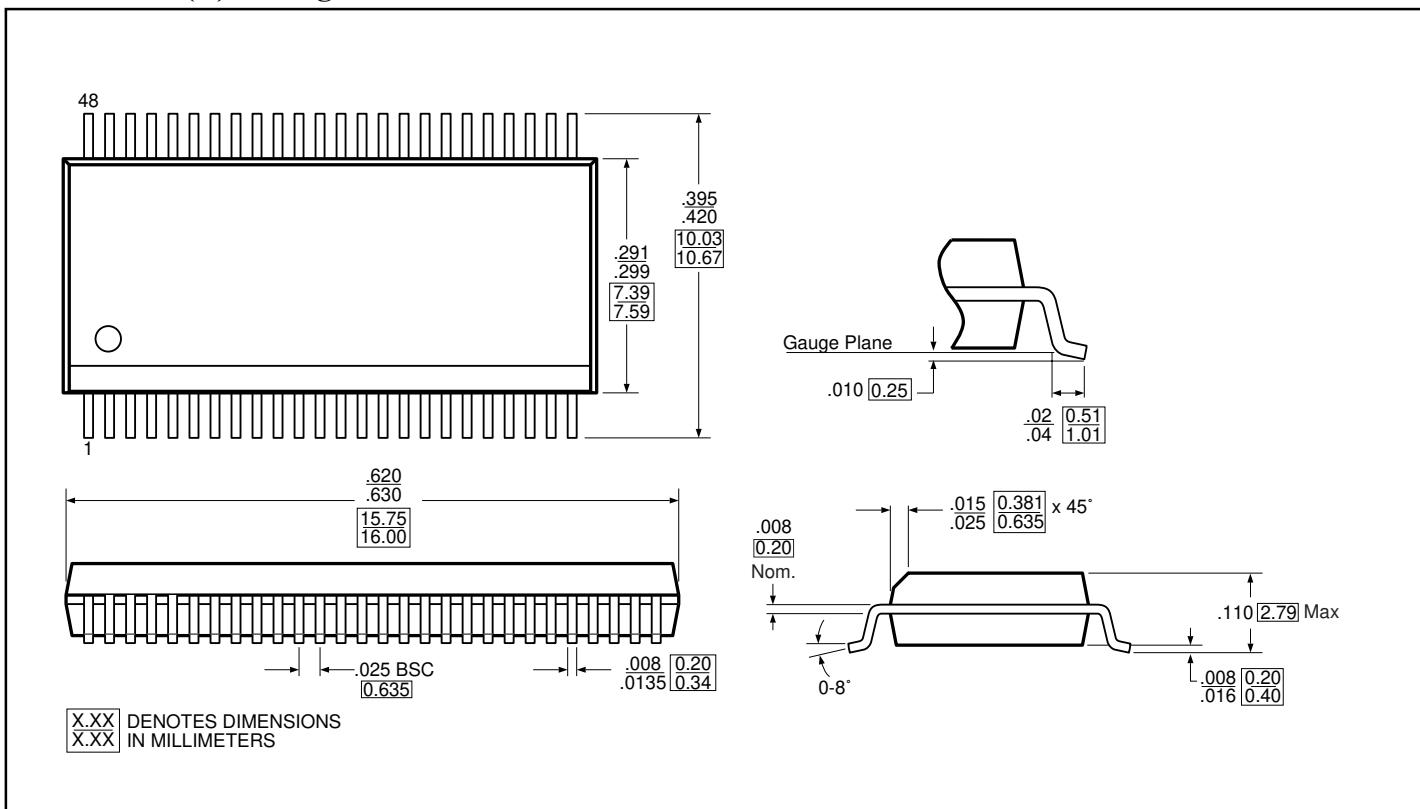
1.  $C_L$  includes probe and jig capacitance.
2. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
3. All input impulses are supplied by generators having the following characteristics:  $PRR \leq MHz$ ,  $Z_O = 50\Omega$ ,  $t_R \leq 2.5ns$ ,  $t_F \leq 2.5ns$ .
4. The outputs are measured one at a time with one transition per measurement.
5.  $t_{TPZ}$  and  $t_{PHZ}$  are the same as  $t_{DIS}$
6.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{EN}$
7.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD}$

## 48-Pin TSSOP (A) Package



## 48-Pin BQSOP (B) Package



**48-Pin SSOP (V) Package**

**Ordering Information**

Part	Pin - Package	Width	Temperature
PI3B16215A	48-TSSOP	240-mil	-40°C to +85°C
PI3B16215B	48-BQSOP	150-mil	
PI3B16215V	48-SSOP	300-mil	

**Applications Information**
**Logic Inputs**

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, IN may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

**Power-Supply Sequencing and Hot Plug Information**

Proper power-supply sequencing is recommended for all CMOS devices. Always apply  $V_{CC}$  before applying  $V_{BIAS}$  and signals to input/output or control pins.

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