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Diodes Incorporated

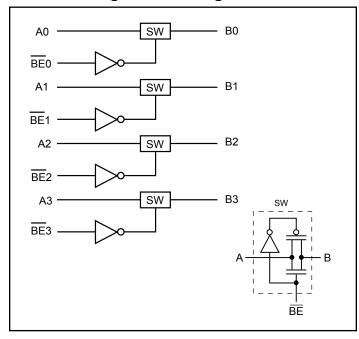


PI3B3125/PI3B3126

3.3V, 4-Bit, 2-Port Nanoswitchw/Individual Enables

Features

- \rightarrow Near zero propagation delay
- \rightarrow 5 Ohm switches connect inputs to outputs
- → Fast Switching Speed 4ns max.
- \rightarrow Ultra Low Quiescent Power (0.1µA Typical)
 - Ideally suited for notebook applications
- → Packages available:
 - 14-pin, SOIC (W)
 - 14-pin, TSSOP (L)
 - 14-pin, TQFN (ZDB)
 - 16-pin, QSOP (Q)



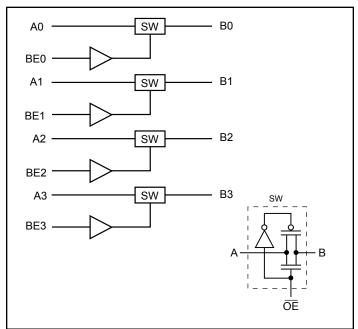
Description

Diodes' PI3B series of logic circuits are produced using the Company's advanced sub micron CMOS technology, achieving industry leading speed grades.

The PI3B3125 and PI3B3126 are 3.3 Volt, 4-bit bus switches designed with four individual 5 Ohm bus switches with fast indiviual enables in an industry standard 74XX125/126 pinout. When enabled via the associated Bus Enable (BE) pin, the "A" pin is directly connected to the "B" pin for that particular gate. The bus switch introduces no additional propagation delay or additional ground bounce noise.

The PI3B3125 device has active LOW enables, and the PI3B3126 has active HIGH enables.

PI3B3126 Logic Block Diagram



PI3B3125 Logic Block Diagram





PI3B3125 14-Pin Product Configuration (SOIC, TSSOP)

BE0	1 14	
A0 🗆	2 13	BE3
В0 🗌	3 12	☐ A3
BE1	4 11	🗆 вз
A1 [5 10	
B1 [6 9	A2
GND 🗌	7 8	🛛 в2
		J

PI3B3125

16-Pin Product Configuration (QSOP)

		-
NC 🗆	1 16	
BE0	2 15	BE3
A0 🗌	3 14	🗅 A3
во 🗆	4 13	🗆 вз
BE1	5 12	
A1 🗌	6 11	A2
B1 [7 10	🛛 в2
GND 🗌	8 9	
		1

Pin Description

Pin Name	Description
BEn	Switch Enable (PI3B3125)
BEn	Switch Enable (PI3B3126)
A3-A0	Bus A
B3-B0	Bus B
V _{CC}	Power
GND	Ground

Truth Table⁽¹⁾

PI3B3125 BEn	PI3B3126 BEn	An	Bn	V _{cc}	Function
X ⁽²⁾	Х	Hi-Z	Hi-Z	GND	Disconnect
Н	L	Hi-Z	Hi-Z	$V_{\rm CC}$	Disconnect
L	Н	Bn	An	V _{CC}	Connect

Notes:

1. H = High Voltage Level, L = Low Voltage Level HI-Z = High Impedance, X = Don't Care

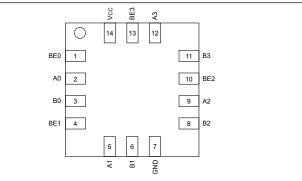
2. A pull-up resistor should be provided for power-up protection.

PI3B3126

14-Pin Product Configuration (SOIC, TSSOP)

Г		1
вео 🗆	1 14	
A0 🗆	2 13] ВЕЗ
во 🗆	3 12] A3
BE1 🗌	4 11] ВЗ
A1 🗌	5 10] BE2
B1 🗌	6 9] A2
GND 🗌	7 8] B2
L		l

PI3B3126	
14-Pin Product Configuration	(TQFN)



PI3B3126 **16-Pin Product Configuration (QSOP)**

		1
NC 🗆	1 16	□ V _{CC}
BE0 🗌	2 15	🗆 ВЕЗ
A0 🗆	3 14] A3
во 🗆	4 13] ВЗ
BE1	5 12] BE2
A1 [6 11] A2
B1 🗌	7 10] B2
GND 🗌	8 9	
		1

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2



Note:



PI3B3125/PI3B3126

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Ambient Temperature with Power Applied	40°C to +85°C
Supply Voltage to Ground Potential	0.5V to +4.6V
DC Input Voltage	-0.5V to +4.6V
DC Output Current	
Power Dissipation	

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics

(Over the Operating Range, $T_A = -40^{\circ}$ C to $+85^{\circ}$ C, $V_{CC} = 3.3$ V $\pm 10\%$)

Parameter	Description	Test Conditions (1)	Min.	Typ. ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
$\mathbf{I}_{_{\mathrm{IH}}}$	Input HIGH Current	$V_{CC} = Max., V_{IN} = Vcc$			±1	MA
I _{IL}	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	MA
I _{OFF}	Off Current	$V_{\rm CC}$ = 0, $V_{\rm OUT}$ = 3 to 3.6V			10	MA
V _{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = -18mA			-1.2	V
D		$V_{\rm CC}$ = Min., $V_{\rm IN}$ = 0.0V, $I_{\rm ON}$ = 48mA or 60mA		5	8	
R _{ON}	Switch On Resistance ⁽³⁾	$V_{\rm CC}$ = Min., $V_{\rm IN}$ = 2.4V, $I_{\rm ON}$ = 15mA		10	17	Ω

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25^{\circ}C$ ambient and maximum loading.

3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, B) pins.

Capacitance

 $(T_A = 25^{\circ}C, f = 1 \text{ MHz})$

Parameter ⁽¹⁾	Description	Test Conditions	Min.	Тур.	Max.	Units
C _{IN}	Input Capacitance	$V_{IN} = 0V$		3.5		pF
C _{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0V$		8		pF

Note:

1. This parameter is determined by device characterization but is not production tested.

Power Supply Chatacteristics

Parameter	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	$V_{\rm CC} = Max.$	V_{IN} = GND or V_{CC}		0.1	3	μA
ΔI_{CC}	Supply Current per Input HIGH	$V_{\rm CC} = Max.$	$V_{\rm IN} = 3.0 V^{(3)}$			750	μA

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.

2. Typical values are at $V_{CC} = 3.3V$, +25°C ambient.

3. Per driven input (control inputs only); A and B pins do not contribute to I_{cc} .





PI3B3125 Switching Characteristics over Operating Range

			PI3B3125			
			Co	Com.		
Parameter	Description	Conditions ⁽¹⁾	Min.	Max.	Units	
t _{plh} t _{phl}	Propagation Delay ^(2,3) Ax to Bx, Bx to Ax	$C_{L} = 50 pF$ $R_{L} = 500\Omega$		0.25	ns	
t _{PZH} t _{PZL}	Bus Enable Time	$C_{L} = 50 pF$ $R_{L} = 500\Omega$	1.0	3.0	ns	
t _{PHZ} t _{PLZ}	Bus Disable Time	$R_L = 500\Omega$	1.0	4.0	ns	

PI3B3126 Switching Characteristics over Operating Range

			PI3B	3126	
			Co	om.	
Parameter	Description	Conditions ⁽¹⁾	Min.	Max.	Units
t _{plh} t _{phl}	Propagation Delay ^(2,3) Ax to Bx, Bx to Ax	$C_{L} = 50 pF$ $R_{L} = 500\Omega$		0.25	ns
t _{PZH} t _{PZL}	Bus Enable Time	$C_{L} = 50 pF$ $R_{L} = 500\Omega$	1.0	2.5	ns
t _{pHZ} t _{pLZ}	Bus Disable Time	$R_{L} = 500\Omega$	1.0	4.0	ns

Notes:

1. See test circuit and waveforms.

This parameter is guaranteed but not tested on Propagation Delays. 2.

3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Applications Information

Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, IN may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

Power-Supply Sequencing and Hot-Plug Information

Proper power-supply sequencing is recommended for all CMOS devices. Always apply V_{CC} and GND before applying signals to input/output or control pins.

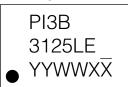
Rail-to-Rail is a registeredtrademark of Nippon Motorola, Ltd.





PI3B3125 Part Marking

L Package



YY: Year WW: Workweek 1st X: Assembly Site Code 2nd X: Fab Site Code

Q Package



YY: Year WW: Workweek 1st G: Assembly Site Code 2nd G: Wafer Fab Site Code

W Package



YY: Year WW: Workweek 1st X: Assembly Site Code 2nd X: Wafer Fab Site Code

PI3B3126 Part Marking

L Package



1st Y: Die Rev 2nd Y: Year W: Workweek 1st X: Assembly Code 2nd X: Fab Code

ZDB Package



YY: Year WW: Workweek 1st X: Assembly Site Code 2nd X: Fab Site Code

Q Package



Y: Year W: Workweek 1st X: Assembly Site Code 2nd X: Fab Site Code

W Package

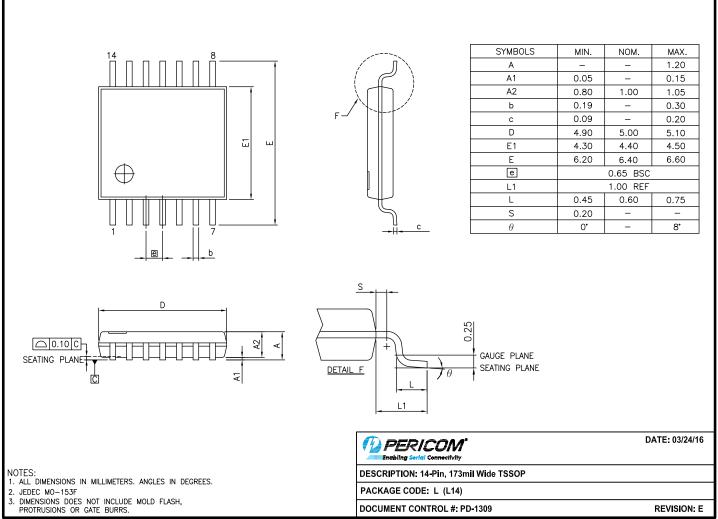


YY: Year WW: Workweek 1st X: Assembly Site Code 2nd X: Wafer Fab Site Code





Packaging Mechanical: 14-TSSOP(L)

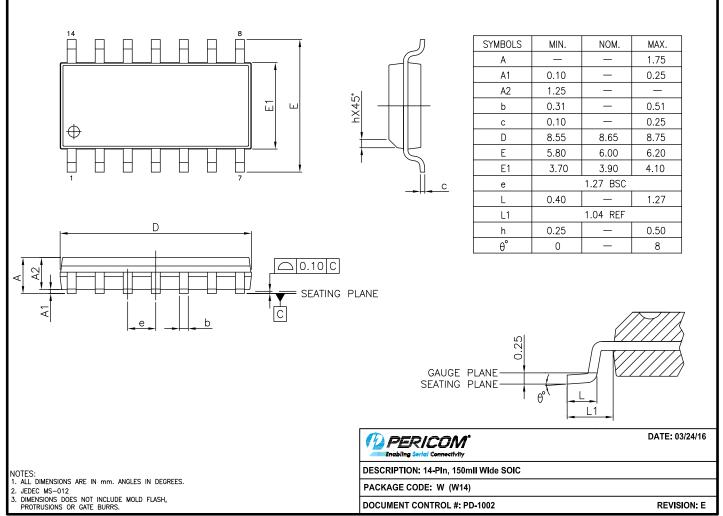


16-0060





Packaging Mechanical: 14-SOIC (W)

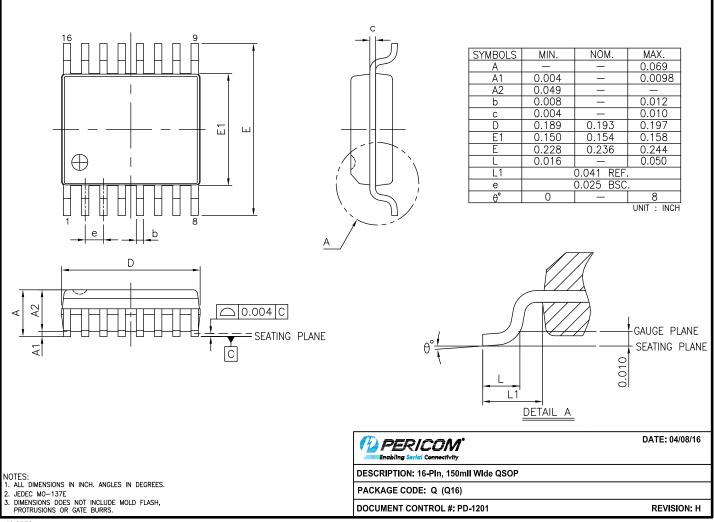


16-0055





Packaging Mechanical: 16-QSOP(Q)

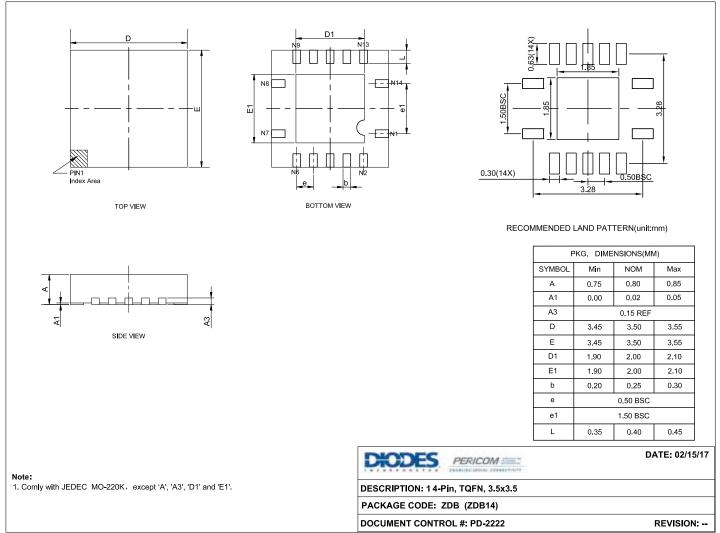


16-0056





Packaging Mechanical: 14-TQFN (ZDB)



17-0029

For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Package Code	Package Description	
PI3B3125LEX	L	14-Pin, 173mil Wide (TSSOP)	
PI3B3125WEX	W	14-Pin, 150mil Wide (SOIC)	
PI3B3125QEX	Q	16-Pin, 150mil Wide(QSOP)	
PI3B3126LEX	L	14-Pin, 173mil Wide (TSSOP)	
PI3B3126WEX	W	14-Pin, 150mil Wide (SOIC)	
PI3B3126QEX	Q	16-Pin, 150mil Wide(QSOP)	
PI3B3126ZDBEX	ZDB	16-Pin, 3.5x3.5 (TQFN)	

Notes:

EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
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