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## Features

- Near-Zero propagation delay
- 5-ohm switches connect inputs to outputs
- High bandwidth ( $>400 \mathrm{MHz}$ )
- Rail-to-rail 2.5 V or 3.3 V switching
- 5V I/O Tolerant
- 2.5 V Supply Voltage Operation
- Permits Hot-Insertion
- Packaging ( $\mathrm{Pb}-\mathrm{free} \&$ Green Available):
- 24-pin 150-mil wide plastic QSOP (Q)
- 24-pin 173-mil wide plastic TSSOP (L)


## Applications

- High bandwidth data switching
- Hot Docking


## Block Diagram



## Truth Table ${ }^{(1)}$

| Function | $\overline{\mathbf{B E}}$ | A0-9 |
| :---: | :---: | :---: |
| Disconnect | H | $\mathrm{Hi}-\mathrm{Z}$ |
| Connect | L | $\mathrm{B}_{0-9}$ |

Note:

1. $\mathrm{H}=$ High Voltage Level, $\mathrm{L}=$ Low Voltage Level Hi-Z = High Impedance

## Description

Pericom Semiconductor's PI3C series of logic circuits are produced using the company's advance submicron CMOS technology, achieving industry leading performance.
The PI3C3861-A is a 10 -bit, 2.5 volt or 3.3 volt, 2-port bus switch designed with a low On-Resistance ( 5 -ohm) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable $(\overline{\mathrm{BE}})$ input signal. This device is very userful in switching signals that have high bandwidth ( $>400 \mathrm{MHz}$ ).

## Pin Configuration



## Pin Description

| Pin Name | Description |
| :---: | :--- |
| $\overline{\mathrm{BE}}$ | Bus Enable Input (Active LOW) |
| $\mathrm{A}_{0-9}$ | Bus A |
| $\mathrm{B}_{0-9}$ | Bus B |
| GND | Ground |
| $\mathrm{V}_{\mathrm{CC}}$ | Power |

Maximum Ratings
(Above which the useful life may be impaired. For user guidelines, not tested.)

|  | Storage Temperature ................................................... $65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| :---: | :---: |
|  | Ambient Temperature with Power Applied ...................... $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
|  | Supply Voltage to Ground Potential (Inputs \& Vcc Only) .. -0.5 V to +4.6 V |
|  | Supply Voltage to Ground Potential (Outputs \& D/O Only) -0.5 V to +4.6 V |
|  | DC Input Voltage .......................................................... -0.5 V to +5.5 V |
|  | DC Output Current................................................................... 120mA |
|  | Power Dissipation ....................................................................... 0.5W |

## Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximumrating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over Operating Range, $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V} \pm 10 \%$ )

| Parameters | Description | Test Conditions ${ }^{(1)}$ | Min. | Typ ${ }^{(2)}$ | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {IH }}$ | Input HIGH Voltage | Guaranteed Logic HIGH Level | 2.0 |  |  | V |
| $\mathrm{V}_{\text {IL }}$ | Input LOW Voltage | Guaranteed Logic LOW Level | $-0.5$ |  | 0.8 |  |
| $\mathrm{I}_{\text {IH }}$ | Input HIGH Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\mathrm{CC}}$ |  |  | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {IL }}$ | Input LOW Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{GND}$ |  |  | $\pm 1$ |  |
| IOZH | High Impedance Output Current | $0 \leq \mathrm{A}, \mathrm{B} \leq \mathrm{V}_{\mathrm{CC}}$ |  |  | $\pm 1$ |  |
| $\mathrm{V}_{\text {IK }}$ | Clamp Diode Voltage | $\mathrm{V}_{\mathrm{CC}}=$ Min., $\mathrm{I}_{\text {IN }}=-18 \mathrm{~mA}$ |  | -0.73 | -1.2 | V |
| $\mathrm{R}_{\mathrm{ON}}$ | Switch On-Resistance ${ }^{(3)}$ | $\mathrm{V}_{\mathrm{CC}}=$ Min., $\mathrm{V}_{\mathrm{IN}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{ON}}=48 \mathrm{~mA}$ |  | 5 | 7 | $\Omega$ |
|  |  | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IN}}=2.4 \mathrm{~V}, \\ & \mathrm{I}_{\mathrm{ON}}=15 \mathrm{~mA} \end{aligned}$ |  | 8 | 15 |  |

## Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ambient and maximum loading.
3. Measured by the voltage drop between $A$ and $B$ pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two $(\mathrm{A}, \mathrm{B})$ pins.

Capacitance $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{f}=1 \mathrm{MHz}\right)$

| Parameters ${ }^{(1)}$ | Description | Test Conditions | Typ. | Units |
| :---: | :---: | :---: | :---: | :---: |
| $\mathrm{C}_{\text {IN }}$ | Input Capacitance | $\mathrm{V}_{\mathrm{IN}}=0 \mathrm{~V}$ | 6.0 | pF |
| CofF | A/B Capacitance, Switch Off |  | 5.0 |  |
| CON | A/B Capacitance, Switch On |  | 10.0 |  |

Notes:

1. This parameter is determined by device characterization but is not production tested.

## Power Supply Characteristics

| Parameters $^{(4)}$ | Description | Test Conditions ${ }^{(1)}$ |  | Min. | Typ. ${ }^{(2)}$ | Max. | Units |
| :---: | :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\mathrm{CC}}$ | Quiescent Power <br> Supply Current | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Max}$. | $\mathrm{V}_{\mathrm{IN}}=\mathrm{GND}$ or $\mathrm{V}_{\mathrm{CC}}$ |  |  |  |  |

## Notes:

1. For Max. or Min.conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V},+25^{\circ} \mathrm{C}$ ambient.
3. Per TTL driven input (control input only); A and B pins do not contribute to $\mathrm{I}_{\mathrm{CC}}$.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

## Switching Characteristics over 3.3V Operating Range

| Parameters | Description | Test Conditions | Com. |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Max. | Units |
| $t_{\text {PLH }}$ tpHL | Propagation Delay ${ }^{(1,2)}$ Ax to Bx, Bx to Ax | $\begin{gathered} \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF} \\ \mathrm{R}_{\mathrm{L}}=500-\mathrm{ohm} \end{gathered}$ |  | 0.25 | ns |
| $\begin{gathered} \mathrm{t}_{\mathrm{PZH}} \\ \mathrm{t}_{\mathrm{PZL}} \\ \hline \end{gathered}$ | Bus Enable Time $\overline{\mathrm{BE}}$ to Ax or Bx |  | 1.5 | 6.5 |  |
| $\begin{aligned} & \mathrm{t}_{\mathrm{PHZ}} \\ & \mathrm{t}_{\mathrm{PLZZ}} \end{aligned}$ | Bus Disable Time $\overline{\mathrm{BE}}$ to Ax or Bx |  | 1.5 | 5.5 |  |

## Notes:

1. This parameter is guaranteed but not tested on Propagation Delays.
2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## Switching Characteristics over 2.5V Operating Range

| Parameters | Description | Test Conditions | Com. |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Max. | Units |
| $\begin{aligned} & \mathrm{t}_{\mathrm{PLH}} \\ & \mathrm{t}_{\mathrm{PHL}} \\ & \hline \end{aligned}$ | Propagation Delay ${ }^{(1,2)}$ <br> Ax to Bx, Bx to Ax | $\begin{gathered} \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF} \\ \mathrm{R}_{\mathrm{L}}=500-\mathrm{ohm} \end{gathered}$ |  | 0.25 | ns |
| $\begin{aligned} & \text { tPZH } \\ & \text { tPZL }^{2} \end{aligned}$ | Bus Enable Time $\overline{\mathrm{BE}}$ to Ax or Bx |  | 1.5 | 9.8 |  |
| $\begin{aligned} & \mathrm{t}_{\mathrm{PHZ}} \\ & \mathrm{t}_{\mathrm{PLZ}} \end{aligned}$ | Bus Disable Time $\overline{\mathrm{BE}}$ to Ax or Bx |  | 1.5 | 8.3 |  |

## Notes:

1. This parameter is guaranteed but not tested on Propagation Delays.
2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.


Output Voltage vs. Input Voltage over Various Supply Voltages

Packaging Mechanical: 24-pin TSSOP (L)


## Packaging Mechanical: 24-pin QSOP (Q)



## Ordering Information

| Ordering Code | Package Code | Package Description |
| :--- | :---: | :--- |
| PI3C3861-ALE | L | Pb-free \& Green, 24-pin TSSOP |
| PI3C3861-AQE | Q | Pb-free \& Green, 24-pin QSOP |

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- $\mathrm{E}=\mathrm{Pb}$-free $\&$ Green
- Adding an X suffix = Tape/Reel

