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# 4-Differential Channel, 2:1 Mux/DeMux, DVI/HDMI Compliant Signal Switch based on TMDS Signaling Standard 

## Features

- 3.3V V DD
- 4-Differential Channel 2:1 Mux/DeMux
- DVI, HDMI, $1.2+1.2$ signal compatible
- Data Rate: 1.65 Gbps
- Supports both AC coupled and DC coupled signals
- Crosstalk: -35dB@825MHz
- Switching speed: 4ns
- Isolation: -30dB@825MHz
- Near-Zero Prop Delay: 250ps max.
- Low bit-to-bit skew
- Enable/Disable Time: 9ns
- Bidirectional
- Enhanced ESD Protection:
- Switch pins: 1.1 kV (machine model) 12 kV (humanbody model) $8 \mathrm{kV}(\text { contact })^{(1)}$
- Packaging (Pb-free \& Green):
-48-pin TSSOP (A)


## Block Diagram



Note:

1. IEC 61000-4-2

## Description

Pericom Semiconductor's PI3HDMI series of switch circuits are targeted for high-resolution video networks that are based on DVI/ HDMI standards, and TMDS signal processing. The PI3HDMI412-B is a 8- to 4-Channel Mux/DeMux Switch. The device multiplexes differential signals to one of two corresponding outputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew and high channel-to-channel noise isolation.
The maximum DVI/HDMI data rate of 1.65 Gbps provides the resolution required by the next generation HDTV and PC graphics. Three differential channels are used for data (video signals for DVI or audio/video signals for HDMI), and one differential channel is used for Clock for decoding the TMDS signals at the outputs.
Because of its passive bidirectional feature, this switch can be used either at the video drivers side or at the receiver side. For PC graphics applications, the device sits at the drivers side to switch between multiple display units such as PC LCD monitor, projector, TV, etc. For consumer video applications, the device sits at the receiver end to switch between the source components such as DVD, D-VHS, STB, etc.

## Pin Configuration

| VDD 1 | 48 | ${ }_{0} \mathrm{~B}_{1}$ |
| :---: | :---: | :---: |
| $A_{0}$ | 47 | ${ }_{1} \mathrm{~B}_{1}$ |
| GND 3 | 46 | GND |
| $\mathrm{A}_{1} 4$ | 45 | ${ }_{0} \mathrm{~B}_{2}$ |
| GND [5 | 44 | ${ }_{1} \mathrm{~B}_{2}$ |
| VDD 6 | 43 | GND |
| GND 4 | 42 | ${ }_{2} \mathrm{~B}_{1}$ |
| $\mathrm{A}_{2} 8$ | 41 | ${ }_{3} B_{1}$ |
| GND 9 | 40 | GND |
| $\mathrm{A}_{3} ¢ 10$ | 39 | ${ }_{2} \mathrm{~B}_{2}$ |
| GND 11 | 38 | ${ }_{3} \mathrm{~B}_{2}$ |
| V 12 | 37 | GND |
| GND 13 | 36 | $V_{D D}$ |
| NC [14 | 35 | ${ }_{4} \mathrm{~B}_{1}$ |
| $\mathrm{A}_{4} 15$ | 34 | ${ }_{5} \mathrm{~B}_{1}$ |
| GND 16 | 33 | GND |
| A5 17 | 32 | $4_{4} \mathrm{~B}_{2}$ |
| GND 18 | 31 | ${ }_{5} \mathrm{~B}_{2}$ |
| VDD [19 | 30 | GND |
| GND 20 | 29 | ${ }_{6} \mathrm{~B}_{1}$ |
| $\mathrm{A}_{6}-21$ | 28 | ${ }_{7} \mathrm{~B}_{1}$ |
| GND 22 | 27 | GND |
| $\mathrm{A}_{7}$ | 26 | ${ }_{6} \mathrm{~B}_{2}$ |
| SEL 24 | 25 | ${ }_{7} \mathrm{~B}_{2}$ |

Truth Table

| Function $^{(\mathbf{1})}$ | SEL |
| :---: | :---: |
| Ax to $\mathrm{xB}_{1}$ | L |
| Ax to $\mathrm{xB}_{2}$ | H |

Note:
$1 \mathrm{x}=0$ through 7

Pin Description

| Pin \# | Pin Name | Type | Description |
| :---: | :---: | :---: | :---: |
| $\begin{gathered} \hline 3,5,7,9,11,13,16, \\ 18,20,22,27,30,3 \\ 3,37,40,43,46 \\ \hline \end{gathered}$ | GND | Power | Ground |
| 1,6,12,19,36 | VDD | Power | Supply Voltage (3.3V $\pm 10 \%$ ) |
| 14 | NC |  | No Connection |
| 2 | A0 | I/O | TMDS Data 0- |
| 4 | A1 | I/O | TMDS Data 0+ |
| 8 | A2 | I/O | TMDS Data 1- |
| 10 | A3 | I/O | TMDS Data 1+ |
| 15 | A4 | I/O | TMDS Data 2- |
| 17 | A5 | I/O | TMDS Data 2+ |
| 21 | A6 | I/O | TMDS Data 3- |
| 23 | A7 | I/O | TMDS Data 3+ |
| 24 | SEL | Input | Selection for Port 1 or Port 2 |
| 25 | 7B2 | I/O | TMDS Data 3+ for Port 2 |
| 26 | 6B2 | I/O | TMDS Data 3- for Port 2 |
| 28 | 7B1 | I/O | TMDS Data 3+ for Port 1 |
| 29 | 6B1 | I/O | TMDS Data 3- for Port 1 |
| 31 | 5B2 | I/O | TMDS Data 2+ for Port 2 |
| 32 | 4B2 | I/O | TMDS Data 2- for Port 2 |
| 34 | 5B1 | I/O | TMDS Data 2+ for Port 1 |
| 35 | 4B1 | I/O | TMDS Data 2- for Port 1 |
| 38 | 3B2 | I/O | TMDS Data 1+ for Port 2 |
| 39 | 2B2 | I/O | TMDS Data 1- for Port 2 |
| 41 | 3B1 | I/O | TMDS Data 1+ for Port 1 |
| 42 | 2B1 | I/O | TMDS Data 1- for Port 1 |
| 44 | 1B2 | I/O | TMDS Data 0+ for Port 2 |
| 45 | 0B2 | I/O | TMDS Data 0-for Port 2 |
| 47 | 1B1 | I/O | TMDS Data 0+ for Port 1 |
| 48 | 0B1 | I/O | TMDS Data 0- for Port 1 |

## Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| :---: | :---: |
| Supply Voltage. | -0.5V to 3.6 V |
| DC Input Voltage | ......... 0 V to $\mathrm{V}_{\mathrm{DD}}$ |
| DC Output Current | .............120mA |
| Power Dissipation | $\ldots . .0 .5 \mathrm{~W}$ |

## Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics for Switching over Operating Range ( $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V} \pm 10 \%$ )

| Paramenter | Description | Test Conditions ${ }^{(\mathbf{1})}$ | Min. | Typ. ${ }^{(2)}$ | Max. |  |
| :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{IH}}{ }^{(3)}$ | Input HIGH Voltage | Guaranteed HIGH level | $0.65\left(\mathrm{~V}_{\mathrm{DD}}\right)$ |  |  | Units |
| $\mathrm{V}_{\mathrm{IL}}{ }^{(3)}$ | Input LOW Voltage | Guaranteed LOW level | -0.5 |  | $0.35\left(\mathrm{~V}_{\mathrm{DD}}\right)$ |  |
| $\mathrm{V}_{\mathrm{IK}}$ | Clamp Diode Voltage | $\mathrm{V}_{\mathrm{DD}}=$ Max., $\mathrm{I}_{\mathrm{IN}}=-18 \mathrm{~mA}$ |  | -0.7 | -1.2 |  |
| IIH | Input HIGH Current | $\mathrm{V}_{\mathrm{DD}}=$ Max., $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\mathrm{DD}}$ |  |  | $\pm 5$ |  |
| I |  |  |  |  |  |  |

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ambient and maximum loading.
3. $\mathrm{V}_{\mathrm{IH}}+\mathrm{V}_{\mathrm{IL}}$ are for SEL input only.

## Power Supply Characteristics

| Parameters | Description | Test Conditions ${ }^{(\mathbf{1 )}}$ | Min. | Typ. ${ }^{(2)}$ | Max. | Units |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\mathrm{CC}}$ | Quiescent Power Supply Current | $\mathrm{V}_{\mathrm{DD}}=$ Max., $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\mathrm{DD}}$ or 0 V |  | 500 |  | $\mu \mathrm{~A}$ |

Dynamic Electrical Characteristics Over the Operating Range ( $\mathrm{T}_{\mathrm{A}}=-40^{\circ}$ to $+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V} \pm 10 \%$ )

| Parameter | Description | Test Conditions | Min. | Typ. ${ }^{(2)}$ | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{X}_{\text {TALK }}$ | Crosstalk | See Fig. 1 for Measurement Setup, $\mathrm{f}=825 \mathrm{MHz}$ |  | -35 |  | dB |
| OIRR | OFF Isolation | See Fig. 2 for Measurement Setup, $\mathrm{f}=825 \mathrm{MHz}$ |  | -30 |  |  |
| DR | Data Rate |  |  | 1.65 |  | Gbps |

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ambient and maximum loading.


Fig 1. Crosstalk Setup

HP4396B


Fig 2. Off-isolation setup


Fig 3. Crosstalk


Fig 4. Off Isolation

Switching Characteristics ( $\mathrm{T}_{\mathrm{A}}=-40^{\circ}$ to $+85^{\circ} \mathrm{C}, \mathrm{GND}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{DD}}=3.3 \mathrm{~V} \pm 10 \%$ )

| Paramenter | Description ${ }^{(1)}$ | Min. | Typ. | Max. | Units |
| :--- | :--- | :---: | :---: | :---: | :---: |
| tPZH, tPZL | Line Enable Time - SEL to AN, BN | 0.5 | - | 8.0 |  |
| tPHZ, tPLZ | Line Disable Time - SEL to AN, BN | 0.5 | - | 4.0 |  |

Notes:

1. For measurement setup, please see "Test Circuit For Electrical Characteristic" on page 6, and "Switching waveforms" on page 7

## Test Circuit for Electrical Characteristics



Notes:

- $\mathrm{C}_{\mathrm{L}}=$ Load capacitance: includes jig and probe capacitance.
- $\mathrm{R}_{\mathrm{T}}=$ Termination resistance: should be equal to Zout of the Pulse Generator
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- All input impulses are supplied by generators having the following characteristics: $\mathrm{PRR} \leq \mathrm{MHz}, \mathrm{Z}_{\mathrm{O}}=50 \Omega, \mathrm{t}_{\mathrm{R}} \leq 2.5 \mathrm{~ns}, \mathrm{t}_{\mathrm{F}} \leq 2.5 \mathrm{~ns}$.
- The outputs are measured one at a time with one transition per measurement.


## Switch Positions

| Test | Switch |
| :--- | :--- |
| tpLZ, $^{\text {tPZL (output on B-side) }}$ | $2 \times\left(\mathrm{V}_{\mathrm{DD}}\right)$ |
| t PHZ, $^{\text {t }}$ (outh |  |
| Prop Delay | Gnd |

## Test Circuit for Dynamic Electrical Characteristics



Switching Waveforms


Voltage Waveforms Enable and Disable Times

## TP3 Rx Test Setup



## TP3 eye diagrams with 3 meter cables



Packaging Mechanical: 48-pin TSSOP (A)


## Ordering Information

| Ordering Code | Package Code | Package Description |
| :---: | :---: | :---: |
| PI3HDMI412-BAE | A | Pb-free \& Green, 48-pin TSSOP |

## Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- $\mathrm{E}=\mathrm{Pb}$-free and Green

