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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









8:1 Mux/DeMux BusSwitch

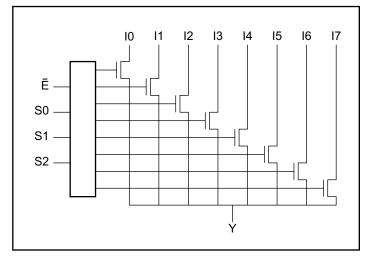
Features:

- · Near-Zero propagation delay
- 5Ω switches connect inputs to outputs
- · Direct bus connection when switches are ON
- Ultra Low Quiescent Power (0.2μA typical)
 - Ideally suited for notebook applications
- Pin compatible with 74 series 251 logic devices
- Packaging (Pb-free & Green available):
 - 16-pin 150-mil wide plastic QSOP (Q)

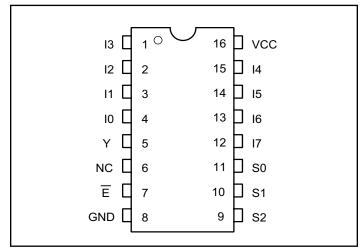
Description:

Pericom Semiconductor's PI5C3251 is a Dual 8:1 Multiplexer/ Demultiplexer with three-state outputs that is pinout compatible with the PI74FCT251T, 74F251, and 74ALS/AS/LS 251. Inputs can be connected to outputs with low on resistance (5Ω) with no additional ground bounce noise or propagation delay.

Block Diagram



Pin Configuration



Truth Table⁽¹⁾

	Sel	ect			
E	S2	S1	S ₀	Y	Function
Н	X	X	X	Hi-Z	Disable
L	L	L	L	10	S2-0 = 0
L	L	L	Н	I1	S2-0 = 1
L	L	Н	L	12	S2-0=2
L	L	Н	Н	13	S2-0=3
L	Н	L	L	I4	S2-0 = 4
L	Н	L	Н	15	S2-0=5
L	Н	Н	L	I6	S2-0 = 6
L	Н	Н	Н	I7	S2-0 = 7

Pin Description

Pin Name	Description	
I0-7	Data Inputs	
S0-2	Select Inputs	
$\overline{\mathrm{E}}$	Enable	
Y	Data Outputs	
GND	Ground	
V _{CC}	Power	

Note:

1. H = High Voltage Level, L = Low Voltage Level

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Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)	0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	0.5V to +7.0V
DC Input Voltage	0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40$ °C to +85°C, $V_{CC} = 5V \pm 5\%$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units	
$V_{ m IH}$	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V	
$V_{ m IL}$	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8		
${ m I}_{ m IH}$	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1		
$I_{ m IL}$	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ	
I_{OZH}	High Impedance Output Current	$0 \le I, Y \le V_{CC}$			±1		
V_{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18 \text{ mA}$		-0.7	-1.2	V	
I_{OS}	Short Circuit Current ⁽³⁾	$I(Y) = 0V, Y(I) = V_{CC}$	100			mA	
V_{H}	Input Hysteresis at Control Pins			150		mV	
D	Switch On-Resistance ⁽⁴⁾	$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48 \text{ mA}$		5	7		
R_{ON}	Switch On-Resistance	$V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15 \text{ mA}$		10	15	Ω	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 5.0V$, $T_A = 25^{\circ}C$ ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between I and Y pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I,Y) pins.

Capacitance ($T_A = 25$ °C, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Тур.	Max.	Units
C_{IN}	Input Capacitance			6	
$C_{I(OFF)}$	I ₀ - I ₇ Capacitance, Switch Off	V 0V		6	nΕ
$C_{Y(OFF)}$	Y Capacitance, Switch Off	$V_{IN} = 0V$		48	pF
$C_{I(ON)}$	I ₀ - I ₇ Capacitance, Switch On		35	54	

Notes:

1. This parameter is determined by device characterization but is not production tested.



Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
Icc	Quiescent Power Supply Current	$V_{CC} = Max.$	$V_{IN} = GND$ or V_{CC}		0.1	3.0	μА
ΔΙcc	Supply Current per Input @ TTL HIGH	$V_{CC} = Max.$	$V_{IN} = 3.4V^{(3)}$			2.5	mA
Iccd	Supply Current per Input per MHz ⁽⁴⁾	$V_{CC} = Max.,$ I and Y Pins Open $\overline{E} = GND$ Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at Vcc = 5.0V, $+25^{\circ}C$ ambient.
- 3. Per TTL driven input ($V_{IN} = 3.4V$, control inputs only); I and Y pins do not contribute to Icc.
- 4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

Parameters	Description	Conditions	Co	Unit	
	Description	Conditions	Min.	Max.	
t_{IY}	Propagation Delay ^(1,2) In to Y			0.25	
t_{SY}	Bus Select Time Sn to Y	CL = 50 pF	0.5	6.6	
t _{PZH} t _{PZL}	Bus Enable Time Ē to Y	$R_{\rm L} = 500\Omega$	0.5	6.0	ns
t _{PHZ}	Bus Disable Time Ē to Y		0.5	6.0	

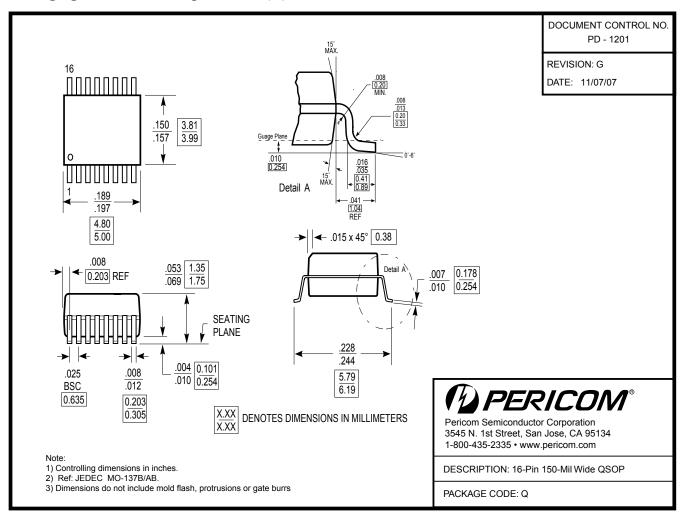
Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

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Packaging Mechanical: 16-pin QSOP (Q)



Ordering Information

Ordering Code	Package Code	Package Description		
PI5C3251QE	Q	Pb-free & Green, 16-pin 150-mil wide QSOP		

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel