

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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PLL Clock Multiplier

Features

- → Zero ppm multiplication error
- → Input crystal frequency range: 5 30MHz
- → Input clock frequency range: 2 50MHz
- → Output clock frequencies up to 200MHz
- → Period jitter ≤ 150ps
- → 9 selectable frequencies
- → Operating voltages: $3.3V \pm 10\%$ and $5.0V \pm 10\%$
- → Tri-state output
- → Packaging (Pb-Free and Green):
 - □ 8-pin SOIC (W)

Description

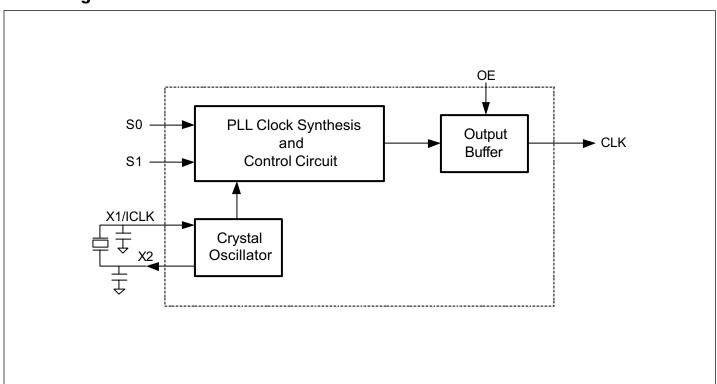
The PI6C4511 is a high-performance frequency multiplier, that integrates analog Phase Lock Loop (PLL).

The PI6C4511 is the most cost effective way to generate a high-quality, high-frequency clock output from a crystal input or clock input. It is designed to replace crystal oscillators in electronic systems, or to be used as clock multiplier and frequency translation.

The complex logic divider generates nine different popular multiplication factors, allowing the device to produce output of many common frequencies.

The device also has an Output Enable pin that tri-states the clock output. The PI6C4511 is intended for applications that needed clock generation and frequency translation with low output jitter.

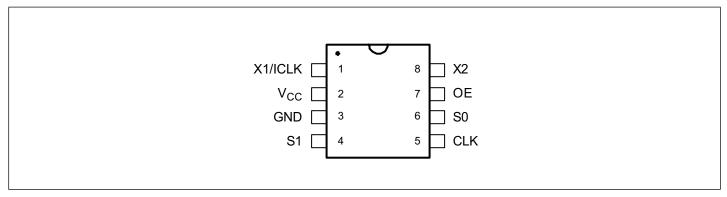
Block Diagram



09-0065 1 www.pericom.com PS8762E 04/01/09



Pin Diagram



Pin Description

Pin	Name	Туре	Description
1	X1/ICLK	X1	Crystal connection or clock input
2	V_{CC}	P	Supply voltage: +3V and +5V
3	GND	P	Connect to Ground
4	S1	TI	Multiplier select pin1. Connect to GND or V _{CC} or float (no connection).
5	CLK	О	Clock output per Table (see below).
6	S0	TI	Multiplier select pin 0. Connect to GND or V _{CC} or float (no connection).
7	OE	I	Output Enable. Tri-state CLK output when low. Internal pull-up
8	X2	XO	Crystal Connection. Leave unconnected for clock input

Clock Output Table

S1	SO	CLK
0	0	x4
0	M	x(16/3)
0	1	x5
M	0	x2.5
M	M	x2
M	1	x(10/3)
1	0	x6
1	M	х3
1	1	x8

M = Mid-point or float



Maximum Ratings (Above which the useful life may be impaired. For user guidelines, not tested)

Storage temperature65 to +150 $^{\circ}$ C
Ambient Operating Temperature0 to +70°C
Supply Voltage to Ground Potential (VCC)0.3 to +7.0V
Inputs (Referenced to GND)0.5 to Vcc+0.5V
Clock Output (Referenced to GND)0.5 to Vcc+0.5V
Soldering Temperature (Max of 10 seconds)+260°C

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operation Conditions

Symbol	Description	Test Condition	Min.	Тур.	Max.	Units
V _{CC}	Supply Voltage		3		5.5	V
V_{IH}	HIGH Level Input Voltage		2			V
$V_{\rm IL}$	LOW Level Input Voltage				0.8	V
T _A	Operating Temperature		0		+70	°C

DC Electrical Characteristics (VCC = $3.3V\pm 10\%$ and $5.0V\pm 10\%$, TA= 0° C to $+70^{\circ}$ C, unless noted)

Symbol	Description	Test Condition	Pin	Min.	Тур.	Max.	Units
V _{CC}	Supply Voltage		V _{CC}	3		5.5	V
I _{CC}	Supply Current	No load, 20MHz crystal			12	20	mA
37	I I I I IIIOII		ICLK	V _{CC/2} +1			V
V_{IH}	Input Logic HIGH		OE	2			V
37	Input Logic LOW		ICLK			V _{CC/2} -1	V
$V_{\rm IL}$			OE			0.8	V
V_{IH}	Input Logic HIGH			V _{CC} -0.5			V
V _{IM}	Input mid-Level		S0, S1		V _{CC/2}		V
$V_{\rm IL}$	Input Low Level					0.5	V
V _{OH}	HIGH-level output voltage	$I_{OH} = -12mA$	CLK	2.4			V
V _{OL}	LOW-level output voltage	$I_{OL} = 12mA$	CLK			0.4	V



AC Electrical Characteristics (VCC = $3.3V \pm 10\%$ and $5.0V \pm 10\%$, TA= 0° C to $+70^{\circ}$ C, unless noted)

Sym.	Parameter	Test Condition	Pin	Min.	Typ.	Max.	Unit
F _{INX}	Input Frequency	Crystal	ICLK	5		30	MHz
F _{INC}	Input Frequency	Clock input	ICLK	2		50	MHz
F _{OUT}	Output Frequency		CLK			200	MHz
T _r	Output clock rise time	0.8 to 2.0V			1		ns
T _f	Output clock fall time	2.0 to 0.8V	CLK		1		ns
T	Output clock duty cycle	At $V_{CC}/2$, $f \le 150MHz$	CLK 45 40	50	55		
T_{DC}		At $V_{CC}/2$, $150MHz \le f \le 200MHz$		40	50	60	70
BW	PLL bandwidth			10			kHz
TOE	Output enable time	OE high to output on				50	ns
T _{OT}	Output disable time	OE low to tri-state				50	
Трј	Period Jitter	70MHz to 200MHz	CLK		75	150	ps
Тррј	Output Peak-to-peak Jitter	40 to 150MHz	CLK		100	300	ps

Recommended Crystal

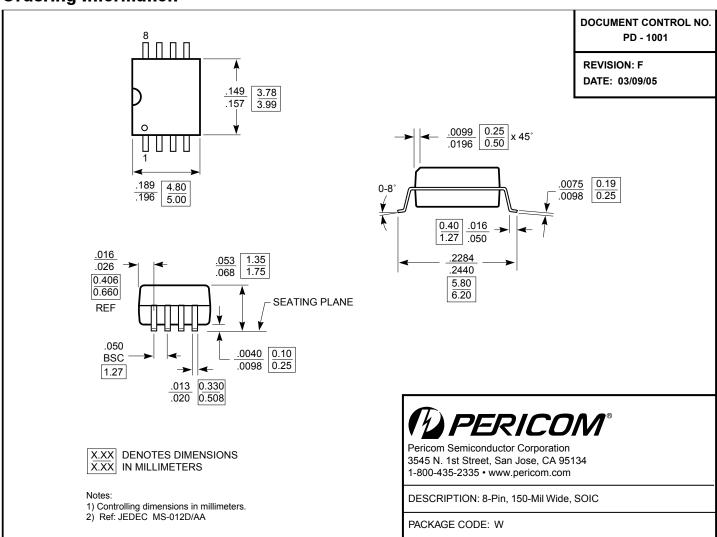
Pericom recommends the Pericom 49S SMD series crystal, which is a low cost, low profile SMD crystal packaged in a HC-49/u short SMD package.

Recommended Crystal Specifications

Parameter	Value	Units
Mode of Oscillation	Fundamental	AT
Frequency	5 - 30	MHz
Frequency Tolerance	±50	PPM
Temperature and Aging Stability	±50	PPM
CO/CI Ratio	240	
Load Cap	18	pF
Equivalent Series Resistance	30	Ω



Ordering Information



Ordering Information

Ordering Code	Package Code	Package Description
PI6C4511WE	W	Pb-Free & Green 8-pin SOIC

- 1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- 2. E = Pb-free and Green
- 3. Adding an X suffix = tape/reel