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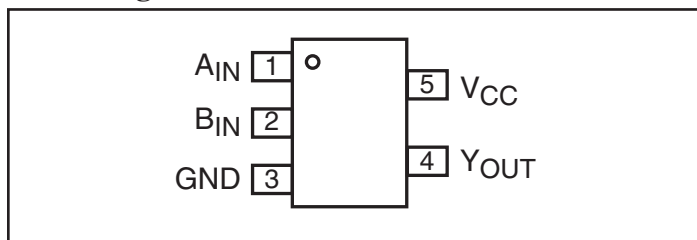
Features

- High-speed: $t_{PD} = 1.8\text{ns}$ typical
- Broad operating range: $V_{CC} = 1.8\text{V} - 3.6\text{V}$
- Power down high-impedance inputs/outputs
- High output drive: $\pm 24\text{mA}$ at $3\text{V } V_{CC}$
- Packaging (Pb-free & Green available):
 - 5-pin SOT23 (T)
 - 5-pin SC70 (C)

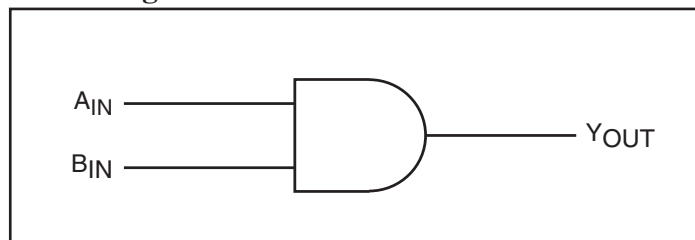
Description

The PI74ST1G08 is a 2-Input AND Gate that operates over the 1.8V to 3.6V V_{CC} operating range.

Pin Configuration



Block Diagram



Pin Description

Pin Name	Description
A_{IN}	Input
B_{IN}	Input
Y_{OUT}	Output

Function Table

Inputs		Outputs
A_{IN}	B_{IN}	Y_{OUT}
L	L	L
L	H	L
H	L	L
H	H	H

Notes:

H = HIGH Logic Level
L = LOW Logic Level

Recommended Operating Conditions⁽¹⁾

Parameter	Condition	Min.	Max.	Units
Supply Voltage (V_{CC})		1.8	3.6	V
Input Voltage (V_{IN})		0	5.5	
Output Voltage (V_{OUT})		0	V_{CC}	
Operating Temperature		-40	85	°C
Input Rise and Fall Time (t_r, t_f)	$V_{CC} = 1.8\text{V}, 2.5\text{V} \pm 0.2\text{V}$	0	20	ns/V
	$V_{CC} = 3.3\text{V} \pm 0.3\text{V}$	0	10	

Notes:

1. Unused inputs must be held HIGH or LOW. They may not float.

Absolute Maximum Ratings

Supply Voltage (V_{CC})		-0.5V to +4V
DC Input Voltage (V_{IN})		-0.5V to +6V
DC Output Voltage (V_{OUT})		-0.5V to +6V
DC Input Diode Current (I_{IK})		-50mA to 20mA
DC Output Diode Current (I_{OK})		-50mA to 20mA
DC Output Current (I_{OUT})		±50mA
DC V_{CC} /GND Current (I_{CC}/I_{GND})		±50mA
Storage Temperature (T_{STG})		-65°C to +150°C
Junction Lead Temperature (I_{OS})		200°C
Power Dissipation SOT23		200mW
SC70		150mW

Note:

Absolute maximum ratings are DC values beyond which the device may be damaged or have its useful life impaired. The datasheet specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Pericom does not recommend operation outside datasheet specifications.

DC Electrical Characteristics (Over supply voltage and operating temperature ranges, unless otherwise specified)

Symbol	Parameter	$V_{CC}(V)$	Conditions	$T_A = +25^\circ C$			$T_A = -40^\circ C$ to $+85^\circ C$		Units
				Min.	Typ.	Max.	Min.	Max.	
V_{IH}	HIGH Level Input Voltage	1.8 2.3-3.6		0.75 V_{CC} 0.70 V_{CC}			0.75 V_{CC} 0.70 V_{CC}		
V_{IL}	LOW Level Input Voltage	1.8 2.3-3.6				0.25 V_{CC} 0.30 V_{CC}		0.25 V_{CC} 0.30 V_{CC}	
V_{OH}	HIGH Level Output Voltage	1.8	$V_{IN} = V_{IH}$	$I_{OH} = -100\mu A$	1.7	1.79		1.7	
		2.3			2.2	2.29		2.2	
		3.0			2.9	2.99		2.9	
		2.3 3.0 3.0		$I_{OH} = -8mA$ $I_{OH} = -16mA$ $I_{OH} = -24mA$	1.9 2.4 2.3	2.13 2.71 2.55		1.9 2.4 2.3	
V_{OL}	LOW Level Out-put Voltage	1.8	$V_{IN} = V_{IL}$	$I_{OL} = 100\mu A$		0.01	0.1		0.1
		2.3				0.01	0.1		0.1
		3.0				0.00	0.1		0.1
		2.3 3.0 3.0		$I_{OH} = 8mA$ $I_{OH} = 16mA$ $I_{OH} = 24mA$		0.10 0.18 0.28	0.3 0.4 0.55		0.3 0.4 0.55
I_{IN}	Input Leakage Current	0-3.6	$V_{IN} = 5.5V, GND$		-1		1	-1	1
I_{OFF}	Power Off Leakage Current	0.0	V_{IN} or $V_{OUT} = 5.5V$		-1		1	-1	1
I_{CC}	Quiescent Supply Current	1.8-3.6	$V_{IN} = 5.5V, GND$			2.0			20

AC Electrical Characteristics

Symbol	Parameter	V _{CC} (V)	Conditions	T _A = +25°C			T _A = -40° C to +85°C		Units	Fig. No.
				Min.	Typ.	Max.	Min.	Max		
t _{PLH} t _{PHL}	Propagation Delay	1.8 2.5±0.2 3.3±0.3	C _L = 15pF, R _L = 1MΩ	2.2 0.8 0.5	3.0 1.9 1.3	4.0 2.6 2.2	2.0 0.8 0.5	4.4 2.8 2.5	ns	1 3
t _{PLH} t _{PHL}	Propagation Delay	3.3±0.3	C _L = 50pF, R _L = 500Ω	1.5	2.9	3.8	1.5	4.2		1 3
C _{IN}	Input Capacitance	V _{IN} = 0			4				pF	
C _{PD} ⁽¹⁾	Power Dissipation Capacitance	3.3	Note 3		20					2

Notes:

- C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output loading and operating at 50% duty cycle (see Figure 2). C_{PD} is related to I_{CCD} dynamic operating current by the expression:

$$I_{CCD} = (C_{PD})(V_{CC})(f_{IN}) + (I_{CC} \text{ static}).$$

AC Loading and Waveforms

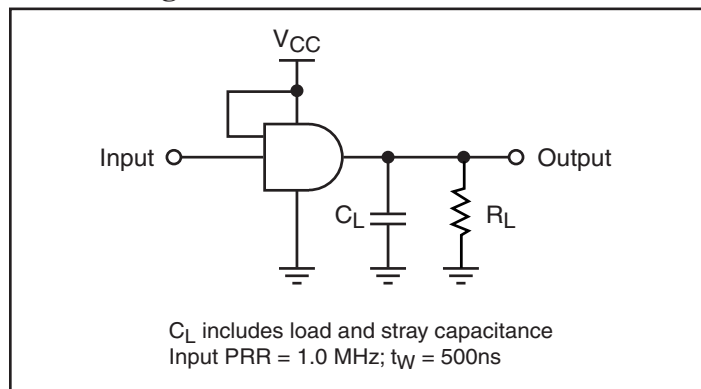


Figure 1. AC Test Circuit

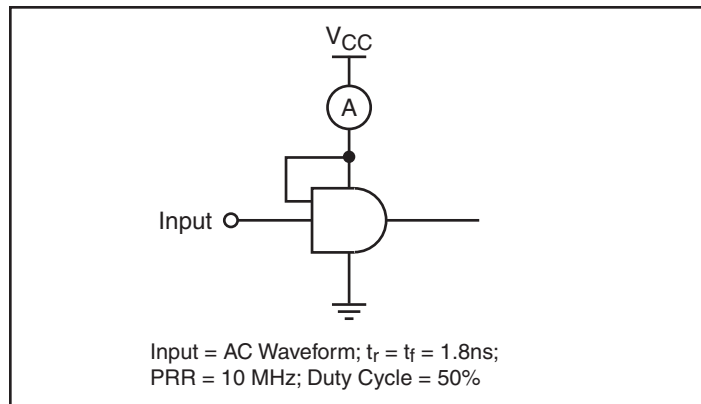


Figure 2. I_{CCD} Test Circuit

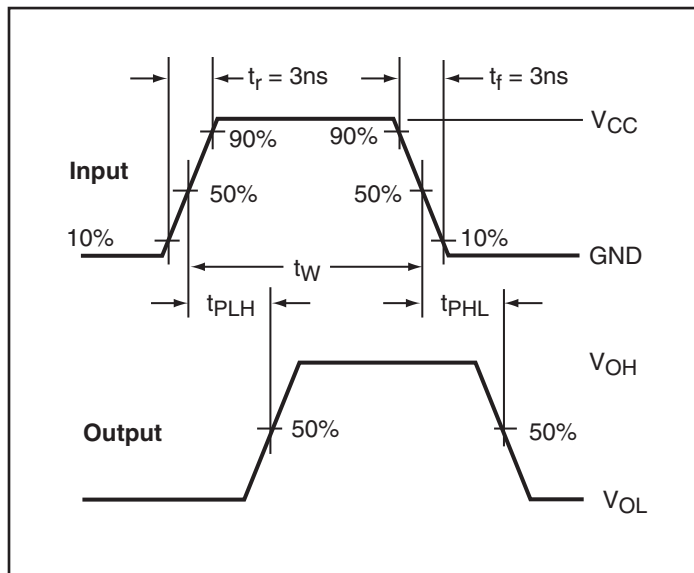
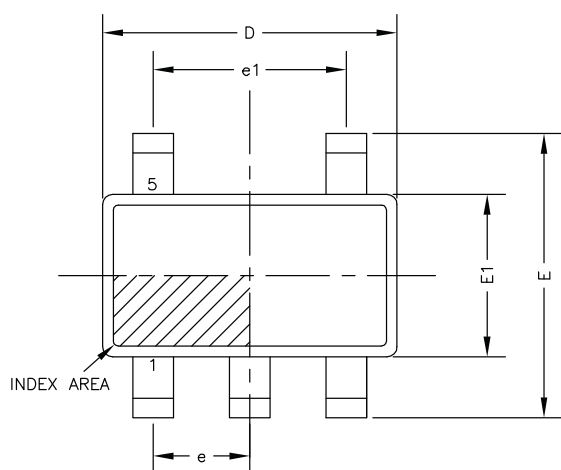
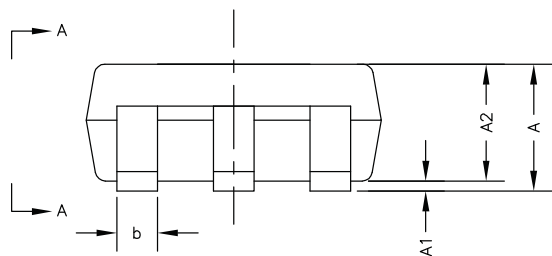


Figure 3. AC Waveforms



TOP VIEW

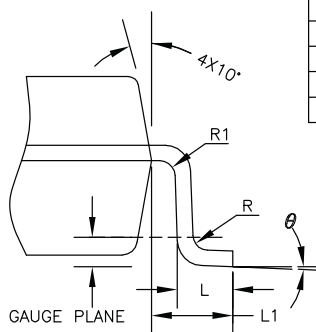


SIDE VIEW

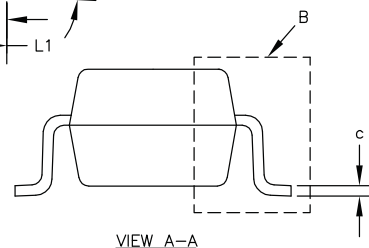
NOTE :

1. ALL DIMENSIONS IN MILLIMETERS. ANGLES IN DEGREES.
2. DIMENSIONS EXCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
3. REFER EIAJ SC74A AND JEDEC MO-178.

SYMBOLS	MIN.	NOM.	MAX.
A	—	—	1.45
A1	0.00	—	0.15
A2	0.90	1.15	1.30
b	0.35	—	0.50
c	0.08	—	0.22
D	2.80	2.90	3.00
E	2.60	2.80	3.00
E1	1.50	1.60	1.75
L	0.30	0.45	0.60
L1	0.60 REF		
R	0.10	—	—
R1	0.10	—	0.25
θ	0°	4°	8°
e	0.95 BSC		
e1	1.90 BSC		



DETAIL B



VIEW A-A



DATE: 10/19/09

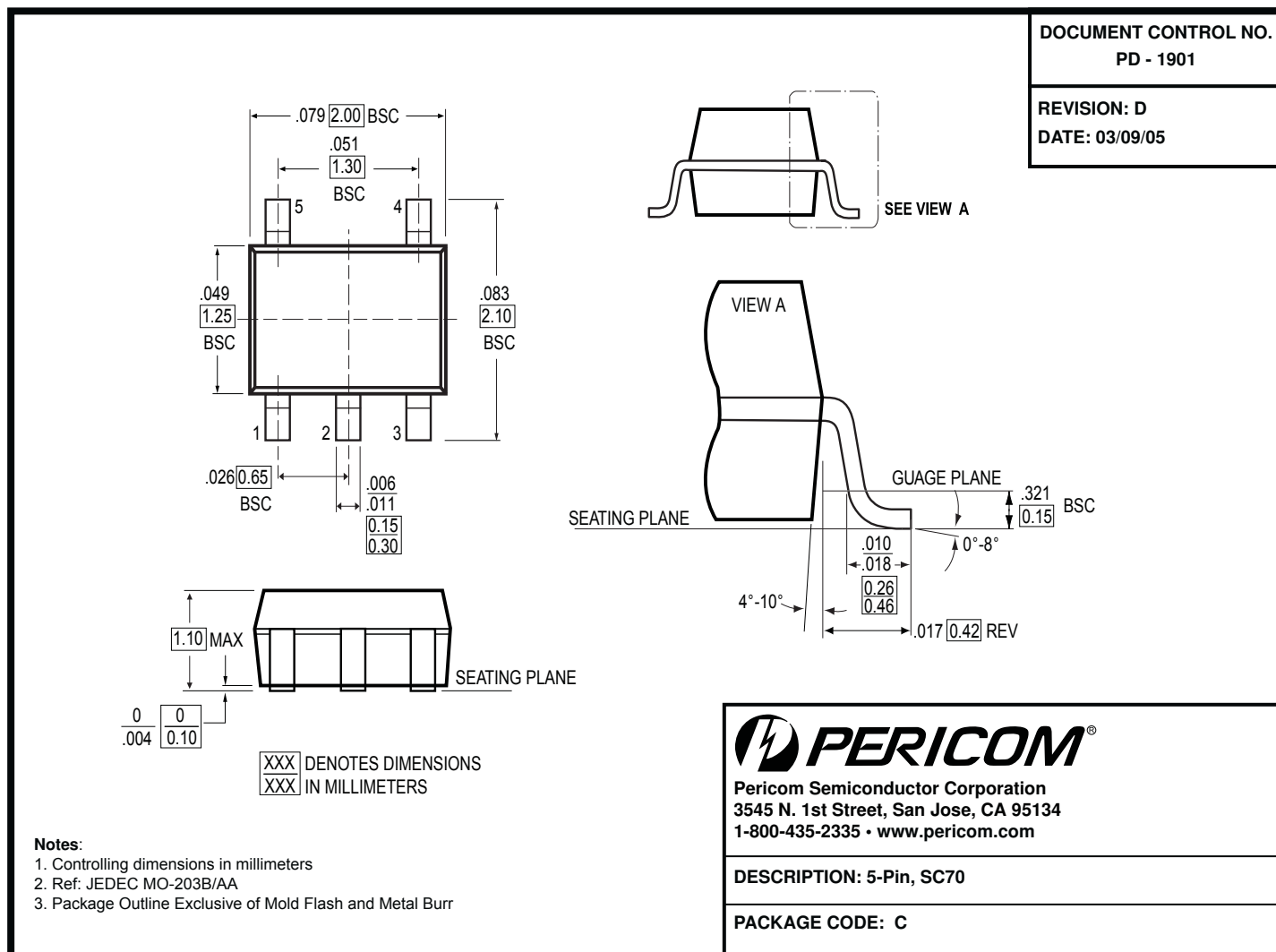
DESCRIPTION: 5-pin, Small Outline Transistor Plastic Package (SOT23)

PACKAGE CODE: T (T5)

DOCUMENT CONTROL #: PD-1911

REVISION: C

09-0130


Note:

- For latest package info, please check: <http://www.pericom.com/products/packaging/mechanicals.php>

Ordering Information

Ordering Code	Package Code	Package Description	Top Marking
PI74ST1G08TEX	T	Pb-free & Green, 5-pin SOT23	A8
PI74ST1G08CEX	C	Pb-free & Green, 5-pin SC70	A8

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- X Suffix = Tape and Reel