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# PIC32MX5XX/6XX/7XX

## 32-bit Microcontrollers (up to 512 KB Flash and 128 KB SRAM) with Graphics Interface, USB, CAN, and Ethernet

### Operating Conditions

- 2.3V to 3.6V, -40°C to +105°C, DC to 80 MHz

### Core: 80 MHz/105 DMIPS MIPS32® M4K®

- MIPS16e® mode for up to 40% smaller code size
- Code-efficient (C and Assembly) architecture
- Single-cycle (MAC) 32x16 and two-cycle 32x32 multiply

### Clock Management

- 0.9% internal oscillator (on some variants)
- Programmable PLLs and oscillator clock sources
- Fail-Safe Clock Monitor (FSCM)
- Independent Watchdog Timer
- Fast wake-up and start-up

### Power Management

- Low-power management modes (Sleep and Idle)
- Integrated Power-on Reset, Brown-out Reset
- 0.5 mA/MHz dynamic current (typical)
- 41 µA IPD current (typical)

### Graphics Features

- External graphics interface with up to 34 Parallel Master Port (PMP) pins:
  - Interface to external graphics controller
  - Capable of driving LCD directly with DMA and internal or external memory

### Analog Features

- ADC Module:
  - 10-bit 1 Msps rate with one Sample and Hold (S&H)
  - 16 analog inputs
  - Can operate during Sleep mode
- Flexible and independent ADC trigger sources
- Comparators:
  - Two dual-input Comparator modules
  - Programmable references with 32 voltage points

### Timers/Output Compare/Input Capture

- Five General Purpose Timers:
  - Five 16-bit and up to two 32-bit Timers/Counters
- Five Output Compare (OC) modules
- Five Input Capture (IC) modules
- Real-Time Clock and Calendar (RTCC) module

### Communication Interfaces

- USB 2.0-compliant Full-Speed OTG controller
- 10/100 Mbps Ethernet MAC with MII and RMII interface
- CAN module:
  - 2.0B Active with DeviceNet™ addressing support
- Six UART modules (20 Mbps):
  - Supports LIN 2.1 protocols and IrDA® support
- Up to four 4-wire SPI modules (25 Mbps)
- Up to five I<sup>2</sup>C modules (up to 1 Mbaud) with SMBus support
- Parallel Master Port (PMP)

### Direct Memory Access (DMA)

- Up to eight channels of hardware DMA with automatic data size detection
- 32-bit Programmable Cyclic Redundancy Check (CRC)
- Six additional channels dedicated to USB, Ethernet and CAN modules

### Input/Output

- 15 mA or 10 mA source/sink for standard VOH/VOL and up to 22 mA for non-standard VOH1
- 5V-tolerant pins
- Selectable open drain and pull-ups
- External interrupts

### Qualification and Class B Support

- AEC-Q100 REVH (Grade 2 -40°C to +105°C)
- Class B Safety Library, IEC 60730

### Debugger Development Support

- In-circuit and in-application programming
- 4-wire MIPS® Enhanced JTAG interface
- Unlimited program and six complex data breakpoints
- IEEE 1149.2-compatible (JTAG) boundary scan

### Packages

Type	QFN	TQFP		TFBGA	VTLA	
Pin Count	64	64	100	100	121	124
I/O Pins (up to)	51	51	83	83	83	83
Contact/Lead Pitch	0.50	0.50	0.40	0.50	0.80	0.50
Dimensions	9x9x0.9	10x10x1	12x12x1	14x14x1	10x10x1.1	9x9x0.9

**Note:** All dimensions are in millimeters (mm) unless specified.

# PIC32MX5XX/6XX/7XX

**TABLE 1: PIC32MX5XX USB AND CAN FEATURES**

USB and CAN																
Device	Pins	Program Memory (KB)	Data Memory (KB)	USB	CAN	Timers/Capture/Compare	DMA Channels (Programmable/Dedicated)	UART <sup>(2,3)</sup>	SPI <sup>(3)</sup>	I <sup>2</sup> C <sup>(3)</sup>	10-bit 1 Msps ADC (Channels)	Comparators	PMP/PSP	JTAG	Trace	Packages <sup>(4)</sup>
PIC32MX534F064H	64	64 + 12 <sup>(1)</sup>	16	1	1	5/5/5	4/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX564F064H	64	64 + 12 <sup>(1)</sup>	32	1	1	5/5/5	4/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX564F128H	64	128 + 12 <sup>(1)</sup>	32	1	1	5/5/5	4/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX575F256H	64	256 + 12 <sup>(1)</sup>	64	1	1	5/5/5	8/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX575F512H	64	512 + 12 <sup>(1)</sup>	64	1	1	5/5/5	8/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX534F064L	100	64 + 12 <sup>(1)</sup>	16	1	1	5/5/5	4/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX564F064L	100	64 + 12 <sup>(1)</sup>	32	1	1	5/5/5	4/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX564F128L	100	128 + 12 <sup>(1)</sup>	32	1	1	5/5/5	4/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX575F256L	100	256 + 12 <sup>(1)</sup>	64	1	1	5/5/5	8/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX575F512L	100	512 + 12 <sup>(1)</sup>	64	1	1	5/5/5	8/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG

**Legend:** PF, PT = TQFP    MR = QFN    BG = TFBGA    TL = VTLA<sup>(5)</sup>

**Note 1:** This device features 12 KB boot Flash memory.

**2:** CTS and RTS pins may not be available for all UART modules. Refer to the “[Device Pin Tables](#)” section for more information.

**3:** Some pins between the UART, SPI and I<sup>2</sup>C modules may be shared. Refer to the “[Device Pin Tables](#)” section for more information.

**4:** Refer to [34.0 “Packaging Information”](#) for more information.

**5:** 100-pin devices in the VTLA package are available upon request. Please contact your local Microchip Sales Office for details.

# PIC32MX5XX/6XX/7XX

**TABLE 2: PIC32MX6XX USB AND ETHERNET FEATURES**

USB and Ethernet																
Device	Pins	Program Memory (KB)	Data Memory (KB)	USB	Ethernet	Timers/Capture/Compare	DMA Channels (Programmable/Dedicated)	UART <sup>(2,3)</sup>	SPI <sup>(3)</sup>	I <sup>2</sup> C <sup>(3)</sup>	10-bit 1 Msps ADC (Channels)	Comparators	PMP/PSP	JTAG	Trace	Packages <sup>(4)</sup>
PIC32MX664F064H	64	64 + 12 <sup>(1)</sup>	32	1	1	5/5/5	4/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX664F128H	64	128 + 12 <sup>(1)</sup>	32	1	1	5/5/5	4/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX675F256H	64	256 + 12 <sup>(1)</sup>	64	1	1	5/5/5	8/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX675F512H	64	512 + 12 <sup>(1)</sup>	64	1	1	5/5/5	8/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX695F512H	64	512 + 12 <sup>(1)</sup>	128	1	1	5/5/5	8/4	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX664F064L	100	64 + 12 <sup>(1)</sup>	32	1	1	5/5/5	4/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX664F128L	100	128 + 12 <sup>(1)</sup>	32	1	1	5/5/5	4/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX675F256L	100	256 + 12 <sup>(1)</sup>	64	1	1	5/5/5	8/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX675F512L	100	512 + 12 <sup>(1)</sup>	64	1	1	5/5/5	8/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG, TL
PIC32MX695F512L	100	512 + 12 <sup>(1)</sup>	128	1	1	5/5/5	8/4	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG, TL

**Legend:** PF, PT = TQFP      MR = QFN      BG = TFBGA      TL = VTLA<sup>(5)</sup>

**Note 1:** This device features 12 KB boot Flash memory.

**Note 2:** CTS and RTS pins may not be available for all UART modules. Refer to the “[Device Pin Tables](#)” section for more information.

**Note 3:** Some pins between the UART, SPI and I<sup>2</sup>C modules may be shared. Refer to the “[Device Pin Tables](#)” section for more information.

**Note 4:** Refer to [34.0 “Packaging Information”](#) for more information.

**Note 5:** 100-pin devices other than those listed here are available in the VTLA package upon request. Please contact your local Microchip Sales Office for details.

# PIC32MX5XX/6XX/7XX

**TABLE 3: PIC32MX7XX USB, ETHERNET, AND CAN FEATURES**

USB, Ethernet, and CAN																	
Device	Pins	Program Memory (KB)	Data Memory (KB)	USB	Ethernet	CAN	Timers/Capture/Compare	DMA Channels (Programmable/Dedicated)	UART <sup>(2,3)</sup>	SPI <sup>(3)</sup>	I <sup>2</sup> C <sup>(3)</sup>	10-bit 1 Msps ADC (Channels)	Comparators	PMP/PSP	JTAG	Trace	Packages <sup>(4)</sup>
PIC32MX764F128H	64	128 + 12 <sup>(1)</sup>	32	1	1	1	5/5/5	4/8	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX775F256H	64	256 + 12 <sup>(1)</sup>	64	1	1	2	5/5/5	8/8	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX775F512H	64	512 + 12 <sup>(1)</sup>	64	1	1	2	5/5/5	8/8	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX795F512H	64	512 + 12 <sup>(1)</sup>	128	1	1	2	5/5/5	8/8	6	3	4	16	2	Yes	Yes	No	PT, MR
PIC32MX764F128L	100	128 + 12 <sup>(1)</sup>	32	1	1	1	5/5/5	4/6	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX775F256L	100	256 + 12 <sup>(1)</sup>	64	1	1	2	5/5/5	8/8	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX775F512L	100	512 + 12 <sup>(1)</sup>	64	1	1	2	5/5/5	8/8	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG
PIC32MX795F512L	100	512 + 12 <sup>(1)</sup>	128	1	1	2	5/5/5	8/8	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG, TL

**Legend:** PF, PT = TQFP    MR = QFN    BG = TFBGA    TL = VTLA<sup>(5)</sup>

**Note 1:** This device features 12 KB boot Flash memory.

**2:** CTS and RTS pins may not be available for all UART modules. Refer to the “[Device Pin Tables](#)” section for more information.

**3:** Some pins between the UART, SPI and I<sup>2</sup>C modules may be shared. Refer to the “[Device Pin Tables](#)” section for more information.

**4:** Refer to [Section 34.0 “Packaging Information”](#) for more information.

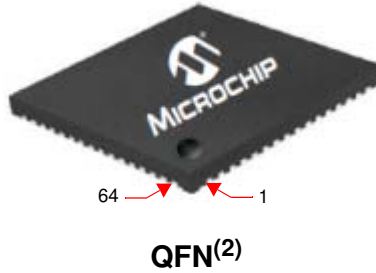
**5:** 100-pin devices other than those listed here are available in the VTLA package upon request. Please contact your local Microchip Sales Office for details.

## Device Pin Tables

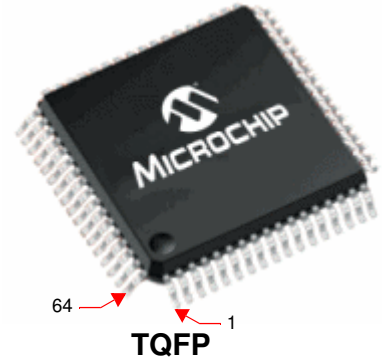
**TABLE 4: PIN NAMES FOR 64-PIN USB AND CAN DEVICES**

### 64-PIN QFN<sup>(2)</sup> AND TQFP (TOP VIEW)

**PIC32MX534F064H**  
**PIC32MX564F064H**  
**PIC32MX564F128H**  
**PIC32MX575F256H**  
**PIC32MX575F512H**



**QFN<sup>(2)</sup>**



**TQFP**

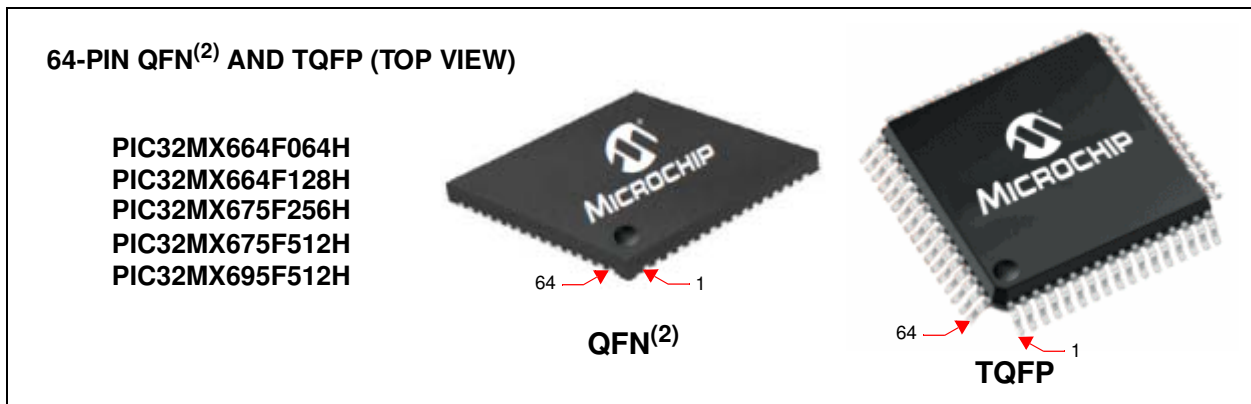
Pin #	Full Pin Name	Pin #	Full Pin Name
1	PMD5/RE5	33	USBID/RF3
2	PMD6/RE6	34	VBUS
3	PMD7/RE7	35	VUSB3V3
4	SCK2/U6TX/U3RTS/PMA5/CN8/RG6	36	D-/RG3
5	SDA4/SDI2/U3RX/PMA4/CN9/RG7	37	D+/RG2
6	SCL4/SDO2/U3TX/PMA3/CN10/RG8	38	VDD
7	MCLR	39	OSC1/CLKI/RC12
8	SS2/U6RX/U3CTS/PMA2/CN11/RG9	40	OSC2/CLKO/RC15
9	VSS	41	VSS
10	VDD	42	RTCC/IC1/INT1/RD8
11	AN5/C1IN+/VBUSON/CN7/RB5	43	SS3/U4RX/U1CTS/SDA1/IC2/INT2/RD9
12	AN4/C1IN-/CN6/RB4	44	SCL1/IC3/PMCS2/PMA15/INT3/RD10
13	AN3/C2IN+/CN5/RB3	45	IC4/PMCS1/PMA14/INT4/RD11
14	AN2/C2IN-/CN4/RB2	46	OC1/INT0/RD0
15	PGEC1/AN1/VREF-/CVREF-/CN3/RB1	47	SOSCI/CN1/RC13
16	PGED1/AN0/VREF+/CVREF+/PMA6/CN2/RB0	48	SOSCO/T1CK/CN0/RC14
17	PGEC2/AN6/OCFA/RB6	49	SCK3/U4TX/U1RTS/OC2/RD1
18	PGED2/AN7/RB7	50	SDA3/SDI3/U1RX/OC3/RD2
19	AVDD	51	SCL3/SDO3/U1TX/OC4/RD3
20	AVSS	52	OC5/IC5/PMWR/CN13/RD4
21	AN8/SS4/U5RX/U2CTS/C1OUT/RB8	53	PMRD/CN14/RD5
22	AN9/C2OUT/PMA7/RB9	54	CN15/RD6
23	TMS/AN10/CVREFOUT/PMA13/RB10	55	CN16/RD7
24	TDO/AN11/PMA12/RB11	56	VCAP
25	VSS	57	VDD
26	VDD	58	C1RX/RF0
27	TCK/AN12/PMA11/RB12	59	C1TX/RF1
28	TDI/AN13/PMA10/RB13	60	PMD0/RE0
29	AN14/SCK4/U5TX/U2RTS/PMALH/PMA1/RB14	61	PMD1/RE1
30	AN15/OCFB/PMALL/PMA0/CN12/RB15	62	PMD2/RE2
31	AC1TX/SDA5/SDI4/U2RX/PMA9/CN17/RF4	63	PMD3/RE3
32	AC1RX/SCL5/SDO4/U2TX/PMA8/CN18/RF5	64	PMD4/RE4

**Note** 1: Shaded pins are 5V tolerant.

2: The metal plane at the bottom of the device is not connected to any pins and is recommended to be connected to VSS externally.

# PIC32MX5XX/6XX/7XX

**TABLE 5: PIN NAMES FOR 64-PIN USB AND ETHERNET DEVICES**



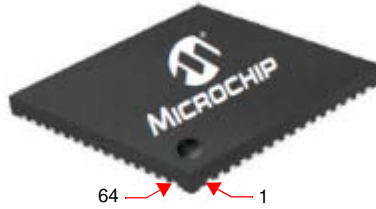
Pin #	Full Pin Name	Pin #	Full Pin Name
1	ETXEN/PMD5/RE5	33	USBID/RF3
2	ETXD0/PMD6/RE6	34	VBUS
3	ETXD1/PMD7/RE7	35	VUSB3V3
4	SCK2/U6TX/U3RTS/PMA5/CN8/RG6	36	D-/RG3
5	SDA4/SDI2/U3RX/PMA4/CN9/RG7	37	D+/RG2
6	SCL4/SDO2/U3TX/PMA3/CN10/RG8	38	VDD
7	MCLR	39	OSC1/CLKI/RC12
8	SS2/U6RX/U3CTS/PMA2/CN11/RG9	40	OSC2/CLKO/RC15
9	VSS	41	VSS
10	VDD	42	RTCC/AERXD1/ETXD3/IC1/INT1/RD8
11	AN5/C1IN+/VBUSON/CN7/RB5	43	AERXD0/ETXD2/SS3/U4RX/U1CTS/SDA1/IC2/INT2/RD9
12	AN4/C1IN-/CN6/RB4	44	ECOL/AECRSDV/SCL1/IC3/PMCS2/PMA15/INT3/RD10
13	AN3/C2IN+/CN5/RB3	45	ECRS/AEREFCLK/IC4/PMCS1/PMA14/INT4/RD11
14	AN2/C2IN-/CN4/RB2	46	OC1/INT0/RD0
15	PGEC1/AN1/VREF-/CVREF-/CN3/RB1	47	SOSCI/CN1/RC13
16	PGED1/AN0/VREF+/CVREF+/PMA6/CN2/RB0	48	SOSCO/T1CK/CN0/RC14
17	PGEC2/AN6/OCFA/RB6	49	EMDIO/AEMDIO/SCK3/U4TX/U1RTS/OC2/RD1
18	PGED2/AN7/RB7	50	SDA3/SDI3/U1RX/OC3/RD2
19	AVDD	51	SCL3/SDO3/U1TX/OC4/RD3
20	AVSS	52	OC5/IC5/PMWR/CN13/RD4
21	AN8/SS4/U5RX/U2CTS/C1OUT/RB8	53	PMRD/CN14/RD5
22	AN9/C2OUT/PMA7/RB9	54	AETXEN/ETXERR/CN15/RD6
23	TMS/AN10/CVREFOUT/PMA13/RB10	55	ETXCLK/AERXERR/CN16/RD7
24	TDO/AN11/PMA12/RB11	56	VCAP
25	VSS	57	VDD
26	VDD	58	AETXD1/ERXD3/RF0
27	TCK/AN12/PMA11/RB12	59	AETXD0/ERXD2/RF1
28	TDI/AN13/PMA10/RB13	60	ERXD1/PMD0/RE0
29	AN14/SCK4/U5TX/U2RTSU2RTS/PMALH/PMA1/RB14	61	ERXD0/PMD1/RE1
30	AN15/EMDC/AEMDC/OCFB/PMALL/PMA0/CN12/RB15	62	ERXDV/ECRSDV/PMD2/RE2
31	SDA5/SDI4/U2RX/PMA9/CN17/RF4	63	ERXCLK/EREFCLK/PMD3/RE3
32	SCL5/SDO4/U2TX/PMA8/CN18/RF5	64	ERXERR/PMD4/RE4

**Note** 1: Shaded pins are 5V tolerant.  
 2: The metal plane at the bottom of the QFN device is not connected to any pins and is recommended to be connected to VSS externally.

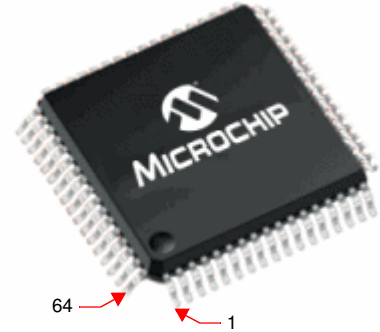
**TABLE 6: PIN NAMES FOR 64-PIN USB, ETHERNET, AND CAN DEVICES**

**64-PIN QFN<sup>(3)</sup> AND TQFP (TOP VIEW)**

**PIC32MX764F128H  
PIC32MX775F256H  
PIC32MX775F512H  
PIC32MX795F512H**



**QFN<sup>(3)</sup>**



**TQFP**

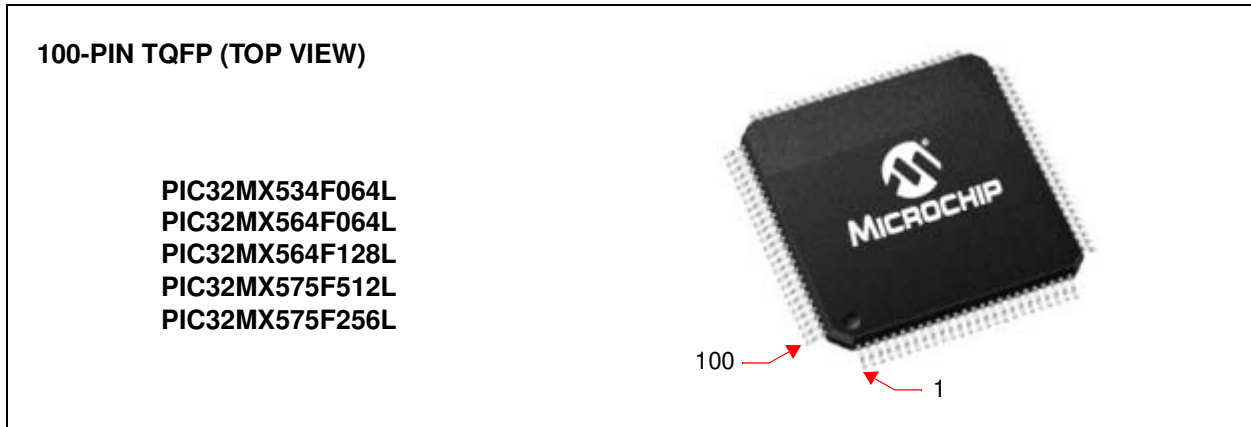
Pin #	Full Pin Name	Pin #	Full Pin Name
1	ETXEN/PMD5/RE5	33	USBID/RF3
2	ETXD0/PMD6/RE6	34	VBUS
3	ETXD1/PMD7/RE7	35	VUSB3v3
4	SCK2/U6TX/U3RTS/PMA5/CN8/RG6	36	D-/RG3
5	SDA4/SDI2/U3RX/PMA4/CN9/RG7	37	D+/RG2
6	SCL4/SDO2/U3TX/PMA3/CN10/RG8	38	VDD
7	MCLR	39	OSC1/CLKI/RC12
8	SS2/U6RX/U3CTS/PMA2/CN11/RG9	40	OSC2/CLKO/RC15
9	Vss	41	Vss
10	VDD	42	RTCC/AERXD1/ETXD3/IC1/INT1/RD8
11	AN5/C1IN+/VBusON/CN7/RB5	43	AERXD0/ETXD2/SS3/U4RX/U1CTS/SDA1/IC2/INT2/RD9
12	AN4/C1IN-/CN6/RB4	44	ECOL/AECRSDV/SCL1/IC3/PMCS2/PMA15/INT3/RD10
13	AN3/C2IN+/CN5/RB3	45	ECRS/AEREFCLK/IC4/PMCS1/PMA14/INT4/RD11
14	AN2/C2IN-/CN4/RB2	46	OC1/INT0/RD0
15	PGEC1/AN1/VREF-/CVREF-/CN3/RB1	47	SOSCI/CN1/RC13
16	PGED1/AN0/VREF+/CVREF+/PMA6/CN2/RB0	48	SOSCO/T1CK/CN0/RC14
17	PGEC2/AN6/OCFA/RB6	49	EMDIO/AEMDIO/SCK3/U4TX/U1RTS/OC2/RD1
18	PGED2/AN7/RB7	50	SDA3/SDI3/U1RX/OC3/RD2
19	AVDD	51	SCL3/SDO3/U1TX/OC4/RD3
20	AVSS	52	OC5/IC5/PMWR/CN13/RD4
21	AN8/C2TX <sup>(2)</sup> /SS4/U5RX/U2CTS/C1OUT/RB8	53	PMRD/CN14/RD5
22	AN9/C2OUT/PMA7/RB9	54	AETXEN/ETXERR/CN15/RD6
23	TMS/AN10/CVREFOUT/PMA13/RB10	55	ETXCLK/AERXERR/CN16/RD7
24	TDO/AN11/PMA12/RB11	56	VCAP
25	Vss	57	VDD
26	VDD	58	C1RX/AETXD1/ERXD3/RF0
27	TCK/AN12/PMA11/RB12	59	C1TX/AETXD0/ERXD2/RF1
28	TDI/AN13/PMA10/RB13	60	ERXD1/PMD0/RE0
29	AN14/C2RX <sup>(2)</sup> /SCK4/U5TX/U2RTS/PMALH/PMA1/RB14	61	ERXD0/PMD1/RE1
30	AN15/EMDC/AEMDC/OCFB/PMALL/PMA0/CN12/RB15	62	ERXDV/ECRSDV/PMD2/RE2
31	AC1TX/SDA5/SDI4/U2RX/PMA9/CN17/RF4	63	ERXCLK/EREFCLKPMD3/RE3
32	AC1RX/SCL5/SDO4/U2TX/PMA8/CN18/RF5	64	ERXERR/PMD4/RE4

- Note**
- 1: Shaded pins are 5V tolerant.
  - 2: This pin is not available on PIC32MX765F128H devices.
  - 3: The metal plane at the bottom of the QFN device is not connected to any pins and is recommended to be connected to Vss externally.



# PIC32MX5XX/6XX/7XX

**TABLE 7: PIN NAMES FOR 100-PIN USB AND CAN DEVICES**



Pin #	Full Pin Name	Pin #	Full Pin Name
1	RG15	36	Vss
2	VDD	37	VDD
3	PMD5/RE5	38	TCK/RA1
4	PMD6/RE6	39	AC1TX/SCK4/U5TX/U2RTS/RF13
5	PMD7/RE7	40	AC1RX/SS4/U5RX/U2CTS/RF12
6	T2CK/RC1	41	AN12/PMA11/RB12
7	T3CK/RC2	42	AN13/PMA10/RB13
8	T4CK/RC3	43	AN14/PMALH/PMA1/RB14
9	T5CK/SDI1/RC4	44	AN15/OCFB/PMALL/PMA0/CN12/RB15
10	SCK2/U6TX/U3RTS/PMA5/CN8/RG6	45	Vss
11	SDA4/SDI2/U3RX/PMA4/CN9/RG7	46	VDD
12	SCL4/SDO2/U3TX/PMA3/CN10/RG8	47	SS3/U4RX/U1CTS/CN20/RD14
13	MCLR	48	SCK3/U4TX/U1RTS/CN21/RD15
14	SS2/U6RX/U3CTS/PMA2/CN11/RG9	49	SDA5/SDI4/U2RX/PMA9/CN17/RF4
15	Vss	50	SCL5/SDO4/U2TX/PMA8/CN18/RF5
16	VDD	51	USBID/RF3
17	TMS/RA0	52	SDA3/SDI3/U1RX/RF2
18	INT1/RE8	53	SCL3/SDO3/U1TX/RF8
19	INT2/RE9	54	VBus
20	AN5/C1IN+/VBUSON/CN7/RB5	55	VUSB3V3
21	AN4/C1IN-/CN6/RB4	56	D-/RG3
22	AN3/C2IN+/CN5/RB3	57	D+/RG2
23	AN2/C2IN-/CN4/RB2	58	SCL2/RA2
24	PGEC1/AN1/CN3/RB1	59	SDA2/RA3
25	PGED1/AN0/CN2/RB0	60	TDI/RA4
26	PGEC2/AN6/OCFA/RB6	61	TDO/RA5
27	PGED2/AN7/RB7	62	VDD
28	VREF-/CVREF-/PMA7/RA9	63	OSC1/CLKI/RC12
29	VREF+/CVREF+/PMA6/RA10	64	OSC2/CLKO/RC15
30	AVDD	65	Vss
31	AVSS	66	SCL1/INT3/RA14
32	AN8/C1OUT/RB8	67	SDA1/INT4/RA15
33	AN9/C2OUT/RB9	68	RTCC/IC1/RD8
34	AN10/CVREFOUT/PMA13/RB10	69	SS1/IC2/RD9
35	AN11/PMA12/RB11	70	SCK1/IC3/PMCS2/PMA15/RD10

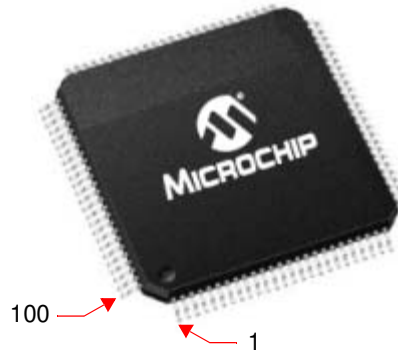
**Note** 1: Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 7: PIN NAMES FOR 100-PIN USB AND CAN DEVICES (CONTINUED)**

**100-PIN TQFP (TOP VIEW)**

PIC32MX534F064L  
 PIC32MX564F064L  
 PIC32MX564F128L  
 PIC32MX575F512L  
 PIC32MX575F256L



Pin #	Full Pin Name	Pin #	Full Pin Name
71	IC4/PMCS1/PMA14/RD11	86	VDD
72	SDO1/OC1/INT0/RD0	87	C1RX/PMD11/RF0
73	SOSCI/CN1/RC13	88	C1TX/PMD10/RF1
74	SOSCO/T1CK/CN0/RC14	89	PMD9/RG1
75	Vss	90	PMD8/RG0
76	OC2/RD1	91	TRCLK/RA6
77	OC3/RD2	92	TRD3/RA7
78	OC4/RD3	93	PMD0/RE0
79	IC5/PMD12/RD12	94	PMD1/RE1
80	PMD13/CN19/RD13	95	TRD2/RG14
81	OC5/PMWR/CN13/RD4	96	TRD1/RG12
82	PMRD/CN14/RD5	97	TRD0/RG13
83	PMD14/CN15/RD6	98	PMD2/RE2
84	PMD15/CN16/RD7	99	PMD3/RE3
85	VCAP	100	PMD4/RE4

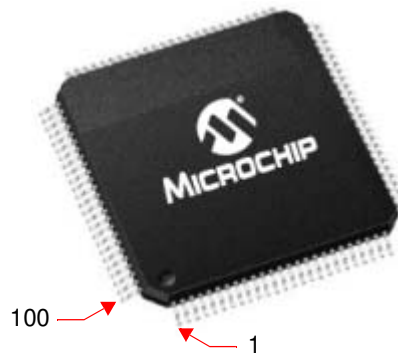
**Note 1:** Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 8: PIN NAMES FOR 100-PIN USB AND ETHERNET DEVICES**

**100-PIN TQFP (TOP VIEW)**

PIC32MX664F064L  
 PIC32MX664F128L  
 PIC32MX675F256L  
 PIC32MX675F512L  
 PIC32MX695F512L



Pin #	Full Pin Name	Pin #	Full Pin Name
1	AERXERR/RG15	36	Vss
2	VDD	37	VDD
3	PMD5/RE5	38	TCK/RA1
4	PMD6/RE6	39	SCK4/U5TX/U2RTS/RF13
5	PMD7/RE7	40	SS4/U5RX/U2CTS/RF12
6	T2CK/RC1	41	AN12/ERXD0/AECRS/PMA11/RB12
7	T3CK/RC2	42	AN13/ERXD1/AECOL/PMA10/RB13
8	T4CK/RC3	43	AN14/ERXD2/AETXD3/PMALH/PMA1/RB14
9	T5CK/SDI1/RC4	44	AN15/ERXD3/AETXD2/OCFB/PMALL/PMA0/CN12/RB15
10	ECOL/SCK2/U6TX/U3RTS/PMA5/CN8/RG6	45	Vss
11	ECRS/SDA4/SDI2/U3RX/PMA4/CN9/RG7	46	VDD
12	ERXDV/AERXDV/ECRS/SDV/AECRS/SDV/SCL4/SDO2/U3TX/PMA3/CN10/RG8	47	AETXD0/SS3/U4RX/U1CTS/CN20/RD14
13	MCLR	48	AETXD1/SCK3/U4TX/U1RTS/CN21/RD15
14	ERXCLK/AERXCLK/EREFCLK/AEREFCLK/SS2/U6RX/U3CTS/PMA2/CN11/RG9	49	SDA5/SDI4/U2RX/PMA9/CN17/RF4
15	Vss	50	SCL5/SDO4/U2TX/PMA8/CN18/RF5
16	VDD	51	USBID/RF3
17	TMS/RA0	52	SDA3/SDI3/U1RX/RF2
18	AERXD0/INT1/RE8	53	SCL3/SDO3/U1TX/RF8
19	AERXD1/INT2/RE9	54	Vbus
20	AN5/C1IN+/VbusON/CN7/RB5	55	Vusb3v3
21	AN4/C1IN-/CN6/RB4	56	D-/RG3
22	AN3/C2IN+/CN5/RB3	57	D+/RG2
23	AN2/C2IN-/CN4/RB2	58	SCL2/RA2
24	PGEC1/AN1/CN3/RB1	59	SDA2/RA3
25	PGED1/AN0/CN2/RB0	60	TDI/RA4
26	PGEC2/AN6/OCFA/RB6	61	TDO/RA5
27	PGED2/AN7/RB7	62	VDD
28	VREF-/CVREF-/AERXD2/PMA7/RA9	63	OSC1/CLKI/RC12
29	VREF+/CVREF+/AERXD3/PMA6/RA10	64	OSC2/CLKO/RC15
30	AVDD	65	Vss
31	AVSS	66	AETXCLK/SCL1/INT3/RA14
32	AN8/C1OUT/RB8	67	AETXEN/SDA1/INT4/RA15
33	AN9/C2OUT/RB9	68	RTCC/EMDIO/AEMDIO/IC1/RD8
34	AN10/CVREFOUT/PMA13/RB10	69	SS1/IC2/RD9
35	AN11/ERXERR/AETXERR/PMA12/RB11	70	SCK1/IC3/PMCS2/PMA15/RD10

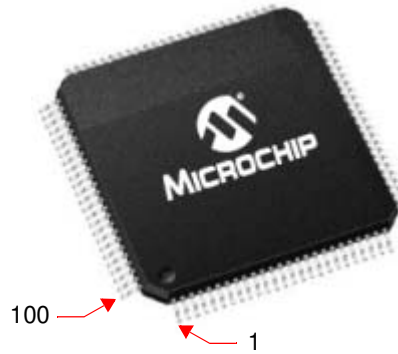
**Note** 1: Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 8: PIN NAMES FOR 100-PIN USB AND ETHERNET DEVICES (CONTINUED)**

**100-PIN TQFP (TOP VIEW)**

PIC32MX664F064L  
 PIC32MX664F128L  
 PIC32MX675F256L  
 PIC32MX675F512L  
 PIC32MX695F512L



Pin #	Full Pin Name	Pin #	Full Pin Name
71	EMDC/AEMDC/IC4/PMCS1/PMA14/RD11	86	VDD
72	SDO1/OC1/INT0/RD0	87	ETXD1/PMD11/RF0
73	SOSCI/CN1/RC13	88	ETXD0/PMD10/RF1
74	SOSCO/T1CK/CN0/RC14	89	ETXERR/PMD9/RG1
75	Vss	90	PMD8/RG0
76	OC2/RD1	91	TRCLK/RA6
77	OC3/RD2	92	TRD3/RA7
78	OC4/RD3	93	PMD0/RE0
79	ETXD2/IC5/PMD12/RD12	94	PMD1/RE1
80	ETXD3/PMD13/CN19/RD13	95	TRD2/RG14
81	OC5/PMWR/CN13/RD4	96	TRD1/RG12
82	PMRD/CN14/RD5	97	TRD0/RG13
83	ETXEN/PMD14/CN15/RD6	98	PMD2/RE2
84	ETXCLK/PMD15/CN16/RD7	99	PMD3/RE3
85	VCAP/VDDCORE	100	PMD4/RE4

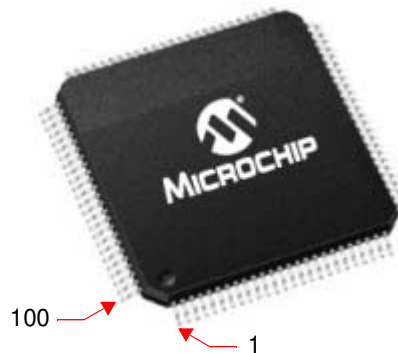
**Note** 1: Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 9: PIN NAMES FOR 100-PIN USB, ETHERNET, AND CAN DEVICES**

**100-PIN TQFP (TOP VIEW)**

PIC32MX764F128L  
 PIC32MX775F256L  
 PIC32MX775F512L  
 PIC32MX795F512L



Pin #	Full Pin Name	Pin #	Full Pin Name
1	AERXERR/RG15	36	Vss
2	VDD	37	VDD
3	PMD5/RE5	38	TCK/RA1
4	PMD6/RE6	39	AC1TX/SCK4/U5TX/U2RTS/RF13
5	PMD7/RE7	40	AC1RX/SS4/U5RX/U2CTS/RF12
6	T2CK/RC1	41	AN12/ERXD0/AECRS/PMA11/RB12
7	T3CK/AC2TX <sup>(1)</sup> /RC2	42	AN13/ERXD1/AECOL/PMA10/RB13
8	T4CK/AC2RX <sup>(1)</sup> /RC3	43	AN14/ERXD2/AETXD3/PMALH/PMA1/RB14
9	T5CK/SD1/RC4	44	AN15/ERXD3/AETXD2/OCFB/PMALL/PMA0/CN12/RB15
10	ECOL/SCK2/U6TX/U3RTS/PMA5/CN8/RG6	45	Vss
11	ECRS/SDA4/SDI2/U3RX/PMA4/CN9/RG7	46	VDD
12	ERXDV/AERXDV/ECRS/DV/AECRS/DV/SCL4/SDO2/U3TX/PMA3/CN10/RG8	47	AETXD0/SS3/U4RX/U1CTS/CN20/RD14
13	MCLR	48	AETXD1/SCK3/U4TX/U1RTS/CN21/RD15
14	ERXCLK/AERXCLK/EREFCLK/AEREFCLK/SS2/U6RX/U3CTS/PMA2/CN11/RG9	49	SDA5/SDI4/U2RX/PMA9/CN17/RF4
15	Vss	50	SCL5/SDO4/U2TX/PMA8/CN18/RF5
16	VDD	51	USBID/RF3
17	TMS/RA0	52	SDA3/SDI3/U1RX/RF2
18	AERXD0/INT1/RE8	53	SCL3/SDO3/U1TX/RF8
19	AERXD1/INT2/RE9	54	VBUS
20	AN5/C1IN+/VBUSON/CN7/RB5	55	VUSB3v3
21	AN4/C1IN-/CN6/RB4	56	D-/RG3
22	AN3/C2IN+/CN5/RB3	57	D+/RG2
23	AN2/C2IN-/CN4/RB2	58	SCL2/RA2
24	PGEC1/AN1/CN3/RB1	59	SDA2/RA3
25	PGED1/AN0/CN2/RB0	60	TDI/RA4
26	PGEC2/AN6/OCFA/RB6	61	TDO/RA5
27	PGED2/AN7/RB7	62	VDD
28	VREF-/CVREF-/AERXD2/PMA7/RA9	63	OSC1/CLKI/RC12
29	VREF+/CVREF+/AERXD3/PMA6/RA10	64	OSC2/CLKO/RC15
30	AVDD	65	Vss
31	AVSS	66	AETXCLK/SCL1/INT3/RA14
32	AN8/C1OUT/RB8	67	AETXEN/SDA1/INT4/RA15
33	AN9/C2OUT/RB9	68	RTCC/EMDIO/AEMDIO/IC1/RD8
34	AN10/CVREFOUT/PMA13/RB10	69	SS1/IC2/RD9
35	AN11/ERXERR/AETXERR/PMA12/RB11	70	SCK1/IC3/PMCS2/PMA15/RD10

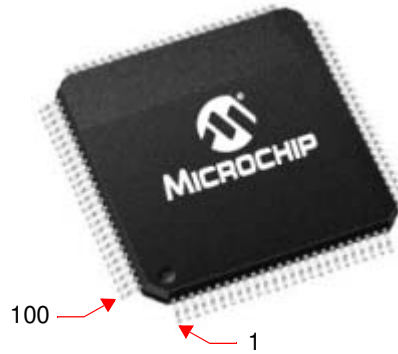
**Note** 1: This pin is not available on PIC32MX764F128L devices.  
 2: Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 9: PIN NAMES FOR 100-PIN USB, ETHERNET, AND CAN DEVICES (CONTINUED)**

**100-PIN TQFP (TOP VIEW)**

PIC32MX764F128L  
 PIC32MX775F256L  
 PIC32MX775F512L  
 PIC32MX795F512L



Pin #	Full Pin Name	Pin #	Full Pin Name
71	EMDC/AEMDC/IC4/PMCS1/PMA14/RD11	86	VDD
72	SDO1/OC1/INT0/RD0	87	C1RX/ETXD1/PMD11/RF0
73	SOSCI/CN1/RC13	88	C1TX/ETXD0/PMD10/RF1
74	SOSCO/T1CK/CN0/RC14	89	C2TX <sup>(1)</sup> /ETXERR/PMD9/RG1
75	Vss	90	C2RX <sup>(1)</sup> /PMD8/RG0
76	OC2/RD1	91	TRCLK/RA6
77	OC3/RD2	92	TRD3/RA7
78	OC4/RD3	93	PMD0/RE0
79	ETXD2/IC5/PMD12/RD12	94	PMD1/RE1
80	ETXD3/PMD13/CN19/RD13	95	TRD2/RG14
81	OC5/PMWR/CN13/RD4	96	TRD1/RG12
82	PMRD/CN14/RD5	97	TRD0/RG13
83	ETXEN/PMD14/CN15/RD6	98	PMD2/RE2
84	ETXCLK/PMD15/CN16/RD7	99	PMD3/RE3
85	VCAP/VDDCORE	100	PMD4/RE4

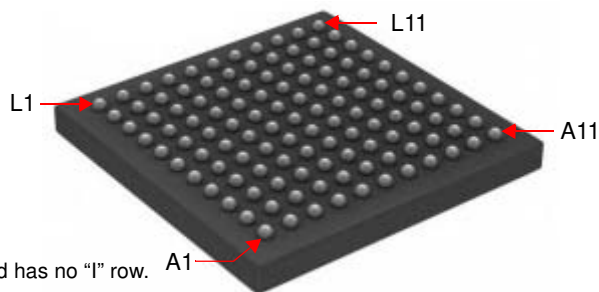
**Note** 1: This pin is not available on PIC32MX764F128L devices.  
 2: Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 10: PIN NAMES FOR USB AND CAN DEVICES**

**121-PIN TFBGA (BOTTOM VIEW)**

PIC32MX534F064L  
 PIC32MX564F064L  
 PIC32MX564F128L  
 PIC32MX575F256L  
 PIC32MX575F512L



**Note:** The TFBGA package skips from row “H” to row “J” and has no “I” row.

Pin #	Full Pin Name	Pin #	Full Pin Name
A1	PMD4/RE4	E2	T4CK/RC3
A2	PMD3/RE3	E3	SCK2/U6TX/U6TX/U3RTS/PMA5/CN8/RG6
A3	TRD0/RG13	E4	T3CK/RC2
A4	PMD0/RE0	E5	VDD
A5	PMD8/RG0	E6	PMD9/RG1
A6	C1TX/PMD10/RF1	E7	VSS
A7	VDD	E8	SDA1/INT4/RA15
A8	VSS	E9	RTCC/IC1/RD8
A9	IC5/PMD12/RD12	E10	SS1/IC2/RD9
A10	OC3/RD2	E11	SCL1/INT3/RA14
A11	OC2/RD1	F1	MCLR
B1	No Connect (NC)	F2	SCL4/SDO2/U3TX/PMA3/CN10/RG8
B2	RG15	F3	SS2/U6RX/U3CTS/PMA2/CN11/RG9
B3	PMD2/RE2	F4	SDA4/SDI2/U3RX/PMA4/CN9/RG7
B4	PMD1/RE1	F5	VSS
B5	TRD3/RA7	F6	No Connect (NC)
B6	C1RX/PMD11/RF0	F7	No Connect (NC)
B7	VCAP	F8	VDD
B8	PMRD/CN14/RD5	F9	OSC1/CLKI/RC12
B9	OC4/RD3	F10	VSS
B10	VSS	F11	OSC2/CLKO/RC15
B11	SOSCO/T1CK/CN0/RC14	G1	INT1/RE8
C1	PMD6/RE6	G2	INT2/RE9
C2	VDD	G3	TMS/RA0
C3	TRD1/RG12	G4	No Connect (NC)
C4	TRD2/RG14	G5	VDD
C5	TRCLK/RA6	G6	VSS
C6	No Connect (NC)	G7	VSS
C7	PMD15/CN16/RD7	G8	No Connect (NC)
C8	OC5/PMWR/CN13/RD4	G9	TDO/RA5
C9	VDD	G10	SDA2/RA3
C10	SOSCI/CN1/RC13	G11	TDI/RA4
C11	IC4/PMCS1/PMA14/RD11	H1	AN5/C1IN+/VBUSON/CN7/RB5
D1	T2CK/RC1	H2	AN4/C1IN-/CN6/RB4
D2	PMD7/RE7	H3	VSS
D3	PMD5/RE5	H4	VDD
D4	VSS	H5	No Connect (NC)
D5	VSS	H6	VDD
D6	No Connect (NC)	H7	No Connect (NC)
D7	PMD14/CN15/RD6	H8	VBUS
D8	PMD13/CN19/RD13	H9	VUSB3V3
D9	SDO1/OC1/INT0/RD0	H10	D+/RG2
D10	No Connect (NC)	H11	SCL2/RA2
D11	SCK1/IC3/PMCS2/PMA15/RD10	J1	AN3/C2IN+/CN5/RB3
E1	T5CK/SDI1/RC4	J2	AN2/C2IN-/CN4/RB2

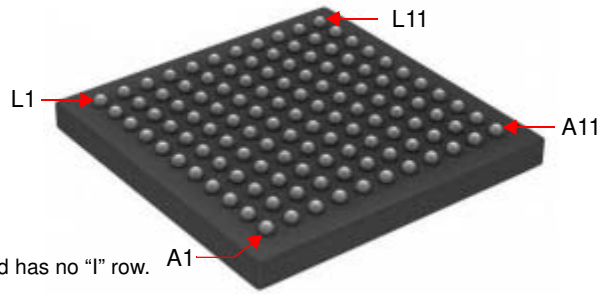
**Note 1:** Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 10: PIN NAMES (CONTINUED) FOR USB AND CAN DEVICES**

**121-PIN TFBGA (BOTTOM VIEW)**

PIC32MX534F064L  
 PIC32MX564F064L  
 PIC32MX564F128L  
 PIC32MX575F256L  
 PIC32MX575F512L



**Note:** The TFBGA package skips from row "H" to row "J" and has no "I" row.

Pin #	Full Pin Name	Pin #	Full Pin Name
J3	PGED2/AN7/RB7	K8	VDD
J4	AVDD	K9	SCK3/U4TX/U1RTS/CN21/RD15
J5	AN11/PMA12/RB11	K10	USBID/RF3
J6	TCK/RA1	K11	SDA3/SDI3/U1RX/RF2
J7	AN12/PMA11/RB12	L1	PGEC2/AN6/OCFA/RB6
J8	No Connect (NC)	L2	VREF-/CVREF-/PMA7/RA9
J9	No Connect (NC)	L3	AVSS
J10	SCL3/SDO3/U1TX/RF8	L4	AN9/C2OUT/RB9
J11	D-/RG3	L5	AN10/CVREFOUT/PMA13/RB10
K1	PGEC1/AN1/CN3/RB1	L6	AC1TX/SCK4/U5TX/U2RTS/RF13
K2	PGED1/AN0/CN2/RB0	L7	AN13/PMA10/RB13
K3	VREF+/CVREF+/PMA6/RA10	L8	AN15/OCFB/PMALL/PMA0/CN12/RB15
K4	AN8/C1OUT/RB8	L9	SS3/U4RX/U1CTS/CN20/RD14
K5	No Connect (NC)	L10	SDA5/SDI4/U2RX/PMA9/CN17/RF4
K6	AC1RX/SS4/U5RX/U2CTS/RF12	L11	SCL5/SDO4/U2TX/PMA8/CN18/RF5
K7	AN14/PMALH/PMA1/RB14		

**Note 1:** Shaded pins are 5V tolerant.

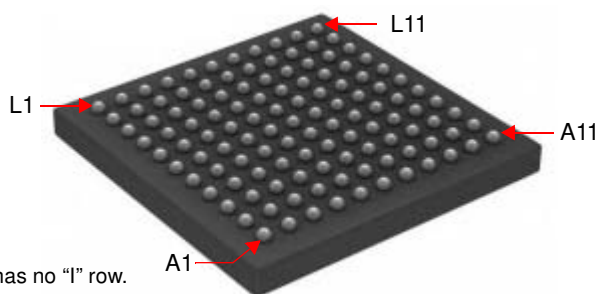


# PIC32MX5XX/6XX/7XX

**TABLE 11: PIN NAMES FOR USB AND ETHERNET DEVICES**

**121-PIN TFBGA (BOTTOM VIEW)**

PIC32MX664F064L  
 PIC32MX664F128L  
 PIC32MX675F256L  
 PIC32MX675F512L  
 PIC32MX695F512L



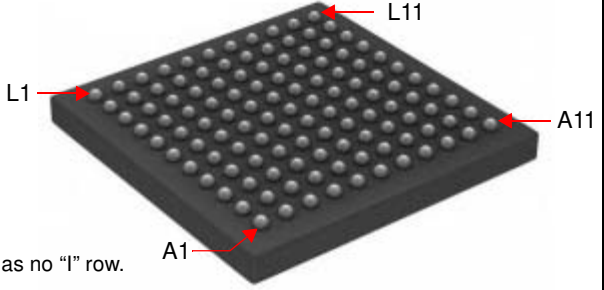
**Note:** The TFBGA package skips from row “H” to row “J” and has no “I” row.

Pin #	Full Pin Name	Pin #	Full Pin Name
A1	PMD4/RE4	E2	T4CK/RC3
A2	PMD3/RE3	E3	ECOL/SCK2/U6TX/U3RTS/PMA5/CN8/RG6
A3	TRD0/RG13	E4	T3CK/RC2
A4	PMD0/RE0	E5	VDD
A5	PMD8/RG0	E6	ETXERR/PMD9/RG1
A6	ETXD0/PMD10/RF1	E7	VSS
A7	VDD	E8	AETXEN/SDA1/INT4/RA15
A8	VSS	E9	RTCC/EMDIO/AEMDIO/IC1/RD8
A9	ETXD2/IC5/PMD12/RD12	E10	SS1/IC2/RD9
A10	OC3/RD2	E11	AETXCLK/SCL1/INT3/RA14
A11	OC2/RD1	F1	MCLR
B1	No Connect (NC)	F2	ERXDV/AERXDV/ECRSDV/AECRSDV//SCL4/SDO2/U3TX/PMA3/CN10/RG8
B2	AERXERR/RG15	F3	ERXCLK/AERXCLK/EREFCLK/AEREFCLK/SS2/U6RX/U3CTS/PMA2/CN11/RG9
B3	PMD2/RE2	F4	ECRS/SDA4/SDI2/U3RX/PMA4/CN9/RG7
B4	PMD1/RE1	F5	VSS
B5	TRD3/RA7	F6	No Connect (NC)
B6	ETXD1/PMD11/RF0	F7	No Connect (NC)
B7	VCAP	F8	VDD
B8	PMRD/CN14/RD5	F9	OSC1/CLKI/RC12
B9	OC4/RD3	F10	VSS
B10	VSS	F11	OSC2/CLKO/RC15
B11	SOSCO/T1CK/CN0/RC14	G1	AERXD0/INT1/RE8
C1	PMD6/RE6	G2	AERXD1/INT2/RE9
C2	VDD	G3	TMS/RA0
C3	TRD1/RG12	G4	No Connect (NC)
C4	TRD2/RG14	G5	VDD
C5	TRCLK/RA6	G6	VSS
C6	No Connect (NC)	G7	VSS
C7	ETXCLK/PMD15/CN16/RD7	G8	No Connect (NC)
C8	OC5/PMWR/CN13/RD4	G9	TDO/RA5
C9	VDD	G10	SDA2/RA3
C10	SOSCI/CN1/RC13	G11	TDI/RA4
C11	EMDC/AEMDC/IC4/PMCS1/PMA14/RD11	H1	AN5/C1IN+/VBUSON/CN7/RB5
D1	T2CK/RC1	H2	AN4/C1IN-/CN6/RB4
D2	PMD7/RE7	H3	VSS
D3	PMD5/RE5	H4	VDD
D4	VSS	H5	No Connect (NC)
D5	VSS	H6	VDD
D6	No Connect (NC)	H7	No Connect (NC)
D7	ETXEN/PMD14/CN15/RD6	H8	VBUS
D8	ETXD3/PMD13/CN19/RD13	H9	VUSB3V3
D9	SDO1/OC1/INT0/RD0	H10	D+/RG2
D10	No Connect (NC)	H11	SCL2/RA2
D11	SCK1/IC3/PMCS2/PMA15/RD10	J1	AN3/C2IN+/CN5/RB3
E1	T5CK/SDI1/RC4	J2	AN2/C2IN-/CN4/RB2

**Note 1:** Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 11: PIN NAMES FOR USB AND ETHERNET DEVICES (CONTINUED)**

121-PIN TFBGA (BOTTOM VIEW)			
<p>PIC32MX664F064L  PIC32MX664F128L  PIC32MX675F256L  PIC32MX675F512L  PIC32MX695F512L</p>			
<p><b>Note:</b> The TFBGA package skips from row “H” to row “J” and has no “I” row.</p>			
			
Pin #	Full Pin Name	Pin #	Full Pin Name
J3	PGED2/AN7/RB7	K8	VDD
J4	AVDD	K9	AETXD1/SCK3/U4TX/U1RTS/CN21/RD15
J5	AN11/ERXERR/AETXERR/PMA12/RB11	K10	USBID/RF3
J6	TCK/RA1	K11	SDA3/SDI3/U1RX/RF2
J7	AN12/ERXD0/AECRS/PMA11/RB12	L1	PGEC2/AN6/OCFA/RB6
J8	No Connect (NC)	L2	VREF-/CVREF-/AERXD2/PMA7/RA9
J9	No Connect (NC)	L3	AVss
J10	SCL3/SDO3/U1TX/RF8	L4	AN9/C2OUT/RB9
J11	D-/RG3	L5	AN10/CVREFOUT/PMA13/RB10
K1	PGEC1/AN1/CN3/RB1	L6	SCK4/U5TX/U2RTS/RF13
K2	PGED1/AN0/CN2/RB0	L7	AN13/ERXD1/AECOL/PMA10/RB13
K3	VREF+/CVREF+/AERXD3/PMA6/RA10	L8	AN15/ERXD3/AETXD2/OCFB/PMALL/PMA0/CN12/RB15
K4	AN8/C1OUT/RB8	L9	AETXD0/SS3/U4RX/U1CTS/CN20/RD14
K5	No Connect (NC)	L10	SDA5/SDI4/U2RX/PMA9/CN17/RF4
K6	SS4/U5RX/U2CTS/RF12	L11	SCL5/SDO4/U2TX/PMA8/CN18/RF5
K7	AN14/ERXD2/AETXD3/PMALH/PMA1/RB14		

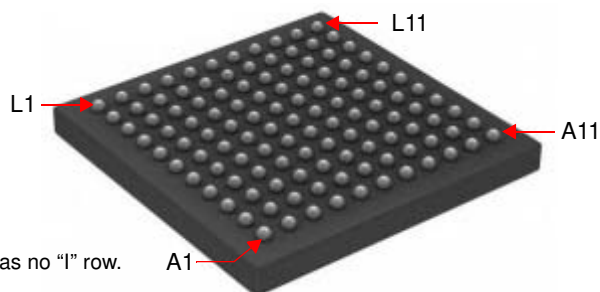
**Note 1:** Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 12: PIN NAMES FOR USB, ETHERNET, AND CAN DEVICES**

**121-PIN TFBGA (BOTTOM VIEW)**

**PIC32MX764F128L**  
**PIC32MX775F256L**  
**PIC32MX775F512L**  
**PIC32MX795F512L**



**Note:** The TFBGA package skips from row “H” to row “J” and has no “I” row.

Pin #	Full Pin Name	Pin #	Full Pin Name
A1	PMD4/RE4	E2	T4CK/AC2RX <sup>(1)</sup> /RC3
A2	PMD3/RE3	E3	ECOL/SCK2/U6TX/U3RTS/PMA5/CN8/RG6
A3	TRD0/RG13	E4	T3CK/AC2TX <sup>(1)</sup> /RC2
A4	PMD0/RE0	E5	VDD
A5	C2RX <sup>(1)</sup> /PMD8/RG0	E6	C2TX <sup>(1)</sup> /ETXERR/PMD9/RG1
A6	C1TX/ETXD0/PMD10/RF1	E7	VSS
A7	VDD	E8	AETXEN/SDA1/INT4/RA15
A8	VSS	E9	RTCC/EMDIO/AEMDIO/IC1/RD8
A9	ETXD2/IC5/PMD12/RD12	E10	SS1/IC2/RD9
A10	OC3/RD2	E11	AETXCLK/SCL1/INT3/RA14
A11	OC2/RD1	F1	MCLR
B1	No Connect (NC)	F2	ERXDV/AERXDV/ECRSDV/AECRSDV/SCL4/SDO2/U3TX/PMA3/CN10/RG8
B2	AERXERR/RG15	F3	ERXCLK/AERXCLK/EREFCLK/AEREFCLK/SS2/U6RX/U3CTS/PMA2/CN11/RG9
B3	PMD2/RE2	F4	ECRS/SDA4/SDI2/U3RX/PMA4/CN9/RG7
B4	PMD1/RE1	F5	VSS
B5	TRD3/RA7	F6	No Connect (NC)
B6	C1RX/ETXD1/PMD11/RF0	F7	No Connect (NC)
B7	VCAP	F8	VDD
B8	PMRD/CN14/RD5	F9	OSC1/CLKI/RC12
B9	OC4/RD3	F10	VSS
B10	VSS	F11	OSC2/CLKO/RC15
B11	SOSCO/T1CK/CN0/RC14	G1	AERXD0/INT1/RE8
C1	PMD6/RE6	G2	AERXD1/INT2/RE9
C2	VDD	G3	TMS/RA0
C3	TRD1/RG12	G4	No Connect (NC)
C4	TRD2/RG14	G5	VDD
C5	TRCLK/RA6	G6	VSS
C6	No Connect (NC)	G7	VSS
C7	ETXCLK/PMD15/CN16/RD7	G8	No Connect (NC)
C8	OC5/PMWR/CN13/RD4	G9	TDO/RA5
C9	VDD	G10	SDA2/RA3
C10	SOSCI/CN1/RC13	G11	TDI/RA4
C11	EMDC/AEMDC/IC4/PMCS1/PMA14/RD11	H1	AN5/C1IN+/VBUSON/CN7/RB5
D1	T2CK/RC1	H2	AN4/C1IN-/CN6/RB4
D2	PMD7/RE7	H3	VSS
D3	PMD5/RE5	H4	VDD
D4	VSS	H5	No Connect (NC)
D5	VSS	H6	VDD
D6	No Connect (NC)	H7	No Connect (NC)
D7	ETXEN/PMD14/CN15/RD6	H8	VBUS
D8	ETXD3/PMD13/CN19/RD13	H9	VUSB3V3
D9	SDO1/OC1/INT0/RD0	H10	D+/RG2
D10	No Connect (NC)	H11	SCL2/RA2
D11	SCK1/IC3/PMCS2/PMA15/RD10	J1	AN3/C2IN+/CN5/RB3
E1	T5CK/SDI1/RC4	J2	AN2/C2IN-/CN4/RB2

**Note 1:** This pin is not available on PIC32MX764F128L devices.  
**Note 2:** Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 12: PIN NAMES FOR USB, ETHERNET, AND CAN DEVICES (CONTINUED)**

**121-PIN TFBGA (BOTTOM VIEW)**

**PIC32MX764F128L**  
**PIC32MX775F256L**  
**PIC32MX775F512L**  
**PIC32MX795F512L**

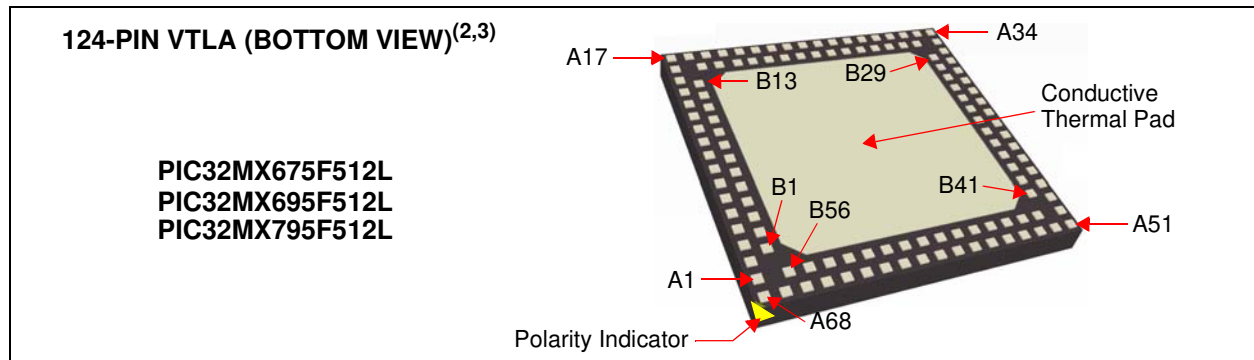
**Note:** The TFBGA package skips from row “H” to row “J” and has no “I” row.

Pin #	Full Pin Name	Pin #	Full Pin Name
J3	PGED2/AN7/RB7	K8	VDD
J4	AVDD	K9	AETXD1/SCK3/U4TX/U1RTS/CN21/RD15
J5	AN11/ERXERR/AETXERR/PMA12/RB11	K10	USBID/RF3
J6	TCK/RA1	K11	SDA3/SDI3/U1RX/RF2
J7	AN12/ERXD0/AECRS/PMA11/RB12	L1	PGEC2/AN6/OCFA/RB6
J8	No Connect (NC)	L2	VREF-/CVREF-/AERXD2/PMA7/RA9
J9	No Connect (NC)	L3	AVSS
J10	SCL3/SDO3/U1TX/RF8	L4	AN9/C2OUT/RB9
J11	D-/RG3	L5	AN10/CVREFOUT/PMA13/RB10
K1	PGEC1/AN1/CN3/RB1	L6	AC1TX/SCK4/U5TX/U2RTS/RF13
K2	PGED1/AN0/CN2/RB0	L7	AN13/ERXD1/AECOL/PMA10/RB13
K3	VREF+/CVREF+/AERXD3/PMA6/RA10	L8	AN15/ERXD3/AETXD2/OCFB/PMALL/PMA0/CN12/RB15
K4	AN8/C1OUT/RB8	L9	AETXD0/SS3/U4RX/U1CTS/CN20/RD14
K5	No Connect (NC)	L10	SDA5/SDI4/U2RX/PMA9/CN17/RF4
K6	AC1RX/SS4/U5RX/U2CTS/RF12	L11	SCL5/SDO4/U2TX/PMA8/CN18/RF5
K7	AN14/ERXD2/AETXD3/PMALH/PMA1/RB14		

- Note** 1: This pin is not available on PIC32MX764F128L devices.  
2: Shaded pins are 5V tolerant.

# PIC32MX5XX/6XX/7XX

**TABLE 13: PIN NAMES FOR 124-PIN USB, ETHERNET, AND CAN DEVICES**

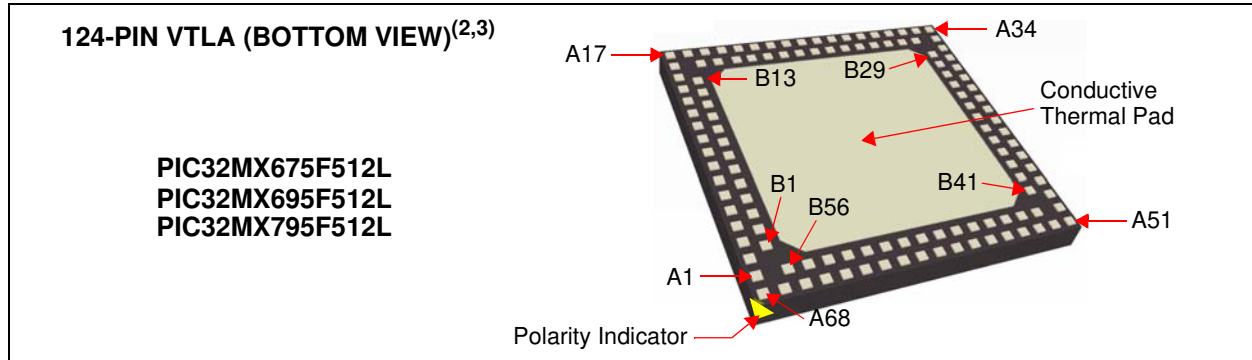


Package Bump #	Full Pin Name	Package Bump #	Full Pin Name
A1	No Connect (NC)	A38	D-/RG3
A2	AERXERR/RG15	A39	SCL2/RA2
A3	Vss	A40	TDI/RA4
A4	PMD6/RE6	A41	VDD
A5	T2CK/RC1	A42	OSC2/CLKO/RC15
A6	T4CK/AC2RX <sup>(1)</sup> /RC3	A43	Vss
A7	ECOL/SCK2/U6TX/U3RTS/PMA5/CN8/RG6	A44	AETXEN/SDA1/INT4/RA15
A8	ERXDV/AERXDV/ECRSDV/AECRSDV/SCL4/SDO2/U3TX/PMA3/CN10/RG8	A45	SS1/IC2/RD9
A9	ERXCLK/AERXCLK/EREFCLK/AEREFCLK/SS2/U6RX/U3CTS/PMA2/CN11/RG9	A46	EMDC/AEMDC/IC4/PMCS1/PMA14/RD11
A10	VDD	A47	SOSCI/CN1/RC13
A11	AERXD0/INT1/RE8	A48	VDD
A12	AN5/C1IN+/VBUSON/CN7/RB5	A49	No Connect (NC)
A13	AN3/C2IN+/CN5/RB3	A50	No Connect (NC)
A14	VDD	A51	No Connect (NC)
A15	PGEC1/AN1/CN3/RB1	A52	OC2/RD1
A16	No Connect (NC)	A53	OC4/RD3
A17	No Connect (NC)	A54	ETXD3/PMD13/CN19/RD13
A18	No Connect (NC)	A55	PMRD/CN14/RD5
A19	No Connect (NC)	A56	ETXCLK/PMD15/CN16/RD7
A20	PGEC2/AN6/OCFA/RB6	A57	No Connect (NC)
A21	VREF-/CVREF-/AERXD2/PMA7/RA9	A58	No Connect (NC)
A22	AVDD	A59	VDD
A23	AN8/C1OUT/RB8	A60	C1TX/ETXD0/PMD10/RF1
A24	AN10/CVREFOUT/PMA13/RB10	A61	C2RX <sup>(1)</sup> /PMD8/RG0
A25	Vss	A62	TRD3/RA7
A26	TCK/RA1	A63	Vss
A27	AC1RX <sup>(1)</sup> /SS4/U5RX/U2CTS/RF12	A64	PMD1/RE1
A28	AN13/ERXD1/AECOL/PMA10/RB13	A65	TRD1/RG12
A29	AN15/ERXD3/AETXD2/OCFB/PMALL/PMA0/CN12/RB15	A66	PMD2/RE2
A30	VDD	A67	PMD4/RE4
A31	AETXD1/SCK3/U4TX/U1RTS/CN21/RD15	A68	No Connect (NC)
A32	SCL5/SDO4/U2TX/PMA8/CN18/RF5	B1	VDD
A33	No Connect (NC)	B2	PMD5/RE5
A34	No Connect (NC)	B3	PMD7/RE7
A35	USBID/RF3	B4	T3CK/AC2TX <sup>(1)</sup> /RC2
A36	SDA3/SDI3/U1RX/RF2	B5	T5CK/SDI1/RC4
A37	VBus	B6	ECRS/SDA4/SDI2/U3RX/PMA4/CN9/RG7
B7	MCLR	B32	SDA2/RA3

- Note**
- 1: This pin is only available on PIC32MX795F512L devices.
  - 2: Shaded package bumps are 5V tolerant.
  - 3: It is recommended that the user connect the printed circuit board (PCB) ground to the conductive thermal pad on the bottom of the package. And to not run non-Vss PCB traces under the conductive thermal pad on the same side of the PCB layout.

# PIC32MX5XX/6XX/7XX

**TABLE 13: PIN NAMES FOR 124-PIN USB, ETHERNET, AND CAN DEVICES (CONTINUED)**



Package Bump #	Full Pin Name	Package Bump #	Full Pin Name
B8	V <sub>SS</sub>	B33	TDO/RA5
B9	TMS/RA0	B34	OSC1/CLKI/RC12
B10	AERXD1/INT2/RE9	B35	No Connect (NC)
B11	AN4/C1IN-/CN6/RB4	B36	AETXCLK/SCL1/INT3/RA14
B12	V <sub>SS</sub>	B37	RTCC/EMDIO/AEMDIO/IC1/RD8
B13	AN2/C2IN-/CN4/RB2	B38	SCK1/IC3/PMCS2/PMA15/RD10
B14	PGED1/AN0/CN2/RB0	B39	SDO1/OC1/INT0/RD0
B15	No Connect (NC)	B40	SOSCO/T1CK/CN0/RC14
B16	PGED2/AN7/RB7	B41	V <sub>SS</sub>
B17	V <sub>REF+</sub> /CV <sub>REF+</sub> /AERXD3/PMA6/RA10	B42	OC3/RD2
B18	AV <sub>SS</sub>	B43	ETXD2/IC5/PMD12/RD12
B19	AN9/C2OUT/RB9	B44	OC5/PMWR/CN13/RD4
B20	AN11/ERXERR/AETXERR/PMA12/RB11	B45	ETXEN/PMD14/CN15/RD6
B21	V <sub>DD</sub>	B46	V <sub>SS</sub>
B22	AC1TX/SCK4/U5TX/U2RTS/RF13	B47	No Connect (NC)
B23	AN12/ERXD0/AECRS/PMA11/RB12	B48	VCAP
B24	AN14/ERXD2/AETXD3/PMALH/PMA1/RB14	B49	C1RX <sup>(1)</sup> /ETXD1/PMD11/RF0
B25	V <sub>SS</sub>	B50	C2TX <sup>(1)</sup> /ETXERR/PMD9/RG1
B26	AETXD0/SS3/U4RX/U1CTS/CN20/RD14	B51	TRCLK/RA6
B27	SDA5/SDI4/U2RX/PMA9/CN17/RF4	B52	PMD0/RE0
B28	No Connect (NC)	B53	V <sub>DD</sub>
B29	SCL3/SDO3/U1TX/RF8	B54	TRD2/RG14
B30	V <sub>USB3V3</sub>	B55	TRD0/RG13
B31	D+/RG2	B56	PMD3/RE3

- Note**
- 1: This pin is only available on PIC32MX795F512L devices.
  - 2: Shaded package bumps are 5V tolerant.
  - 3: It is recommended that the user connect the printed circuit board (PCB) ground to the conductive thermal pad on the bottom of the package. And to not run non-V<sub>SS</sub> PCB traces under the conductive thermal pad on the same side of the PCB layout.

# PIC32MX5XX/6XX/7XX

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# PIC32MX5XX/6XX/7XX

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## Referenced Sources

This device data sheet is based on the following individual chapters of the “*PIC32 Family Reference Manual*”. These documents should be considered as the general reference for the operation of a particular module or device feature.

**Note 1:** To access the documents listed below, browse to the documentation section of the [PIC32MX795F512L](#) product page on the Microchip web site ([www.microchip.com](http://www.microchip.com)) or select a family reference manual section from the following list.

In addition to parameters, features, and other documentation, the resulting page provides links to the related family reference manual sections.

- **Section 1. “Introduction”** (DS60001127)
- **Section 2. “CPU”** (DS60001113)
- **Section 4. “Prefetch Cache”** (DS60001119)
- **Section 3. “Memory Organization”** (DS60001115)
- **Section 5. “Flash Program Memory”** (DS60001121)
- **Section 6. “Oscillator Configuration”** (DS60001112)
- **Section 7. “Resets”** (DS60001118)
- **Section 8. “Interrupt Controller”** (DS60001108)
- **Section 9. “Watchdog Timer and Power-up Timer** (DS60001114)
- **Section 10. “Power-Saving Features”** (DS60001130)
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- **Section 14. “Timers”** (DS60001105)
- **Section 15. “Input Capture”** (DS60001122)
- **Section 16. “Output Capture”** (DS60001111)
- **Section 17. “10-bit Analog-to-Digital Converter (ADC)”** (DS60001104)
- **Section 19. “Comparator”** (DS60001110)
- **Section 20. “Comparator Voltage Reference (CVREF)”** (DS60001109)
- **Section 21. “Universal Asynchronous Receiver Transmitter (UART)”** (DS60001107)
- **Section 23. “Serial Peripheral Interface (SPI)”** (DS60001106)
- **Section 24. “Inter-Integrated Circuit (I2C)”** (DS60001116)
- **Section 27. “USB On-The-Go (OTG)”** (DS60001126)
- **Section 29. “Real-Time Clock and Calendar (RTCC)”** (DS60001125)
- **Section 31. “Direct Memory Access (DMA) Controller”** (DS60001117)
- **Section 32. “Configuration”** (DS60001124)
- **Section 33. “Programming and Diagnostics”** (DS60001129)
- **Section 34. “Controller Area Network (CAN)”** (DS60001154)
- **Section 35. “Ethernet Controller”** (DS60001155)

## 1.0 DEVICE OVERVIEW

**Note:** This data sheet summarizes the features of the PIC32MX5XX/6XX/7XX family of devices. It is not intended to be a comprehensive reference source. To complement the information in this data sheet, refer to the documents listed in the *Documentation > Reference Manual* section of the Microchip PIC32 web site ([www.microchip.com/pic32](http://www.microchip.com/pic32)).

This document contains device-specific information for PIC32MX5XX/6XX/7XX devices.

Figure 1-1 illustrates a general block diagram of the core and peripheral modules in the PIC32MX5XX/6XX/7XX family of devices.

Table 1-1 lists the functions of the various pins shown in the pinout diagrams.

**FIGURE 1-1: BLOCK DIAGRAM<sup>(1,2)</sup>**

