# imall

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We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



# Contact us

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#### DC – 200 MHz 1:2 Fan-Out Buffer

#### FEATURES

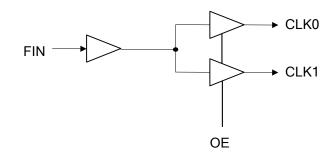
- Supports 3.3V, 2.5V, and 1.8V power supplies
- Frequency Support
  - 3.3V Supplies: DC 200 MHz
  - 2.5V Supplies: DC 150 MHz
  - 1.8V Supplies: DC 100 MHz
- Output Enable (OE) pin
- LVCMOS Input/Output
  - o Accepts SST and non SST clock signals
- Operating temperature range from -40°C to 85°C
- Available in space-saving 6-pin DFN GREEN/RoHS compliant package.

#### DESCRIPTION

#### **BLOCK DIAGRAM**

The PL123-02N is a low-cost general purpose 1-to-2 LVCMOS fan-out buffer. An output enable (OE) pin is available to enable the outputs or disable them into an active low state. When the outputs are disabled, the IC consumes less than  $5\mu$ A of power. The OE pin incorporates a pull up resistor giving a default condition of logic "1".

The input and outputs are LVCMOS levels and operate up to 200 MHz. Input signals with Spread Spectrum Modulation can also be used. The spread spectrum modulation will not be affected by the PL123-02N as the signal passes through the IC. A space-saving 6pin DFN package enables designs requiring minimal board area.



#### **PIN CONFIGURATION AND DESCRIPTION**

FIN	<b>1</b>	6 🤇	OE
CLK1	⊇ 2	5 🤇	VDD
GND	⊃ 3	4 🧲	CLK0

DFN-6L (2.0 x 1.3 x 0.6mm)

Name	Pin Assignment	Туре	Description		
FIN	1	I	Reference input pin		
CLK1	2	0	Clock Output		
GND	3	Р	GND connection		
CLK0	4	0	Clock Output		
VDD	5	Р	V <sub>DD</sub> connection		
OE	6	I	Output Enable (OE) input. Outputs are enabled when set high. Outputs are 'Active low' mode when set low.		



### DC – 200 MHz 1:2 Fan-Out Buffer

#### **ELECTRICAL SPECIFICATIONS**

#### **ABSOLUTE MAXIMUM RATINGS**

PARAMETERS	SYMBOL	MIN.	MAX.	UNITS
Supply Voltage Range	V <sub>DD</sub>	-0.5	4.6	V
Input Voltage Range	VI	-0.5	V <sub>DD</sub> +0.5	V
Output Voltage Range	Vo	-0.5	V <sub>DD</sub> +0.5	V
Soldering Temperature (Green package)			260	°C
Storage Temperature	Ts	-65	150	°C
Ambient Operating Temperature*		-40	85	°C

Exposure of the device under conditions beyond the limits specified by Maximum Ratings for extended periods may cause permanent damage to the device and affect product reliability. These conditions represent a stress rating only, and functional operations of the device at these or any other conditions above the operational limits noted in this specification is not implied. \*Operating temperature is guaranteed by design. Parts are tested to commercial grade only.

#### **AC SPECIFICATIONS**

PARAMETERS	CONDITIONS	MIN.	TYP.	MAX.	UNITS
	@ V <sub>DD</sub> =3.3V, 15pF Load			200	
Frequency <sup>[1]</sup>	@ V <sub>DD</sub> =2.5V, 15pF Load	DC		150	MHz
	@ V <sub>DD</sub> =1.8V, 15pF Load			100	
Input Voltage Low				$0.3 x V_{DD}$	V
Input Voltage High		$0.7 \mathrm{xV}_{\mathrm{DD}}$			V
Output Enable Time	Ta=25° C, 15pF Load			2	ms
Output Rise Time	15pF Load, 10/90% V <sub>DD</sub> , 3.3V		2.0	3.0	ns
Output Fall Time	15pF Load, 90/10% V <sub>DD</sub> , 3.3V		2.0	3.0	ns
Duty Cycle	Dependant upon input duty cycle				%

Notes: [1] Higher frequencies may be achieved for lower capacitive loads.

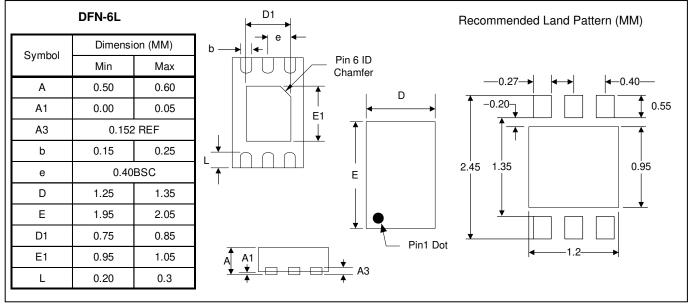


#### DC – 200 MHz 1:2 Fan-Out Buffer

#### **DC SPECIFICATIONS**

PARAMETERS	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Supply Current, Dynamic, with Loaded CMOS Output	IDD	@ $V_{DD}$ =3.3V, 32kHz, load=15pF		0.8		mA
Supply Current, Dynamic, with Loaded CMOS Output	I <sub>DD</sub>	@ V <sub>DD</sub> =2.5V, 32KHz, load=15pF		0.6		mA
Supply Current, Dynamic, with Loaded CMOS Output	I <sub>DD</sub>	@ V <sub>DD</sub> =1.8V, 32kHz, load=15pF		0.4		mA
Supply Current, Dynamic, with Loaded Outputs	I <sub>DD</sub>	When OE=0			5	μA
Operating Voltage	V <sub>DD</sub>		1.62		3.63	V
Output Low Voltage	V <sub>OL</sub>	I <sub>OL</sub> = +4mA			0.4	V
Output High Voltage	V <sub>OH</sub>	I <sub>ОН</sub> = -4mA	$V_{DD} - 0.4$			V
Output Current	I <sub>OSD</sub>	V <sub>OL</sub> = 0.4V, V <sub>OH</sub> = 2.4V	8			mA

#### PACKAGE DRAWINGS (GREEN PACKAGE COMPLIANT)





#### DC – 200 MHz 1:2 Fan-Out Buffer

#### **ORDERING INFORMATION (GREEN PACKAGE COMPLIANT)**

2180 For Tel: (4 The order number Part number, Pa	tune Drive, San Jose, 108) 944-0800 Fax: (4 <b>PART NUMBE</b> for this device is a co	08) 474-1000 <b>R</b> mbination of the following: ating temperature range				
PART NUMBER	PART NUMBER R=TAPE and REEL					
PACKAGE TYPE TEMPERATURE G=DFN-6L						
Part /Order Number	Marking*	Package Option				
PL123-02NGC-R	02N LLL	6-Pin DFN (Tape and Reel)				
PL123-02NGI-R	02N LLL	6-Pin DFN (Tape and Reel)				
*Note: LLL designates lot nu	ımber	1	-			

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