



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

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0 (0)  
0.15 (0.006)  
1.65 (0.065)  
1.8 (0.071)  
3.52 (0.138)  
5.2 (0.205)

8-8

Ø 3.45 (0.136)

Ø 3.81 (0.15)

Q-Q

5.2 (0.205)

Ø 8.62 (0.34)  $\pm 0.15$

0 (0)

1.65 (0.065)

6.6 (0.26)

7.61 (0.299)

7.8 (0.307)

80.5 (3.169)

63.5 (2.5)

50.8 (2)

38.1 (1.5)

25.4 (1)

12.7 (0.5)

0 (0)

12.7 (0.5)

25.4 (1)

38.1 (1.5)

50.8 (2)

63.5 (2.5)

80.5 (3.169)

25.4 (1)

30 (1.181)

59.4 (2.339)

Ø 3.2 (0.13)

86.5 (3.406)  $\pm 0.125$

81.3 (3.201)

69 (2.717)

50.8 (2)

27.1 (1.067)

25.4 (1)

0 (0)

25.4 (1)

27.1 (1.067)

50.8 (2)

69 (2.717)

81.3 (3.201)

86.5 (3.406)  $\pm 0.125$

Ø 3.2 (0.13)

R

PLL 120404

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

R

36 (1.417)

61 (2.402)

71.4 (2.811)  $\pm 0.25$

Ø 3.2 (0.13)

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

Q-Q

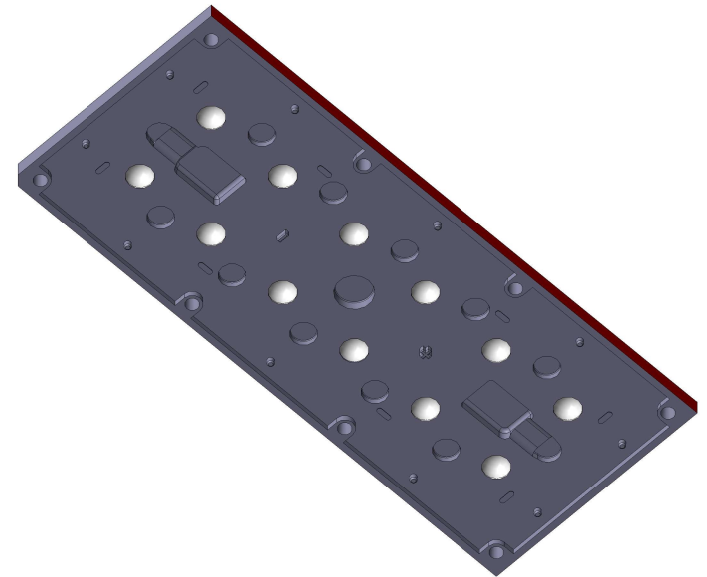
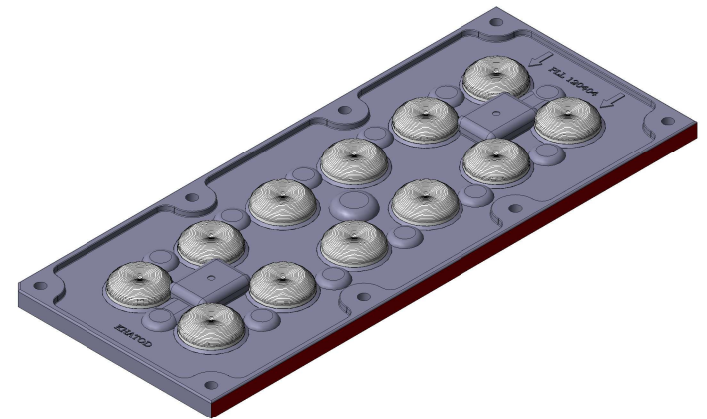
Q-Q

Q-Q

Ø 2 (0.079)

Ø 1.69 (0.074)

R-R



Note:

- Dimensions in inches into brackets
- For missing dimensions see 3D file
- WHITE surfaces: mirror polished
- GREY surfaces: embossed 25/27 VDI
- RED surface: gate area. Sink mark injection will be present
- Ⓢ = Quality Control Dimensions (QCD)
- Planarity tolerance +/- 0.80 mm

** Project and design owned by Khatod OPTOELECTRONIC s. r. l. ** The technical solutions adopted in this design are Khatod intellectual property, they cannot be reproduced and transmitted to third parties without our prior written permission. Unauthorized disclosure of confidential proprietary information will be prosecuted by law.			
DESCRIPTION :	12 Array lens	DESIGNER :	LM
CODE :	PLL 120404	CONTROLLED :	LS
KCHATOD Via Alessandrina, 25 Cusano Milanino MILANO ITALIA WWW.KCHATOD.COM		PROJECTS :	VI_II
All Dimensions in mm General Tolerances +/- 0.25 mm		DATA :	11/11/2016
HEAT TREATMENT: No One		MATERIAL: PMMA 8N	
SCALE : 1 : 1		SHEET : 1 of 1	