



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



# PM200RL1A060

FLAT-BASE TYPE  
INSULATED PACKAGE

## PM200RL1A060



### FEATURE

Inverter + Brake + Drive & Protection IC

- a) Adopting new 5th generation Full-Gate CSTBT™ chip
- b) The over-temperature protection which detects the chip surface temperature of CSTBT™ is adopted.
- c) Error output signal is possible from all each protection upper and lower arm of IPM.
- d) Compatible L-series package.

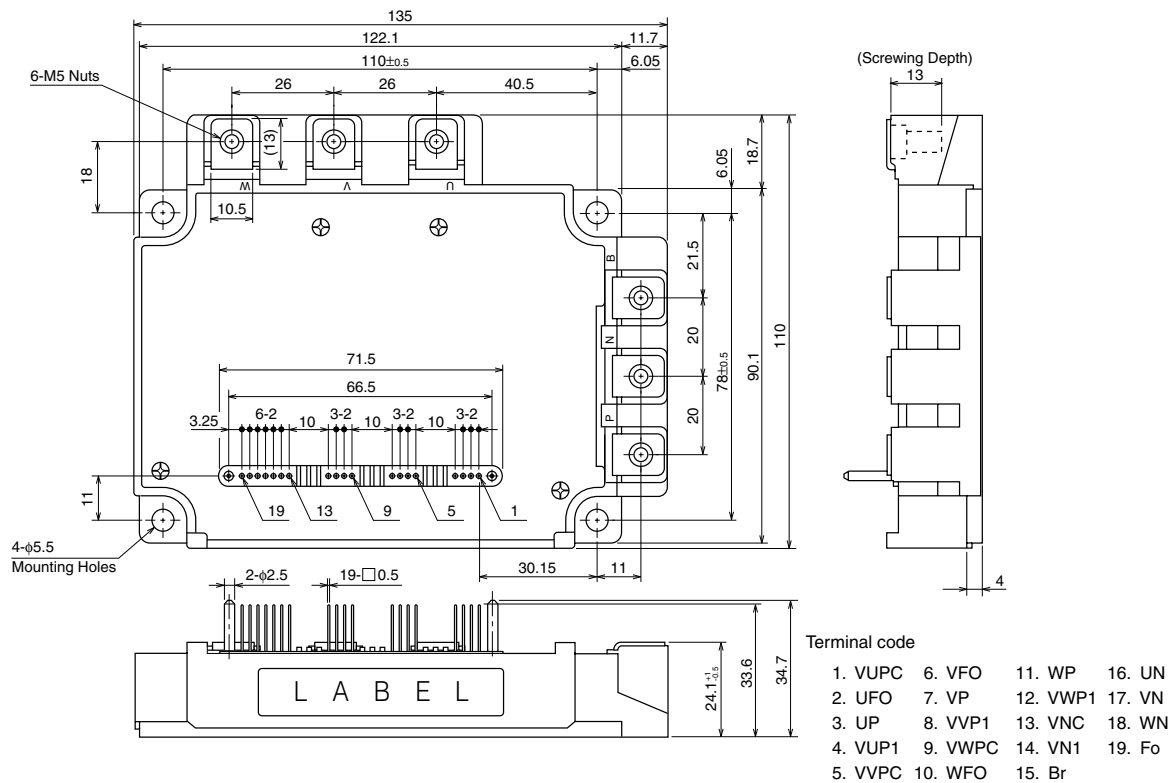
- 3φ 200A, 600V Current-sense and temperature sense IGBT type inverter
- Monolithic gate drive & protection logic
- Detection, protection & status indication circuits for, short-circuit, over-temperature & under-voltage (P-FO available from upper arm devices)
- UL Recognized

## APPLICATION

General purpose inverter, servo drives and other motor controls

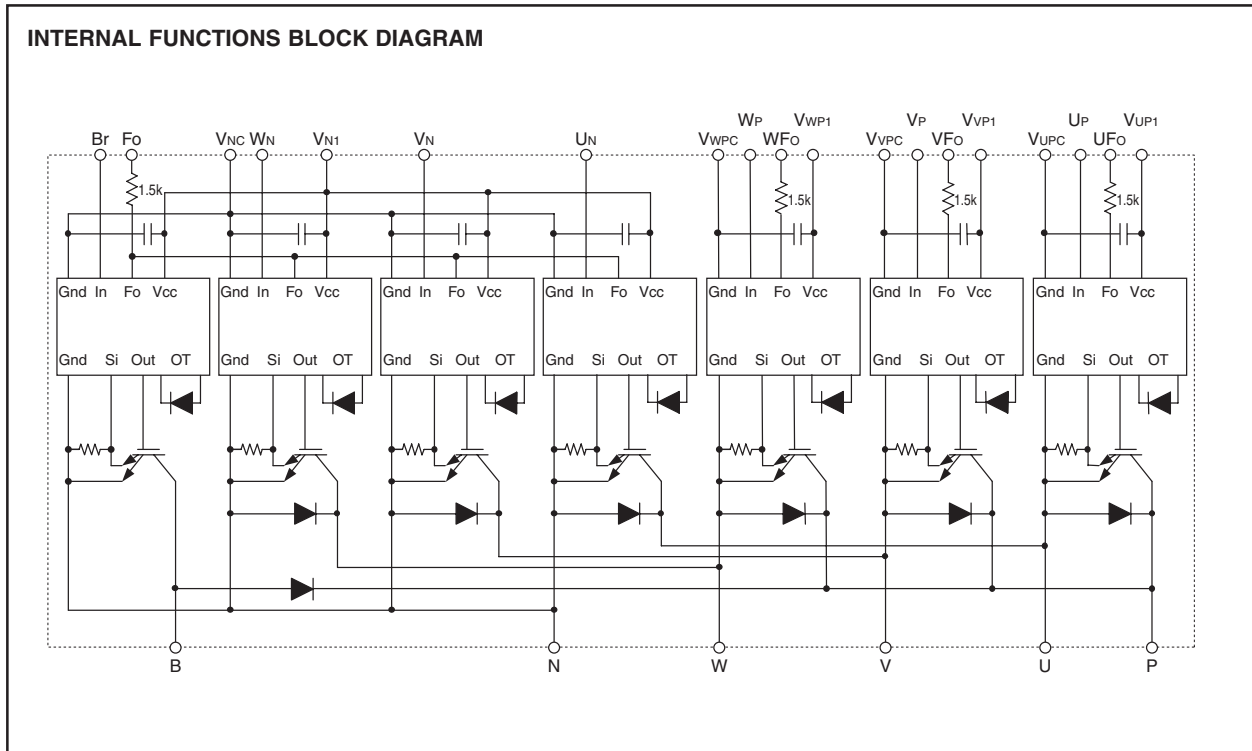
### PACKAGE OUTLINES

Dimensions in mm



# PM200RL1A060

FLAT-BASE TYPE  
INSULATED PACKAGE



**MAXIMUM RATINGS** (Tj = 25°C, unless otherwise noted)

**INVERTER PART**

Symbol	Parameter	Condition	Ratings	Unit
V <sub>CES</sub>	Collector-Emitter Voltage	V <sub>D</sub> = 15V, V <sub>CIN</sub> = 15V	600	V
±I <sub>C</sub>	Collector Current	T <sub>C</sub> = 25°C (Note-1)	200	A
±I <sub>CP</sub>	Collector Current (Peak)	T <sub>C</sub> = 25°C	400	A
P <sub>C</sub>	Collector Dissipation	T <sub>C</sub> = 25°C (Note-1)	625	W
T <sub>j</sub>	Junction Temperature		-20 ~ +150	°C

\*: T<sub>c</sub> measurement point is just under the chip.

**BRAKE PART**

Symbol	Parameter	Condition	Ratings	Unit
V <sub>CES</sub>	Collector-Emitter Voltage	V <sub>D</sub> = 15V, V <sub>CIN</sub> = 15V	600	V
I <sub>C</sub>	Collector Current	T <sub>C</sub> = 25°C (Note-1)	100	A
I <sub>CP</sub>	Collector Current (Peak)	T <sub>C</sub> = 25°C	200	A
P <sub>C</sub>	Collector Dissipation	T <sub>C</sub> = 25°C (Note-1)	390	W
I <sub>F</sub>	FWDi Forward Current	T <sub>C</sub> = 25°C	100	A
V <sub>R(DC)</sub>	FWDi Rated DC Reverse Voltage	T <sub>C</sub> = 25°C	600	V
T <sub>j</sub>	Junction Temperature		-20 ~ +150	°C

**CONTROL PART**

Symbol	Parameter	Condition	Ratings	Unit
V <sub>D</sub>	Supply Voltage	Applied between : V <sub>UP1</sub> -V <sub>UPC</sub> , V <sub>VP1</sub> -V <sub>VP1</sub> V <sub>WP1</sub> -V <sub>WP1</sub> , V <sub>VN1</sub> -V <sub>VNC</sub>	20	V
V <sub>CIN</sub>	Input Voltage	Applied between : U <sub>P</sub> -V <sub>UPC</sub> , V <sub>P</sub> -V <sub>VP1</sub> , W <sub>P</sub> -V <sub>WP1</sub> U <sub>N</sub> • V <sub>N</sub> • W <sub>N</sub> • B <sub>r</sub> -V <sub>NC</sub>	20	V
V <sub>F0</sub>	Fault Output Supply Voltage	Applied between : U <sub>F0</sub> -V <sub>UPC</sub> , V <sub>F0</sub> -V <sub>VP1</sub> , W <sub>F0</sub> -V <sub>WP1</sub> F <sub>0</sub> -V <sub>NC</sub>	20	V
I <sub>F0</sub>	Fault Output Current	Sink current at U <sub>F0</sub> , V <sub>F0</sub> , W <sub>F0</sub> , F <sub>0</sub> terminals	20	mA

**PM200RL1A060**

**FLAT-BASE TYPE  
INSULATED PACKAGE**

**TOTAL SYSTEM**

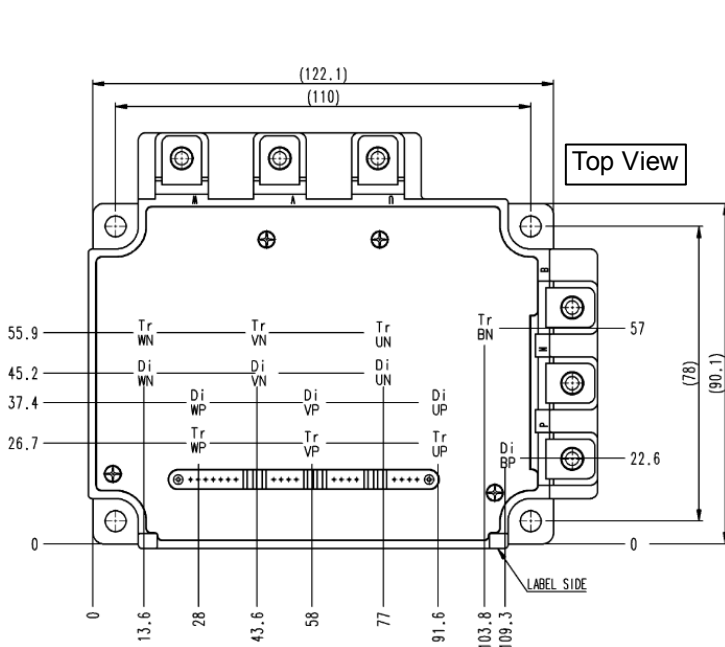
Symbol	Parameter	Conditions	Ratings	Unit
$V_{CC(Prot)}$	Supply Voltage Protected by SC	$V_D = 13.5V \sim 16.5V$ Inverter Part, $T_j = +125^\circ C$ Start	400	V
$V_{CC(surge)}$	Supply Voltage (Surge)	Applied between : P-N, Surge value	500	V
$T_{stg}$	Storage Temperature		-40 ~ +125	$^\circ C$
$V_{iso}$	Isolation Voltage	60Hz, Sinusoidal, Charged part to Base plate, AC 1min, RMS	2500	V

\*:  $T_C$  measurement point is just under the chip.

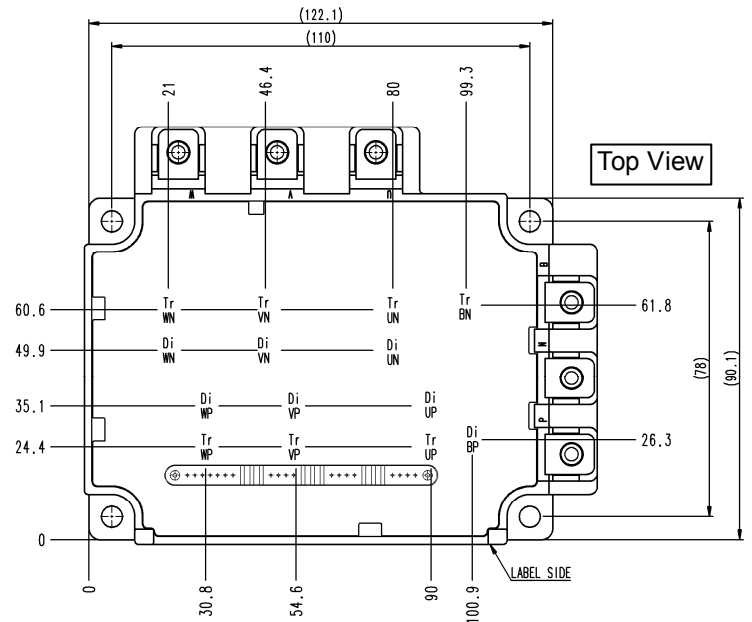
**THERMAL RESISTANCE**

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal Resistance	Inverter, IGBT (per 1 element) (Note.1)	-	-	0.20	$^\circ C/W$
$R_{th(j-c)F}$		Inverter, FwDi (per 1 element) (Note.1)	-	-	0.30	
$R_{th(j-c)Q}$		Brake, IGBT (Note.1)	-	-	0.32	
$R_{th(j-c)F}$		Brake, FwDi upper part (Note.1)	-	-	0.53	
$R_{th(c-f)}$	Contact Thermal Resistance	Case to fin, (per 1 module) Thermal grease applied (Note.1)	-	-	0.023	

Note.1: If you use this value,  $R_{th(f-a)}$  should be measured just under the chips.



PM200RL1A060



PM200RL1A060 350G

\* "350G" is printed on the label

**ELECTRICAL CHARACTERISTICS ( $T_j = 25^\circ C$ , unless otherwise noted)**

**INVERTER PART**

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_D = 15V, I_C = 200A$ $V_{CIN} = 0V$ , Pulsed (Fig. 1)	-	1.75	2.35	V
$V_{EC}$	FwDi Forward Voltage	$-I_C = 200A, V_D = 15V, V_{CIN} = 15V$ (Fig. 2)	-	1.7	2.8	V
$t_{on}$	Switching Time	$V_D = 15V, V_{CIN} = 0V \leftrightarrow 15V$ $V_{CC} = 300V, I_C = 200A$ $T_j = 125^\circ C$ Inductive Load (Fig. 3,4)	0.3	0.8	2.0	$\mu s$
$t_{rr}$			-	0.4	0.8	
$t_{c(on)}$			-	0.4	1.0	
$t_{c(off)}$			-	1.0	2.3	
$t_{c(off)}$			-	0.3	1.0	
$I_{CES}$	Collector-Emitter Cut-off Current	$V_{CE} = V_{CES}, V_D = 15V, V_{CIN} = 15V$ (Fig. 5)	-	-	1	mA
			-	-	10	

# PM200RL1A060

FLAT-BASE TYPE  
INSULATED PACKAGE

## BRAKE PART

Symbol	Parameter	Condition	Limits			Unit	
			Min.	Typ.	Max.		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	V <sub>D</sub> = 15V, I <sub>C</sub> = 100A V <sub>CIN</sub> = 0V, Pulsed (Fig. 1)	T <sub>J</sub> = 25°C	—	1.75	2.35	V
			T <sub>J</sub> = 125°C	—	1.75	2.35	
V <sub>EC</sub>	FWDi Forward Voltage	-I <sub>C</sub> = 100A, V <sub>CIN</sub> = 15V, V <sub>D</sub> = 15V (Fig. 2)	—	1.7	2.8	V	
I <sub>CES</sub>	Collector-Emitter Cutoff Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>D</sub> = 15V (Fig. 5)	T <sub>J</sub> = 25°C	—	—	1	mA
			T <sub>J</sub> = 125°C	—	—	10	

## CONTROL PART

Symbol	Parameter	Condition	Limits			Unit	
			Min.	Typ.	Max.		
I <sub>D</sub>	Circuit Current	V <sub>D</sub> = 15V, V <sub>CIN</sub> = 15V	V <sub>N1-VNC</sub>	—	8	16	mA
			V*P1-V*PC	—	2	4	
V <sub>th(ON)</sub>	Input ON Threshold Voltage	Applied between : UP-VUPC, VP-VVPC, WP-VWPC	1.2	1.5	1.8	V	
V <sub>th(OFF)</sub>	Input OFF Threshold Voltage	U <sub>N</sub> • V <sub>N</sub> • W <sub>N</sub> • B <sub>r</sub> -V <sub>Nc</sub>	1.7	2.0	2.3		
SC	Short Circuit Trip Level	-20 ≤ T <sub>J</sub> ≤ 125°C, V <sub>D</sub> = 15V (Fig. 3,6)	Inverter part	400	—	—	A
			Brake part	200	—	—	
t <sub>off(SC)</sub>	Short Circuit Current Delay Time	V <sub>D</sub> = 15V (Fig. 3,6)	—	0.2	—	μs	
OT	Over Temperature Protection	Detect Temperature of IGBT chip	Trip level	135	—	—	°C
			Hysteresis	—	20	—	
UV	Supply Circuit Under-Voltage Protection	-20 ≤ T <sub>J</sub> ≤ 125°C	Trip level	11.5	12.0	12.5	V
			Reset level	—	12.5	—	
I <sub>FO(H)</sub>	Fault Output Current	V <sub>D</sub> = 15V, V <sub>CIN</sub> = 15V (Note-2)	—	—	0.01	mA	
I <sub>FO(L)</sub>			—	10	15		
t <sub>FO</sub>	Minimum Fault Output Pulse Width	V <sub>D</sub> = 15V (Note-2)	1.0	1.8	—	ms	

(Note-2) Fault output is given only when the internal SC, OT & UV protections schemes of either upper or lower arm device operate to protect it.

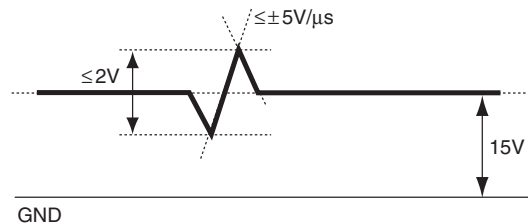
## MECHANICAL RATINGS AND CHARACTERISTICS

Symbol	Parameter	Condition	Limits			Unit
			Min.	Typ.	Max.	
—	Mounting torque	Mounting part screw : M5	2.5	3.0	3.5	N • m
—	Mounting torque	Main terminal part screw : M5	2.5	3.0	3.5	N • m
—	Weight	—	—	800	—	g

## RECOMMENDED CONDITIONS FOR USE

Symbol	Parameter	Condition	Recommended value	Unit
V <sub>CC</sub>	Supply Voltage	Applied across P-N terminals	≤ 400	V
V <sub>D</sub>	Control Supply Voltage	Applied between : VUP1-VUPC, VVP1-VVPC VWP1-VWPC, VN1-VNC (Note-3)	15.0 ± 1.5	V
V <sub>CIN(ON)</sub>	Input ON Voltage	Applied between : UP-VUPC, VP-VVPC, WP-VWPC UN • VN • WN • Br-VNC	≤ 0.8	V
V <sub>CIN(OFF)</sub>	Input OFF Voltage		≥ 9.0	
f <sub>PWM</sub>	PWM Input Frequency	Using Application Circuit of Fig. 8	≤ 20	kHz
t <sub>dead</sub>	Arm Shoot-through Blocking Time	For IPM's each input signals (Fig. 7)	≥ 2.0	μs

(Note-3) With ripple satisfying the following conditions: dv/dt swing ≤ ±5V/μs, Variation ≤ 2V peak to peak

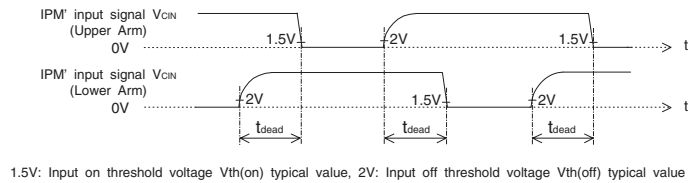
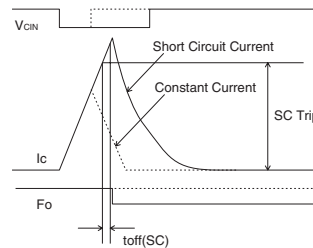
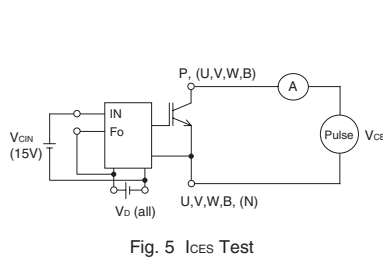
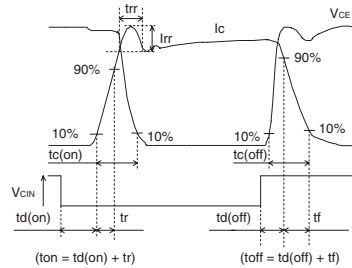
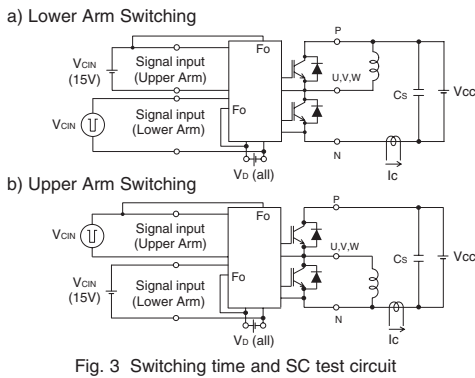
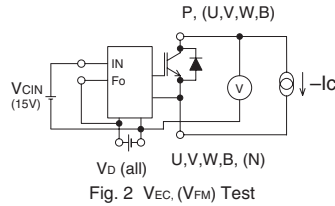
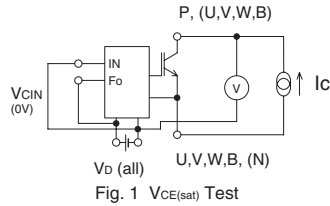


# PM200RL1A060

FLAT-BASE TYPE  
INSULATED PACKAGE

## PRECAUTIONS FOR TESTING

- Before applying any control supply voltage ( $V_D$ ), the input terminals should be pulled up by resistors, etc. to their corresponding supply voltage and each input signal should be kept off state.  
After this, the specified ON and OFF level setting for each input signal should be done.
- When performing "SC" tests, the turn-off surge voltage spike at the corresponding protection operation should not be allowed to rise above  $V_{CES}$  rating of the device.  
(These test should not be done by using a curve tracer or its equivalent.)



# PM200RL1A060

FLAT-BASE TYPE  
INSULATED PACKAGE

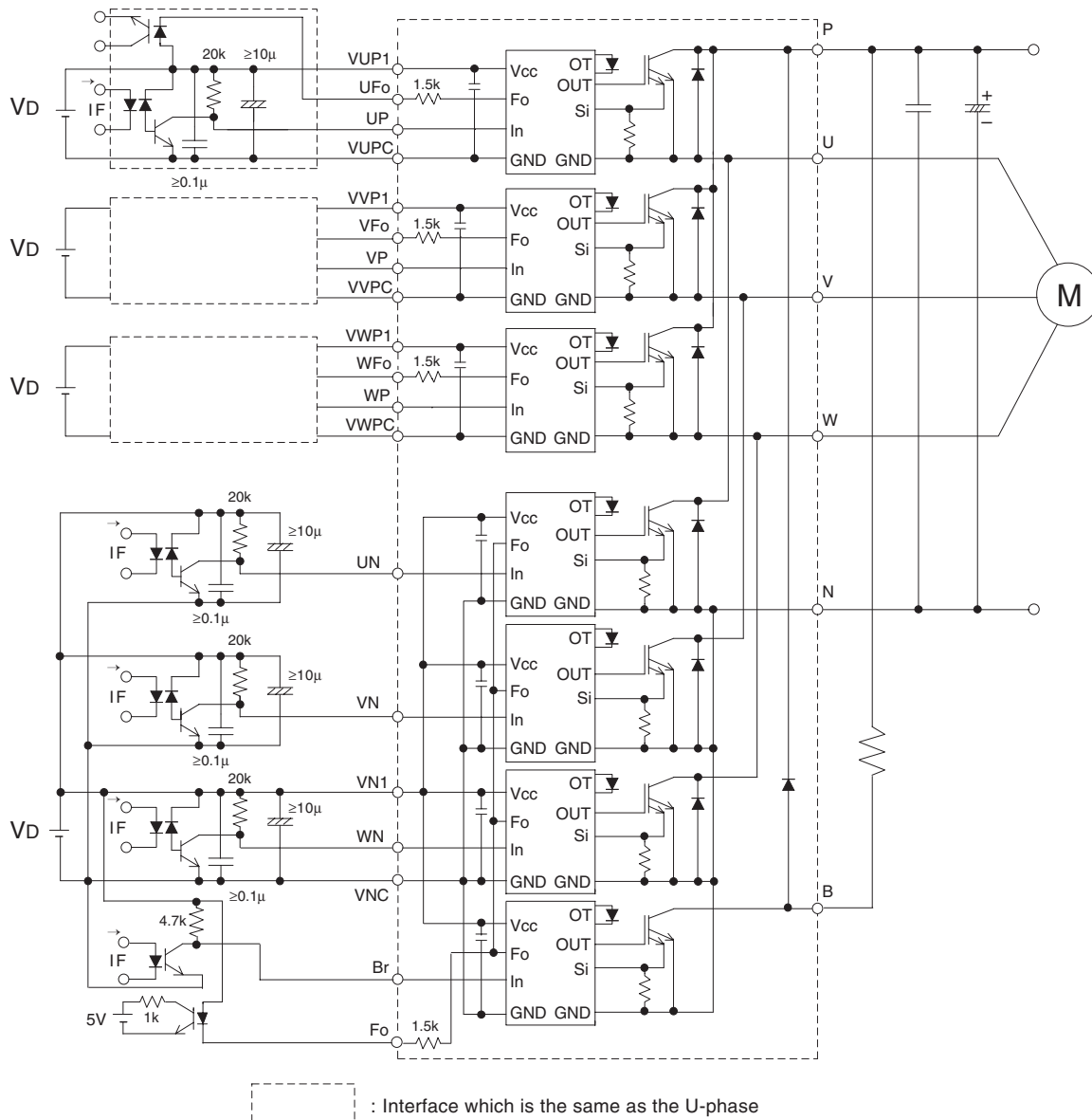


Fig. 8 Application Example Circuit

**NOTES FOR STABLE AND SAFE OPERATION ;**

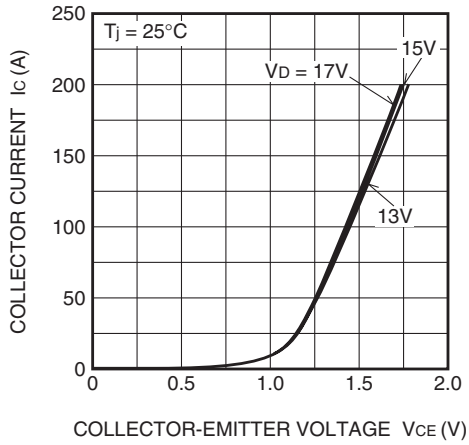
- Design the PCB pattern to minimize wiring length between opto-coupler and IPM's input terminal, and also to minimize the stray capacity between the input and output wirings of opto-coupler.
- Connect low impedance capacitor between the Vcc and GND terminal of each fast switching opto-coupler.
- Fast switching opto-couplers:  $t_{PLH}, t_{PHL} \leq 0.8\mu s$ , Use High CMR type.
- Slow switching opto-coupler: CTR > 100%
- Use 4 isolated control power supplies (VD). Also, care should be taken to minimize the instantaneous voltage charge of the power supply.
- Make inductance of DC bus line as small as possible, and minimize surge voltage using snubber capacitor between P and N terminal.
- Use line noise filter capacitor (ex. 4.7nF) between each input AC line and ground to reject common-mode noise from AC line and improve noise immunity of the system.

# PM200RL1A060

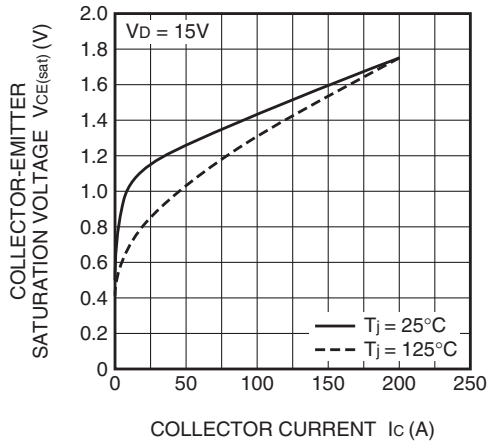
FLAT-BASE TYPE  
INSULATED PACKAGE

**PERFORMANCE CURVES**  
(Inverter Part)

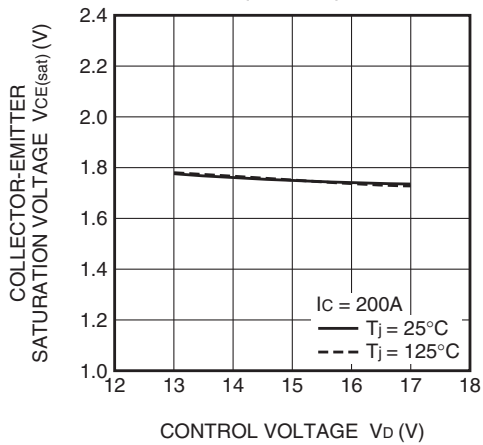
**OUTPUT CHARACTERISTICS (TYPICAL)**



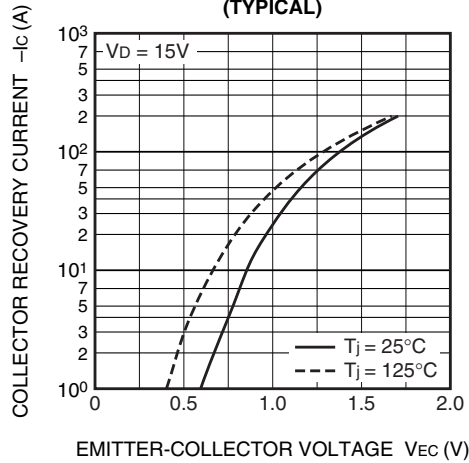
**COLLECTOR-EMITTER SATURATION VOLTAGE (VS.  $I_c$ ) CHARACTERISTICS (TYPICAL)**



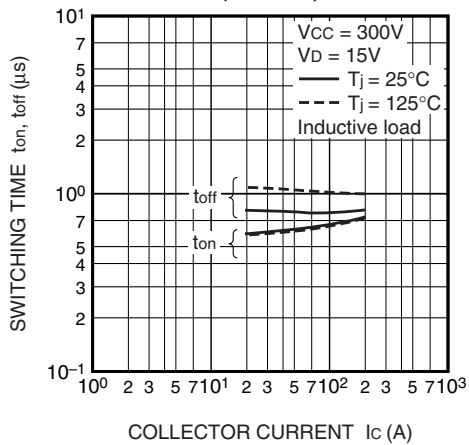
**COLLECTOR-EMITTER SATURATION VOLTAGE (VS.  $V_D$ ) CHARACTERISTICS (TYPICAL)**



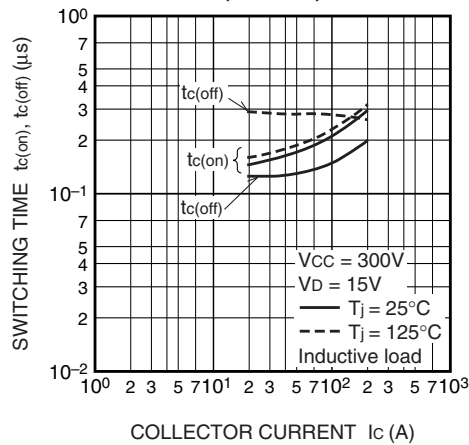
**DIODE FORWARD CHARACTERISTICS (TYPICAL)**



**SWITCHING TIME ( $t_{on}$ ,  $t_{off}$ ) CHARACTERISTICS (TYPICAL)**



**SWITCHING TIME ( $t_{c(on)}$ ,  $t_{c(off)}$ ) CHARACTERISTICS (TYPICAL)**

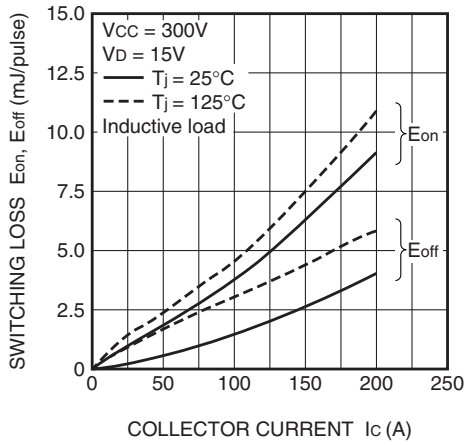




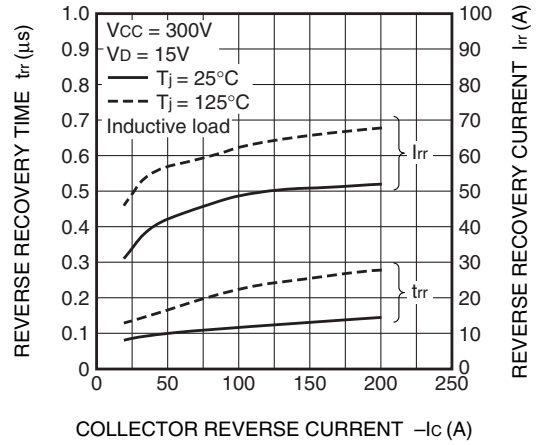
# PM200RL1A060

FLAT-BASE TYPE  
INSULATED PACKAGE

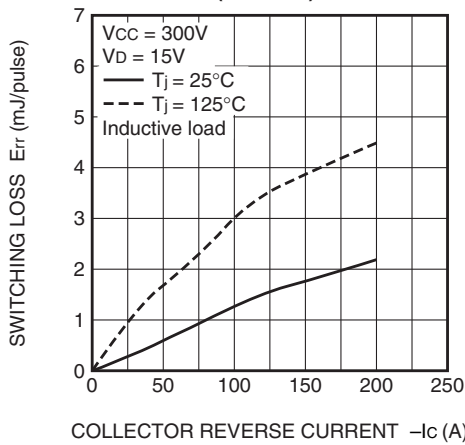
**SWITCHING LOSS CHARACTERISTICS (TYPICAL)**



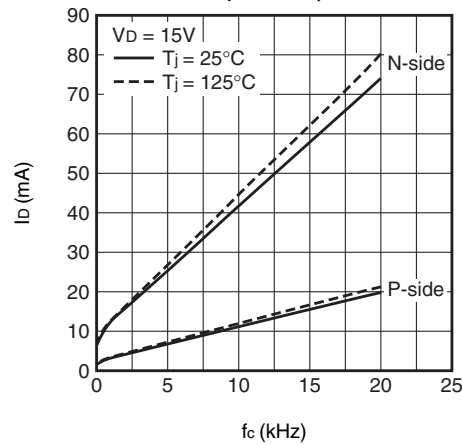
**DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)**



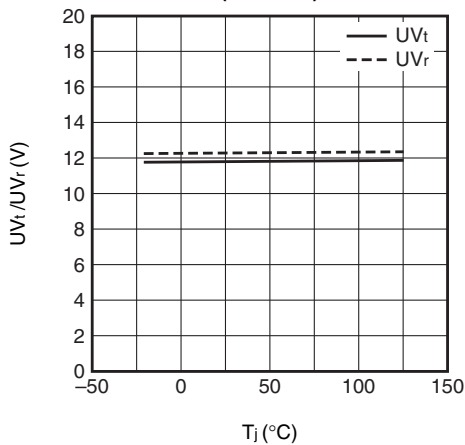
**SWITCHING RECOVERY LOSS CHARACTERISTICS (TYPICAL)**



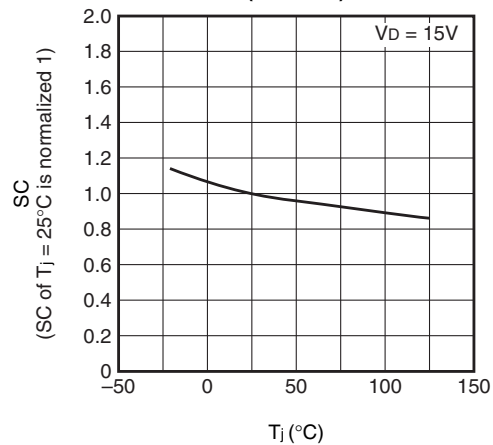
**$I_D$  VS.  $f_c$  CHARACTERISTICS (TYPICAL)**



**UV TRIP LEVEL VS.  $T_j$  CHARACTERISTICS (TYPICAL)**



**SC TRIP LEVEL VS.  $T_j$  CHARACTERISTICS (TYPICAL)**



# PM200RL1A060

FLAT-BASE TYPE  
INSULATED PACKAGE

(Brake Part)

