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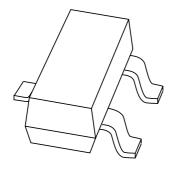






DISCRETE SEMICONDUCTORS

DATA SHEET



PMBT2907; PMBT2907A PNP switching transistors

Product data sheet Supersedes data of 1999 Apr 27 2004 Jan 16



PNP switching transistors

PMBT2907; PMBT2907A

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 60 V).

APPLICATIONS

• Switching and linear amplification.

DESCRIPTION

PNP switching transistor in a SOT23 plastic package. NPN complements: PMBT2222 and PMBT2222A.

MARKING

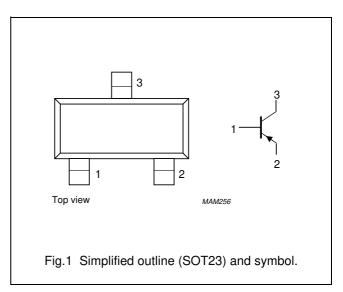
TYPE NUMBER	MARKING CODE(1)
PMBT2907	*2B
PMBT2907A	*2F

Note

- 1. * = p : Made in Hong Kong.
 - * = t : Made in Malaysia.
 - * = W: Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE	PACKAGE				
NUMBER	NAME	DESCRIPTION	VERSION		
PMBT2907	-	plastic surface mounted package; 3 leads	SOT23		
PMBT2907A	_	plastic surface mounted package; 3 leads	SOT23		

PNP switching transistors

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-60	V
V_{CEO}	collector-emitter voltage	open base			
	PMBT2907		_	-40	V
	PMBT2907A		_	-60	V
V _{EBO}	emitter-base voltage	open collector	_	- 5	V
I _C	collector current (DC)		_	-600	mA
I _{CM}	peak collector current		_	-800	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

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CHARACTERISTICS

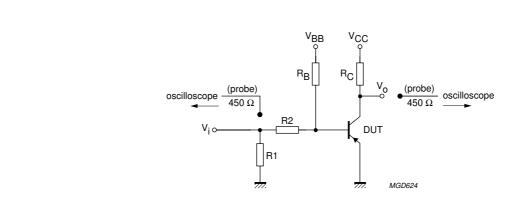
 T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$I_E = 0; V_{CB} = -50 \text{ V}$			
	PMBT2907		_	-20	nA
	PMBT2907A		_	-10	nA
	collector-base cut-off current	$I_E = 0$; $V_{CB} = -50 \text{ V}$; $T_j = 125 \text{ °C}$			
	PMBT2907		_	-20	μΑ
	PMBT2907A		_	-10	μΑ
I _{EBO}	emitter-base cut-off current	$I_{C} = 0; V_{EB} = -5 \text{ V}$	_	-50	nA
h _{FE}	DC current gain	$I_C = -0.1 \text{ mA}; V_{CE} = -10 \text{ V}$			
	PMBT2907		35	_	
	PMBT2907A		75	_	
	DC current gain	$I_{C} = -1 \text{ mA}; V_{CE} = -10 \text{ V}$			
	PMBT2907		50	_	
	PMBT2907A		100	_	
	DC current gain	$I_{C} = -10 \text{ mA}; V_{CE} = -10 \text{ V}$			
	PMBT2907		75	_	
	PMBT2907A		100	_	
	DC current gain	$I_C = -150 \text{ mA}; V_{CE} = -10 \text{ V}$	100	300	
	DC current gain	$I_C = -500 \text{ mA}; V_{CE} = -10 \text{ V}$			
	PMBT2907		30	_	
	PMBT2907A		50	_	
V _{CEsat}	collector-emitter saturation	$I_C = -150 \text{ mA}; I_B = -15 \text{ mA}$	_	-400	mV
	voltage	$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}$	_	-1.6	V
V _{BEsat}	base-emitter saturation voltage	$I_C = -150 \text{ mA}; I_B = -15 \text{ mA}$	_	-1.3	V
		$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}$	_	-2.6	V
C _c	collector capacitance	$I_E = I_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	8	pF
C _e	emitter capacitance	$I_C = I_c = 0$; $V_{EB} = -2 \text{ V}$; $f = 1 \text{ MHz}$	_	30	pF
f _T	transition frequency	$I_C = -50 \text{ mA}; V_{CE} = -20 \text{ V}; f = 100 \text{ MHz}$	200	_	MHz
Switching t	imes (between 10% and 90% leve	els); (see Fig.2)			
t _{on}	turn-on time	$I_{Con} = -150 \text{ mA}; I_{Bon} = -15 \text{ mA};$	_	40	ns
t _d	delay time	I _{Boff} = 15 mA	_	12	ns
t _r	rise time		_	30	ns
t _{off}	turn-off time		_	365	ns
t _s	storage time		_	300	ns
t _f	fall time		_	65	ns

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$$\begin{split} V_i &= -9.5 \ V; \, T = 500 \ \mu s; \, t_p = 10 \ \mu s; \, t_r = t_f \leq 3 \ ns. \\ R1 &= 68 \ \Omega; \, R2 = 325 \ \Omega; \, R_B = 325 \ \Omega; \, R_C = 160 \ \Omega. \\ V_{BB} &= 3.5 \ V; \, V_{CC} = -29.5 \ V. \end{split}$$

Oscilloscope: input impedance Z_i = 50 Ω .

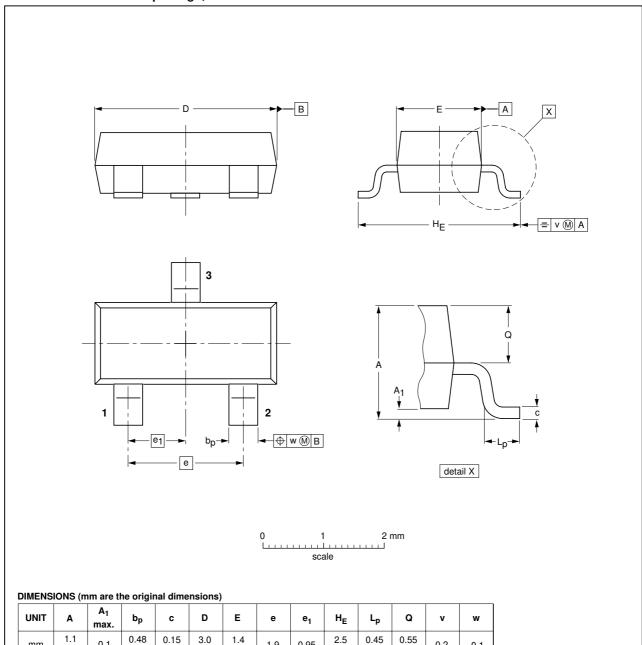
Fig.2 Test circuit for switching times.

PNP switching transistors

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PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads SOT23



UNIT	Α	max.	bp	С	D	E	е	e ₁	HE	Lp	Q	V	W
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC			ISSUE DATE	
SOT23		TO-236AB				-04-11-04- 06-03-16

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PNP switching transistors

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DATA SHEET STATUS

DOCUMENT STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

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