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Kind regards,

Team Nexperia



PMBT3906

PNP switching transistor Rev. 06 — 2 March 2010

Product data sheet

Product profile

1.1 General description

PNP switching transistor in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package.

NPN complement: PMBT3904.

1.2 Features and benefits

- Collector-emitter voltage V_{CEO} = -40 V
- Collector current capability I_C = -200 mA

1.3 Applications

General amplification and switching

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-40	V
I _C	collector current		-	-	-200	mA

Pinning information 2.

Table 2. **Pinning**

Pin	Description	Simplified outline	Graphic symbol
1	base		0
2	emitter		3
3	collector	1 2	1—
			006aab25



3. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMBT3906	-	plastic surface-mounted package; 3 leads	SOT23		

4. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMBT3906	*2A

[1] * = -: made in Hong Kong

* = p: made in Hong Kong

* = t: made in Malaysia

* = W: made in China

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	-40	V
V_{CEO}	collector-emitter voltage	open base	-	-40	V
V_{EBO}	emitter-base voltage	open collector	-	-6	V
I _C	collector current		-	-200	mA
I _{CM}	peak collector current		-	-200	mA
I _{BM}	peak base current		-	-100	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$	<u>[1]</u> -	250	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C
T _{stg}	storage temperature		-65	+150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB).

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	500	K/W

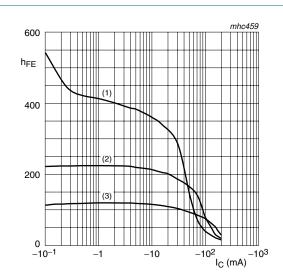
^[1] Device mounted on an FR4 PCB.

7. Characteristics

Table 7. Characteristics

T_{amb} = 25 °C unless otherwise specified.

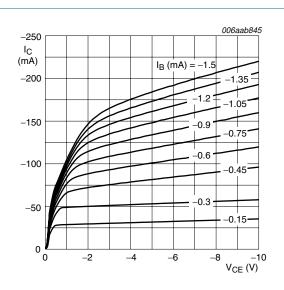
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = -30 \text{ V}; I_E = 0 \text{ A}$	-	-	-50	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -6 \text{ V}; I_C = 0 \text{ A}$	-	-	-50	nA
h _{FE}	DC current gain	$V_{CE} = -1 V$				
		$I_C = -0.1 \text{ mA}$	60	-	-	
		$I_C = -1 \text{ mA}$	80	-	-	
		$I_C = -10 \text{ mA}$	100	-	300	
		$I_C = -50 \text{ mA}$	60	-	-	
		$I_{\rm C} = -100 \; {\rm mA}$	30	-	-	
V _{CEsat}	collector-emitter	$I_C = -10 \text{ mA}; I_B = -1 \text{ mA}$	-	-	-250	mV
	saturation voltage	$I_C = -50 \text{ mA}; I_B = -5 \text{ mA}$	-	-	-400	mV
V _{BEsat}		$I_C = -10 \text{ mA}; I_B = -1 \text{ mA}$	-	-	-850	mV
saturation v	saturation voltage	$I_C = -50 \text{ mA}; I_B = -5 \text{ mA}$	-	-	-950	mV
t _d	delay time	$I_{Con} = -10 \text{ mA};$	-	-	35	ns
t _r	rise time	$I_{Bon} = -1 \text{ mA};$	-	-	35	ns
t _{on}	turn-on time	I _{Boff} = 1 mA	-	-	70	ns
ts	storage time		-	-	225	ns
t _f	fall time		-	-	75	ns
t _{off}	turn-off time		-	-	300	ns
f _T	transition frequency	$V_{CE} = -20 \text{ V};$ $I_{C} = -10 \text{ mA};$ f = 100 MHz	250	-	-	MHz
C _c	collector capacitance	$\begin{split} V_{CB} &= -5 \text{ V; } I_E = i_e = 0 \text{ A;} \\ f &= 1 \text{ MHz} \end{split}$	-	-	4.5	pF
C _e	emitter capacitance	$V_{EB} = -500 \text{ mV};$ $I_{C} = i_{c} = 0 \text{ A}; f = 1 \text{ MHz}$	-	-	10	pF
NF	noise figure	$I_{C} = -100 \ \mu A;$ $V_{CE} = -5 \ V; \ R_{S} = 1 \ k\Omega;$ $f = 10 \ Hz \ to \ 15.7 \ kHz$	-	-	4	dB



$$V_{CE} = -1 V$$

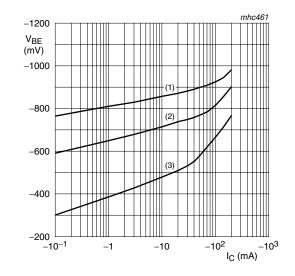
- (1) $T_{amb} = 150 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

Fig 1. DC current gain as a function of collector current; typical values



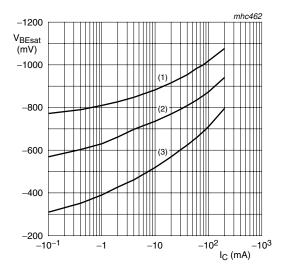
 $T_{amb} = 25 \, ^{\circ}C$

Fig 2. Collector current as a function of collector-emitter voltage; typical values



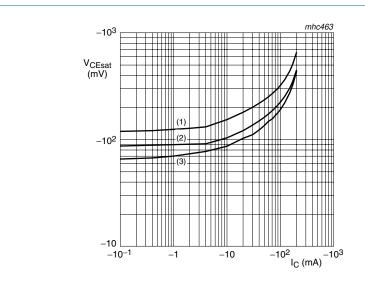
- $V_{CE} = -1 V$
- (1) $T_{amb} = -55 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 150 \, ^{\circ}C$

Fig 3. Base-emitter voltage as a function of collector current; typical values



- $I_{\rm C}/I_{\rm B} = 10$
- (1) $T_{amb} = -55 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 150 \, ^{\circ}C$

Fig 4. Base-emitter saturation voltage as a function of collector current; typical values



 $I_C/I_B = 10$

- (1) $T_{amb} = 150 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

Fig 5. Collector-emitter saturation voltage as a function of collector current; typical values

8. Test information

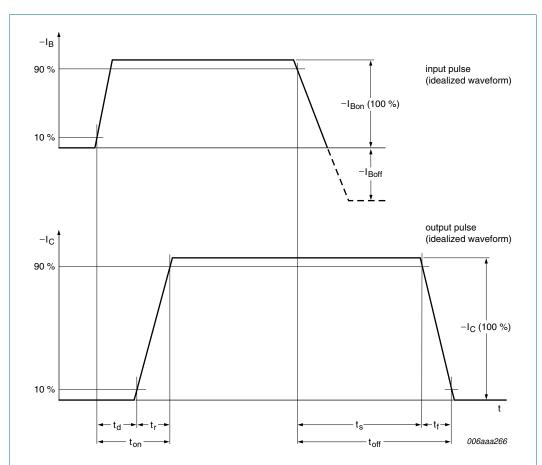
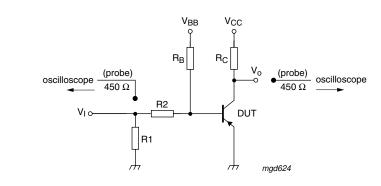


Fig 6. BISS transistor switching time definition



 $V_{l}=5$ V; T = 500 $\mu s;$ $t_{p}=10$ $\mu s;$ $t_{r}=t_{f}\leq 3$ ns

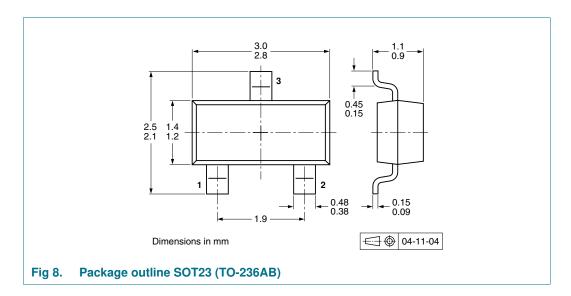
R1 = 56 $\Omega;$ R2 = 2.5 k $\Omega;$ R_B = 3.9 k $\Omega;$ R_C = 270 Ω

 V_{BB} = 1.9 V; V_{CC} = -3 V

Oscilloscope: input impedance $Z_i = 50 \Omega$

Fig 7. Test circuit for switching times

9. Package outline



10. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity	
			3000	10000
PMBT3906	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235

^[1] For further information and the availability of packing methods, see Section 13.

11. Revision history

Table 9. **Revision history**

	- · · · · ·					
Document ID	Release date	Data sheet status	Change notice	Supersedes		
PMBT3906_6	20100302	Product data sheet	-	PMBT3906_N_5		
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. 					
	 Legal texts have been adapted to the new company name where appropriate. 					
	Section 4 "Marking": amended					
	<u>Table 7 "Characteristics"</u> : F redefined to NF noise figure					
	Section 8 "Test information": added					
	• Figure 6: added					
	Figure 8: superseded by minimized package outline drawing					
	 Section 10 	"Packing information": adde	ed			
	Section 12	"Legal information": update	d			
PMBT3906_N_5	20071004	Product data sheet	-	PMBT3906_4		
PMBT3906_4	20040121	Product specification	-	PMBT3906_3		
PMBT3906_3	19990427	Product specification	-	PMBT3906_CNV_2		
PMBT3906_CNV_2	19970505	Product specification	-	-		

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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PNP switching transistor

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PMBT3906

PNP switching transistor

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