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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

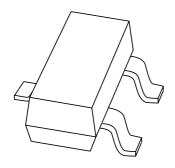






DISCRETE SEMICONDUCTORS

DATA SHEET



PMBT5401 PNP high-voltage transistor

Product specification Supersedes data of 1999 Apr 15 2004 Jan 21





PNP high-voltage transistor

PMBT5401

FEATURES

• Low current (max. 300 mA)

• High voltage (max. 150 V).

APPLICATIONS

• Switching and amplification in high voltage applications such as telephony.

DESCRIPTION

PNP high-voltage transistor in a SOT23 plastic package. NPN complement: PMBT5550.

MARKING

TYPE NUMBER	MARKING CODE(1)	
PMBT5401	*2L	

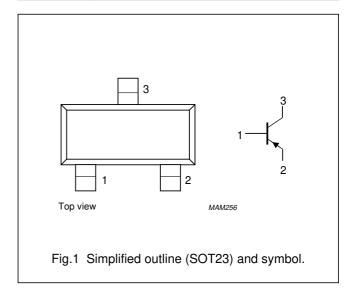
Note

1. * = p : Made in Hong Kong.

* = t : Made in Malaysia. * = W : Made in China.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



ORDERING INFORMATION

TYPE	PACKAGE				
NUMBER	NAME	NAME DESCRIPTION VERSION			
PMBT5401	_	plastic surface mounted package; 3 leads SOT23			

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-160	V
V _{CEO}	collector-emitter voltage	open base	_	-150	V
V _{EBO}	emitter-base voltage	open collector	_	-5	V
I _C	collector current (DC)		_	-300	mA
I _{CM}	peak collector current		_	-600	mA
I _{BM}	peak base current		_	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

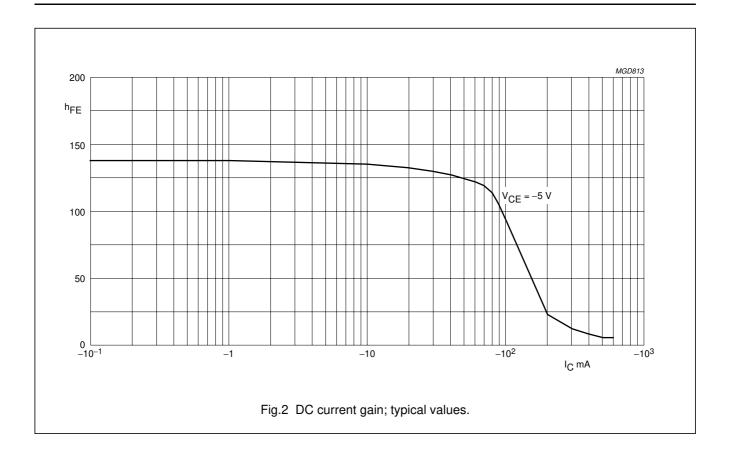
 T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	I _E = 0; V _{CB} = -120 V	_	-50	nA
		I _E = 0; V _{CB} = -120 V; T _{amb} = 150 °C	_	-50	μΑ
I _{EBO}	emitter-base cut-off current	$I_C = 0; V_{EB} = -4 V$	_	-50	nA
h _{FE}	DC current gain	V _{CE} = -5 V; (see Fig.2)			
		$I_C = -1 \text{ mA}$	50	_	
		$I_{C} = -10 \text{ mA}$	60	240	
		$I_C = -50 \text{ mA}$	50	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -1 \text{ mA}$	_	-200	mV
		$I_C = -50 \text{ mA}; I_B = -5 \text{ mA}$	_	-500	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -1 \text{ mA}$	_	-1	V
		$I_C = -50 \text{ mA}; I_B = -5 \text{ mA}$	_	-1	V
C _c	collector capacitance	$I_E = I_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	6	pF
f _T	transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -10 \text{ V};$ f = 100 MHz; $T_{amb} = 25 \text{ °C}$	100	300	MHz
F	noise figure	I_C = -200 μA; V_{CE} = -5 V; R_S = 2 kΩ; f = 10 Hz to 15.7 kHz; T_{amb} = 25 °C	_	8	dB

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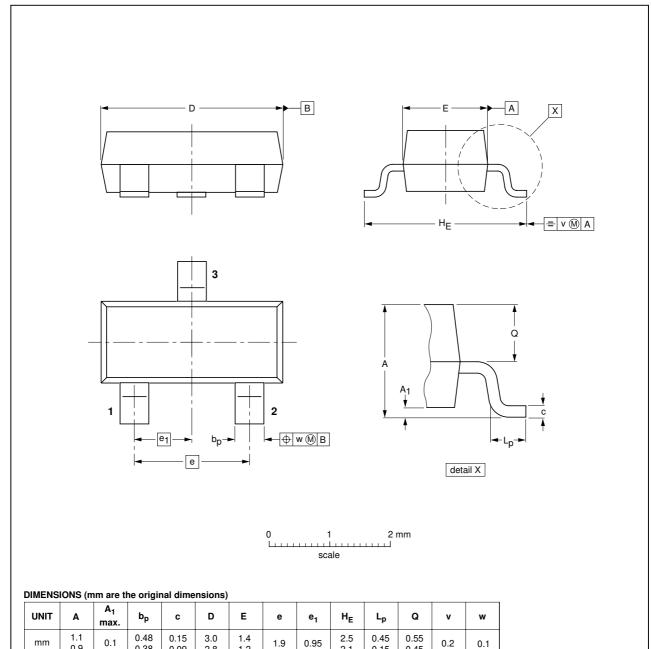
PNP high-voltage transistor

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



OUTLINE	IE REFERENCES			EUROPEAN	IOOUE DATE		
VERSION	IEC JEDEC		EIAJ		PROJECTION	ISSUE DATE	
SOT23		TO-236AB				-97-02-28- 99-09-13	

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Philips Semiconductors Product specification

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DATA SHEET STATUS

LEVEL	DATA SHEET STATUS(1)	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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Contact information

For additional information please visit http://www.semiconductors.philips.com. Fax: +31 40 27 24825 For sales offices addresses send e-mail to: sales.addresses@www.semiconductors.philips.com.

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