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PMEG4010ETP

40 V, 1 A low VF MEGA Schottky barrier rectifier Rev. 1 — 5 October 2011 Pro

Product data sheet

Product profile 1.

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

1.2 Features and benefits

- Average forward current: I_{F(AV)} ≤ 1 A
- Reverse voltage: V_R ≤ 40 V
- Low forward voltage
- High power capability due to clip-bonding technology

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply

1.4 Quick reference data

- Small and flat lead SMD plastic package
- AEC-Q101 qualified
- High temperature T_i ≤ 175 °C
- Reverse polarity protection
- Low power consumption applications
- High temperature applications

| Table 1. | Quick reference data | | | | | | |
|--------------------|----------------------------|--|-----|-----|-----|-----|------|
| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
| I _{F(AV)} | average forward current | square wave; δ = 0.5; f = 20 kHz; T _{amb} ≤ 145 °C | [1] | - | - | 1 | А |
| | | square wave; δ = 0.5; f = 20 kHz; T _{sp} ≤ 165 °C | | - | - | 1 | А |
| V _R | reverse voltage | T _j = 25 °C | | - | - | 40 | V |
| V _F | forward voltage | I _F = 1 A; T _j = 25 °C | | - | 430 | 490 | mV |
| I _R | reverse current | $V_{R} = 40 V; T_{j} = 25 °C$ | | - | 10 | 50 | μA |
| | | | | | | | |

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.



40 V, 1 A low VF MEGA Schottky barrier rectifier

2. Pinning information

| Table 2. | Pinning | j information | | |
|----------|---------|------------------------|--------------------|----------------|
| Pin | Symbol | Description | Simplified outline | Graphic symbol |
| 1 | К | cathode ^[1] | | . 54 - |
| 2 | А | anode | 1 | 1 🕂 2 |
| | | | | sym001 |
| | | | SOD128 | |

[1] The marking bar indicates the cathode.

3. Ordering information

| Table 3. Ordering in | nformation | | |
|----------------------|------------|--|---------|
| Type number | Package | | |
| | Name | Description | Version |
| PMEG4010ETP | - | plastic surface-mounted package; 2 leads | SOD128 |

4. Marking

| Table 4. Marking codes | |
|------------------------|--------------|
| Type number | Marking code |
| PMEG4010ETP | C1 |

40 V, 1 A low VF MEGA Schottky barrier rectifier

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Мах | Unit |
|--------------------|--|---|------------|-----|------|------|
| V _R | reverse voltage | T _j = 25 °C | | - | 40 | V |
| I _{F(AV)} | average forward current | square wave; δ = 0.5; f = 20 kHz; T _{amb} ≤ 145 °C | <u>[1]</u> | - | 1 | А |
| | | square wave; δ = 0.5; f = 20 kHz; T _{sp} ≤ 165 °C | | - | 1 | А |
| I _{FSM} | non-repetitive peak forward current | square wave; $t_p = 8 \text{ ms}$; $T_{j(init)} = 25 \text{ °C}$ | | - | 50 | А |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [2][3] | - | 750 | mW |
| | | | [4][3] | - | 1250 | mW |
| | | | [1][3] | - | 2500 | mW |
| Tj | junction temperature | | | - | 175 | °C |
| T _{amb} | ambient temperature | | | -55 | 175 | °C |
| T _{stg} | storage temperature | | | -65 | 175 | °C |
| - | | | | | | |

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Reflow soldering is the only recommended soldering method.

[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

6. Thermal characteristics

| Table 6. | Thermal characteristics | | | | | | |
|----------------------|--|-------------|------------|-----|-----|-----|------|
| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
| R _{th(j-a)} | thermal resistance | in free air | [1][2][3] | - | - | 200 | K/W |
| | from junction to ambient | | [1][4][3] | - | | 120 | K/W |
| | amplent | | [1][5][3] | - | - | | K/W |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | <u>[6]</u> | - | - | 12 | K/W |

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Reflow soldering is the only recommended soldering method.

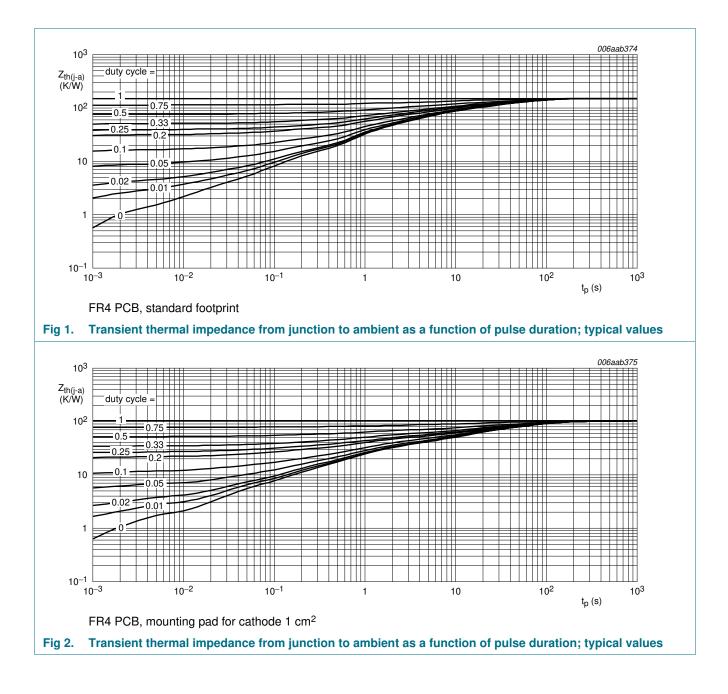
[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[5] Device mounted on a ceramic PCB, Al_2O_3 , standard footprint.

[6] Soldering point of cathode tab.

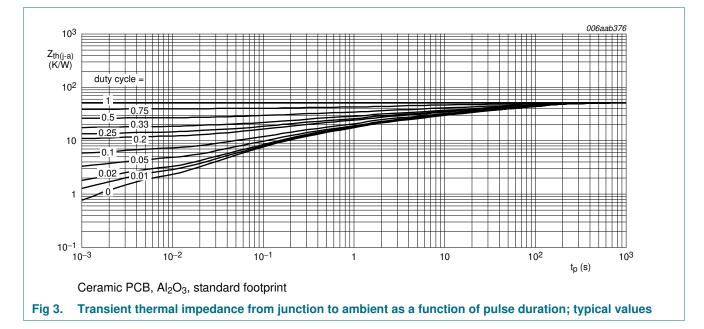
PMEG4010ETP

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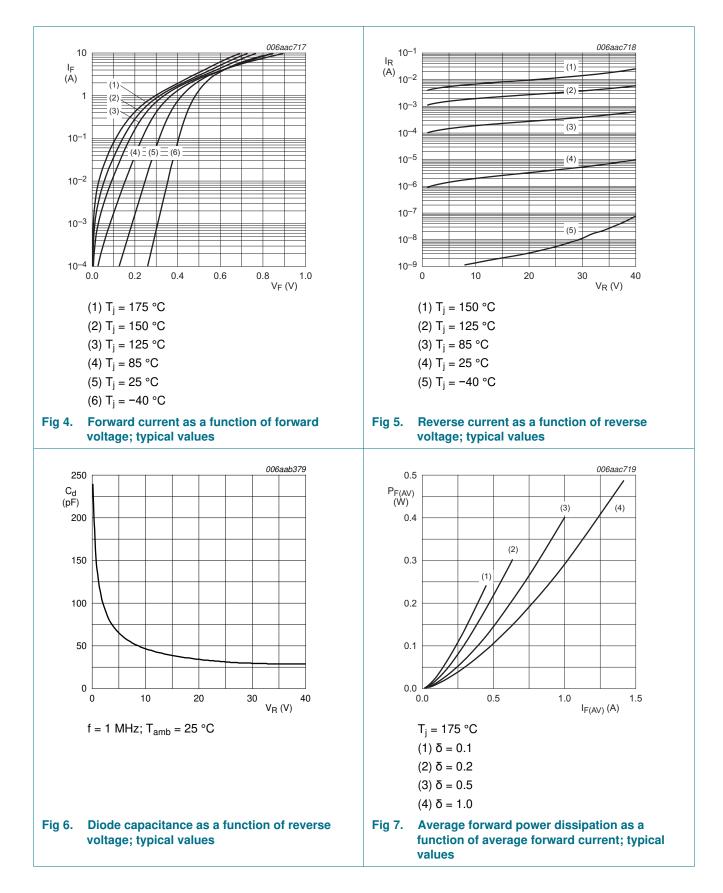
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7. Characteristics

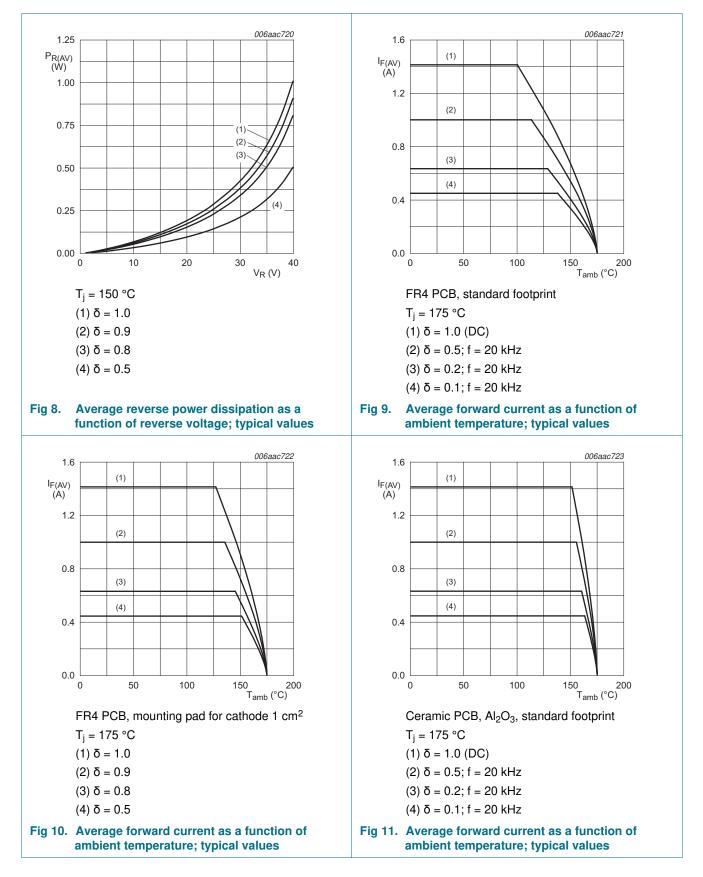
| Table 7. | Characteristics | | | | | |
|--------------------------------|-------------------|--|-----|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| V _F | forward voltage | I _F = 0.1 A; T _j = 25 °C | - | 310 | 360 | mV |
| | | I _F = 1 A; T _j = 25 °C | - | 430 | 490 | mV |
| | | I _F = 1 A; T _j = 125 °C | - | 330 | 380 | mV |
| I _R reverse current | reverse current | V _R = 10 V; T _j = 25 °C | - | 3 | 13 | μA |
| | | $V_{R} = 40 \text{ V}; \text{ T}_{j} = 25 \text{ °C}$ | - | 10 | 50 | μA |
| | | V _R = 10 V; T _j = 125 °C | - | 2 | - | mA |
| | | $V_{R} = 40 \text{ V}; \text{ T}_{j} = 125 \text{ °C}$ | - | 6 | - | mA |
| C _d | diode capacitance | V _R = 1 V; f = 1 MHz; T _j = 25 °C | - | 130 | - | pF |
| | | $V_{R} = 10 \text{ V; } f = 1 \text{ MHz; } T_{j} = 25 \text{ °C}$ | - | 50 | - | pF |

40 V, 1 A low VF MEGA Schottky barrier rectifier



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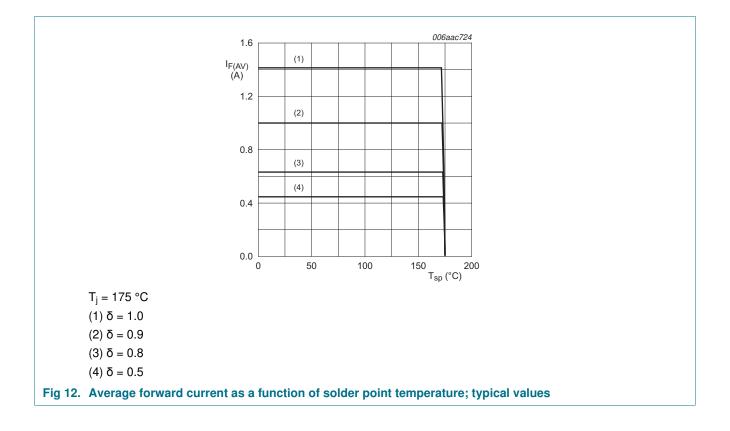


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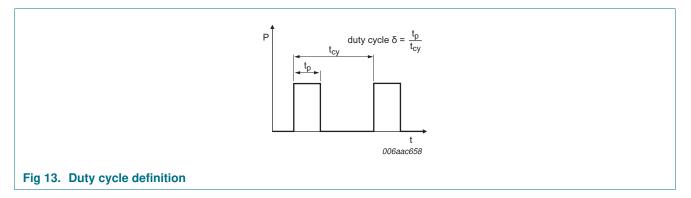
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40 V, 1 A low VF MEGA Schottky barrier rectifier

8. Test information

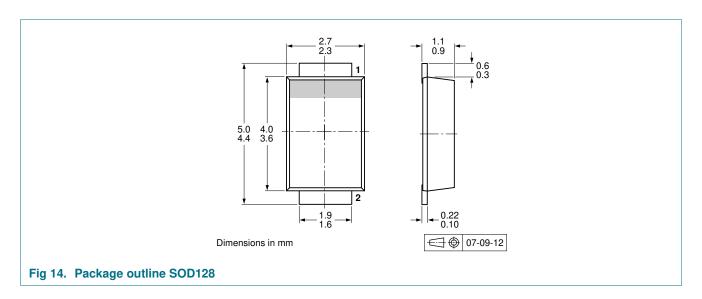


The current ratings for the typical waveforms as shown in figures 9, 10, 11 and 12 are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

9. Package outline



40 V, 1 A low VF MEGA Schottky barrier rectifier

10. Packing information

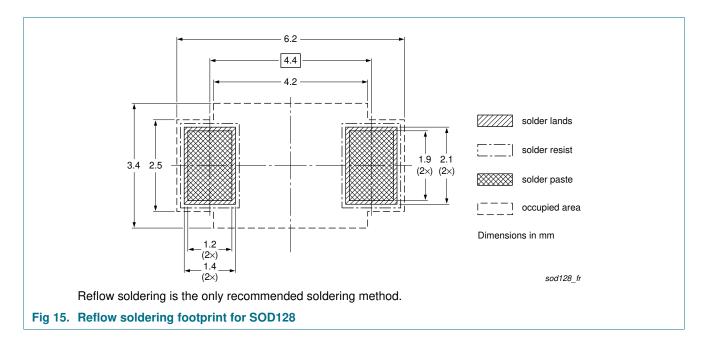
Table 8. Ordering information

The indicated -xxx are the last three digits of the 12NC ordering code. [1]

| Type number | Package | Description | Packing quantity |
|-------------|---------|---------------------------------|------------------|
| | | | 3000 |
| PMEG4010ETP | SOD128 | 4 mm pitch, 12 mm tape and reel | -115 |

[1] For further information and the availability of packing methods, see 14 "Contact information".

11. Soldering



40 V, 1 A low VF MEGA Schottky barrier rectifier

12. Revision history

| Table 9. Revision h | . Revision history | | | | | |
|---------------------|--------------------|--------------------|---------------|------------|--|--|
| Document ID | Release date | Data sheet status | Change notice | Supersedes | | |
| PMEG4010ETP v.1 | 20111005 | Product data sheet | - | - | | |

40 V, 1 A low VF MEGA Schottky barrier rectifier

13. Legal information

13.1 Data sheet status

| Document status [1] [2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
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[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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