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Kind regards,

Team Nexperia

1 A very low V_F MEGA Schottky barrier rectifiers Rev. 02 — 27 March 2007 Pro

Product data sheet

Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifiers with an integrated guard ring for stress protection, encapsulated in small and flat lead Surface-Mounted Device (SMD) plastic packages.

Table 1. **Product overview**

Type number	Package		Configuration
	NXP	JEITA	
PMEG6010CEH	SOD123F	-	single
PMEG6010CEJ	SOD323F	SC-90	single

1.2 Features

Forward current: I_F ≤ 1 A

Reverse voltage: V_R ≤ 60 V

Very low forward voltage

Small and flat lead SMD plastic packages

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

1.4 Quick reference data

Table 2. **Quick reference data**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I_{F}	forward current	$T_{sp} \le 55 ^{\circ}C$	-	-	1	Α
V_R	reverse voltage		-	-	60	V
V _F	forward voltage	$I_F = 1 A$	<u>[1]</u> -	570	660	mV

^[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.



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2. Pinning information

Table 3. Pinning

	3	
Pin	Description	Simplified outline Symbol
1	cathode	[1]
2	anode	1

^[1] The marking bar indicates the cathode.

3. Ordering information

Table 4. Ordering information

Type number	Package	Package					
	Name	Description	Version				
PMEG6010CEH	-	plastic surface-mounted package; 2 leads	SOD123F				
PMEG6010CEJ	SC-90	plastic surface-mounted package; 2 leads	SOD323F				

4. Marking

Table 5. Marking codes

Type number	Marking code
PMEG6010CEH	CA
PMEG6010CEJ	EQ

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5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

	Conditions	Min	Max	Unit
reverse voltage		-	60	V
forward current	T _{sp} ≤ 55 °C	-	1	Α
repetitive peak forward current	$\begin{array}{l} t_p \leq 1 \text{ ms;} \\ \delta \leq 0.25 \end{array}$	-	7	Α
non-repetitive peak forward current	square wave; $t_p = 8 \text{ ms}$			
PMEG6010CEH		-	9	Α
PMEG6010CEJ		-	10	Α
total power dissipation	T _{amb} ≤ 25 °C			
PMEG6010CEH		<u>[1]</u> -	375	mW
		[2] _	830	mW
PMEG6010CEJ		<u>[1]</u> -	350	mW
		[2] _	830	mW
junction temperature		-	150	°C
ambient temperature		-65	+150	°C
storage temperature		-65	+150	°C
	forward current repetitive peak forward current non-repetitive peak forward current PMEG6010CEH PMEG6010CEJ total power dissipation PMEG6010CEH PMEG6010CEH	$\begin{array}{ll} \text{forward current} & T_{sp} \leq 55 \text{ °C} \\ \text{repetitive peak forward current} & t_{p} \leq 1 \text{ ms}; \\ \delta \leq 0.25 \\ \text{non-repetitive peak forward} & \text{square wave;} \\ \text{current} & t_{p} = 8 \text{ ms} \\ \\ \text{PMEG6010CEH} \\ \text{PMEG6010CEJ} \\ \text{total power dissipation} & T_{amb} \leq 25 \text{ °C} \\ \\ \text{PMEG6010CEH} \\ \\ \text{PMEG6010CEJ} \\ \\ \text{junction temperature} \\ \\ \text{ambient temperature} \\ \end{array}$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{llllllllllllllllllllllllllllllllllll$

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

6. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	<u>[1]</u>			
	PMEG6010CEH		[2] _	-	330	K/W
			[3] _	-	150	K/W
	PMEG6010CEJ		[2] _	-	350	K/W
			[3] _	-	150	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		<u>[4]</u>			
	PMEG6010CEH		-	-	60	K/W
	PMEG6010CEJ		-	-	55	K/W

^[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

^[4] Soldering point of cathode tab.

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7. Characteristics

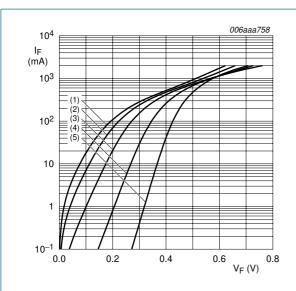
Table 8. Characteristics

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{F}	forward voltage		<u>[1]</u>			
		I _F = 1 mA	-	210	250	mV
		I _F = 10 mA	-	270	310	mV
		I _F = 100 mA	-	350	400	mV
	I _F = 500 mA	-	460	530	mV	
		I _F = 700 mA	-	510	580	mV
		I _F = 1 A	-	570	660	mV
I _R	reverse current	$V_R = 5 V$	-	0.8	-	μΑ
		V _R = 10 V	-	1.1	-	μΑ
		$V_R = 60 \text{ V}$	-	11	50	μΑ
C _d	diode capacitance	$V_R = 1 V; f = 1 MHz$	-	60	68	pF

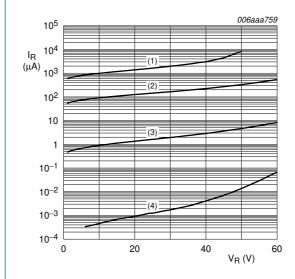
^[1] Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

1 A very low V_F MEGA Schottky barrier rectifiers



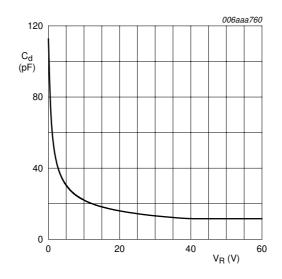
- (1) $T_{amb} = 150 \, ^{\circ}C$
- (2) $T_{amb} = 125 \, ^{\circ}C$
- (3) $T_{amb} = 85 \, ^{\circ}C$
- (4) $T_{amb} = 25 \, ^{\circ}C$
- (5) $T_{amb} = -40 \, ^{\circ}C$

Forward current as a function of forward Fig 1. voltage; typical values



- (1) $T_{amb} = 125 \, ^{\circ}C$
- (2) $T_{amb} = 85 \, ^{\circ}C$
- (3) $T_{amb} = 25 \, ^{\circ}C$
- (4) $T_{amb} = -40 \, ^{\circ}C$

Fig 2. Reverse current as a function of reverse voltage; typical values

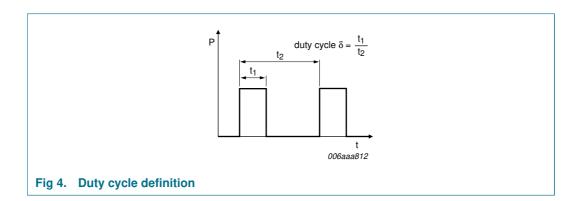


 $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$

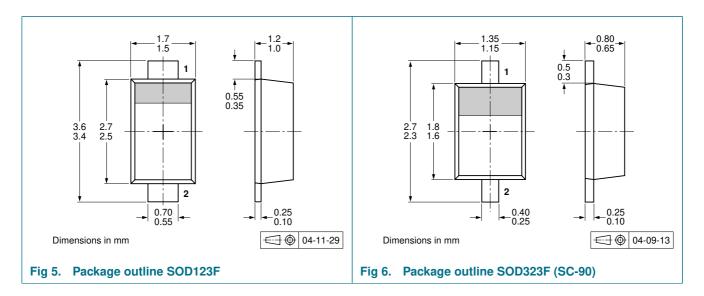
Fig 3. Diode capacitance as a function of reverse voltage; typical values

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8. Test information



9. Package outline



10. Packing information

Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

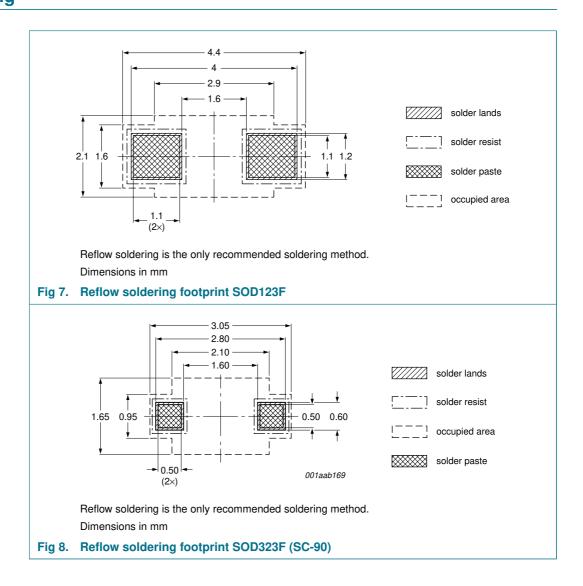
Type number	Package	Description	Packing qu	Packing quantity	
			3000	10000	
PMEG6010CEH	SOD123F	4 mm pitch, 8 mm tape and reel	-115	-135	
PMEG6010CEJ	SOD323F				

[1] For further information and the availability of packing methods, see Section 14.

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11. Soldering



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12. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG6010CEH_PMEG6010CEJ_ 2	20070327	Product data sheet	-	PMEG6010CEJ_1		
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. 					
	 Legal texts have been adapted to the new company name where appropriate appropriate in the company name where appropriate app					
	 Type number PMEG6010CEH added 					
	 Section 1.1 " 	General description": amen	ided			
	• Table 1 "Prod	duct overview": added				
	• Table 7 "The	rmal characteristics": Table	note 1 amended			
	 Section 8 "Te 	est information": added				
PMEG6010CEJ_1	20060414	Product data sheet	-	-		

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13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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PMEG6010CEH; PMEG6010CEJ

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