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# ne<mark>x</mark>peria

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Kind regards,

Team Nexperia



500 mA NPN general-purpose transistors Rev. 3 — 22 July 2010

Product data sheet

#### 1. **Product profile**

## 1.1 General description

NPN general-purpose transistors in a SOT323 (SC-70) very small Surface-Mounted Device (SMD) plastic package.

#### Table 1. **Product overview**

Type number	Package I		PNP complement
	NXP	JEITA	-
PMSTA05	SOT323	SC-70	PMSTA55
PMSTA06			PMSTA56

## 1.2 Features and benefits

- High current (max. 500 mA)
- Collector-emitter voltage:
  - ◆ 60 V (PMSTA05)
  - 80 V (PMSTA06)
- AEC-Q101 qualified
- Very small SMD plastic package

## **1.3 Applications**

Primarily intended for telephony and professional communication equipment

## 1.4 Quick reference data

Table 2.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base				
	PMSTA05		-	-	60	V
	PMSTA06		-	-	80	V
I <sub>C</sub>	collector current		-	-	500	mA
h <sub>FE</sub> DC current gain		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 10 mA	50	-	-	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1] 50	-	-	

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .



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# 2. Pinning information

Table 3.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	base		
2	emitter		3
3	collector	1 2	
			sym021

# 3. Ordering information

Table 4.         Ordering information						
Type number	Package					
	Name	Description	Version			
PMSTA05	SC-70	plastic surface-mounted package; 3 leads	SOT323			
PMSTA06						

# 4. Marking

Type numberMarking code <sup>[1]</sup> PMSTA05*1H	
PMSTA05 *1H	
PMSTA06 *1G	

[1] \* = -: made in Hong Kong

\* = p: made in Hong Kong

\* = t: made in Malaysia

\* = W: made in China

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# 5. Limiting values

Symbol	Parameter	Conditions	Min	Мах	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter			
	PMSTA05		-	60	V
	PMSTA06		-	80	V
V <sub>CEO</sub>	collector-emitter voltage	open base			
	PMSTA05		-	60	V
	PMSTA06		-	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	4	V
lc	collector current		-	500	mA
I <sub>CM</sub>	peak collector current		-	500	mA
I <sub>BM</sub>	peak base current		-	500	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	[1] -	200	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB).

# 6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	625	K/W

[1] Device mounted on an FR4 PCB.

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## 7. Characteristics

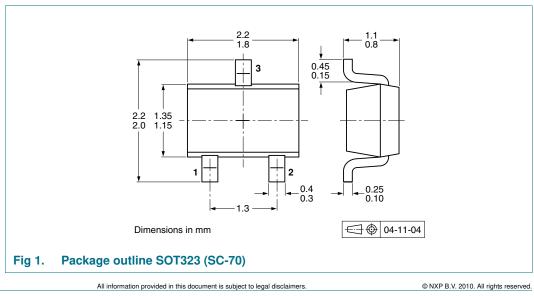
<b>Table 8.</b> T <sub>amb</sub> = 25	<b>Characteristics</b> 5 °C unless otherwise spe	ecified.					
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current						
	PMSTA05	$V_{CB} = 60 \text{ V}; I_E = 0 \text{ A}$		-	-	100	nA
	PMSTA06	$V_{CB} = 80 \text{ V}; I_E = 0 \text{ A}$		-	-	100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 3 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	500	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 2 \text{ V}; \text{ I}_{C} = 10 \text{ mA}$		50	-	-	
		$V_{CE} = 1 \text{ V}; I_{C} = 100 \text{ mA}$	[1]	50	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{\rm C}$ = 100 mA; $I_{\rm B}$ = 10 mA	<u>[1]</u>	-		250	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{\rm C} = 100 \text{ mA};$ $I_{\rm B} = 10 \text{ mA}$	<u>[1]</u>	-	-	900	mV
$V_{BE}$	base-emitter voltage	$I_{C} = 100 \text{ mA}; V_{CE} = 1 \text{ V}$		-	-	1.2	V
f <sub>T</sub>	transition frequency	$V_{CE}$ = 2 V; $I_C$ = 10 mA; f = 100 MHz		100	-	-	MHz
-							

# 8. Test information

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 9. Package outline



PMSTA05 06

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# **10. Packing information**

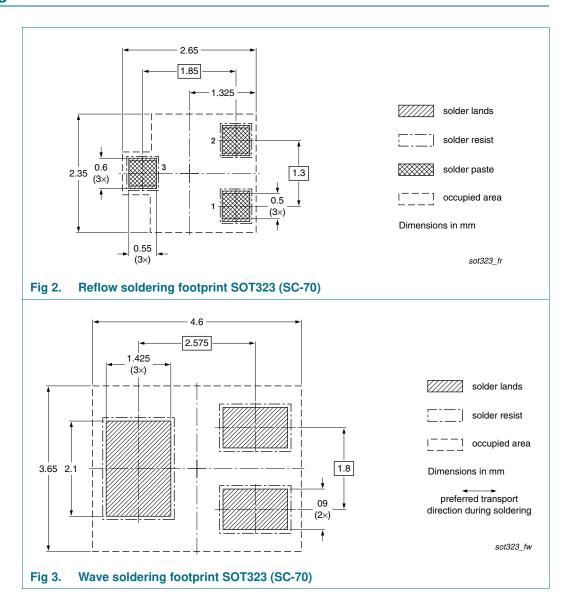
#### Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing	quantity
			3000	10000
PMSTA05	SOT323	4 mm pitch, 8 mm tape and reel	-115	-135
PMSTA06				

[1] For further information and the availability of packing methods, see <u>Section 14</u>.

## 11. Soldering



#### 500 mA NPN general-purpose transistors

# 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
PMSTA05_06 v.3	20100722	Product data sheet	-	PMSTA05_06_2			
Modifications:		of this data sheet has been of NXP Semiconductors.	redesigned to comply w	vith the new identity			
	<ul> <li>Legal texts</li> </ul>	have been adapted to the n	ew company name whe	ere appropriate.			
	Section 1 "	<ul> <li><u>Section 1 "Product profile"</u>: amended</li> </ul>					
	Section 3 "	<u>Section 3 "Ordering information"</u> : added					
	Section 4 "	<u>Section 4 "Marking"</u> : updated					
	Section 8 "	Section 8 "Test information": added					
	• Figure 1: s	uperseded by minimized pac	kage outline drawing				
	Section 10	"Packing information": adde	d				
	Section 11	"Soldering": added					
	Section 13	"Legal information": updated	Ł				
PMSTA05_06_2	19990429	Product specification	-	PMSTA05_06_1			
PMSTA05 06 1	19970616	Product specification	-	-			

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# 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

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PMSTA05\_06

#### 500 mA NPN general-purpose transistors

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Date of release: 22 July 2010 Document identifier: PMSTA05\_06