

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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PORTNOTE SOLUTIONS

SLIC Protection

4 kV ITU-T Solution

Solution Products



TISP9110MDMR-S



Objective

The SLIC (Subscriber Line Interface Circuit) provides all the BORSCHT functions such as battery, ringing and supervision between the codec and telephone handset.

This PortNote* solution provides protection recommendations for SLIC interfaces against 4 kV 10/700 µs surge and power contact threats.

Solution

1 Thyristor: TISP9110MDMR-S 2 CPTC Resettable Fuses: CMF-RL55-10-0

Compliance

ITU-T Basic for K.20, K.21, K.45

• Increased surge withstand to 4 kV 10/700 μs

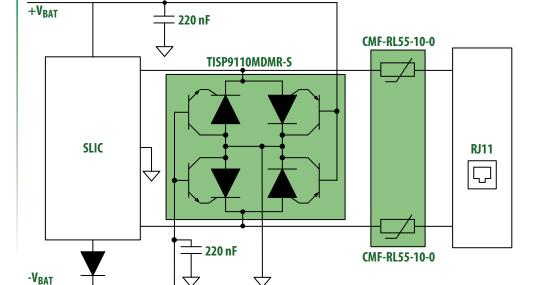
Alternate Recommendations

Other PortNote® Solutions:

- Negative Battery Voltage SLIC Protection ITU-T Solution
- Negative Battery Voltage SLIC Protection GR-1089-CORE Intra-building Solution
- SLIC Protection GR-1089-CORE Intra-building Solution

Benefit

This solution provides a 4 kV 10/700 μs level of protection in dual voltage ringing SLICs.



The schematic above illustrates the application protection and does not constitute the complete circuit design. Customers should verify actual device performance in their specific applications.

Design Kit



PN-DESIGNKIT-47

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For more information, go to:

Bourns* PortNote* solutions provide protection recommendations for typical port threats.

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