

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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Preferred Device

General Purpose Transistor

PNP Silicon



ON Semiconductor™

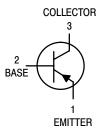
http://onsemi.com

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	-60	Vdc
Collector-Base Voltage	V _{CBO}	-60	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	I _C	-600	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	83.3	°C/W





TO-92 CASE 29 STYLE 1

MARKING DIAGRAM



PN2907A = Device Code Y = Year WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
PN2907A	TO-92	5000 Units/Box
PN2907ARLRA	TO-92	2000/Tape & Reel

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Chara	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS				<u> </u>	
Collector–Emitter Breakdown Voltage (N $(I_C = -10 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	-60	_	Vdc	
Collector–Base Breakdown Voltage (I _C = –10 μAdc, I _E = 0)		V _{(BR)CBO}	-60	-	Vdc
Emitter–Base Breakdown Voltage $(I_E = -10 \mu Adc, I_C = 0)$		V _{(BR)EBO}	-5.0	_	Vdc
Collector Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)		I _{CEX}	_	-50	nAdc
	I _{CBO}	_ _	-0.01 -10	μAdc	
Base Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)	l _Β	-	-50	nAdc	
ON CHARACTERISTICS					
$ \begin{array}{l} \text{DC Current Gain} \\ (I_C = -0.1 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (I_C = -1.0 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (I_C = -10 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (I_C = -150 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ mAdc, V}_{CE} = -10 \text{ Vdc}) \\ (Not (I_C = -500 \text{ MAdc, V}_{CE} = -10 \text{ Vdc}) \\ (No$	e 1.) e 1.)	h _{FE}	75 100 100 100 50	- - - 300 -	_
Collector–Emitter Saturation Voltage (Note 1.) $ \begin{pmatrix} I_C = -150 \text{ mAdc, } I_B = -15 \text{ mAdc} \end{pmatrix} $ $ \begin{pmatrix} I_C = -500 \text{ mAdc, } I_B = -50 \text{ mAdc} \end{pmatrix} $		V _{CE(sat)}	_ _	-0.4 -1.6	Vdc
Base–Emitter Saturation Voltage (Note 1.) $ \begin{pmatrix} I_C = -150 \text{ mAdc}, \ I_B = -15 \text{ mAdc} \end{pmatrix} $ $ \begin{pmatrix} I_C = -500 \text{ mAdc}, \ I_B = -50 \text{ mAdc} \end{pmatrix} $		V _{BE(sat)}		-1.3 -2.6	Vdc
SMALL-SIGNAL CHARACTERISTI	cs				
Current–Gain – Bandwidth Product (Notes 1. and 2.), (I _C = –50 mAdc, V _{CE} = –20 Vdc, f = 100 MHz)		f _T	200	-	MHz
Output Capacitance $(V_{CB} = -10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$		C _{obo}	_	8.0	pF
Input Capacitance $(V_{EB} = -2.0 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$		C _{ibo}	_	30	pF
SWITCHING CHARACTERISTICS		•		•	-
Turn-On Time	$(V_{CC} = -30 \text{ Vdc}, I_{C} = -150 \text{ mAdc},$	t _{on}	-	45	ns
Delay Time	$I_{B1} = -15 \text{ mAdc}$) (Figures 1 and 5)	t _d	-	10	ns
Rise Time		t _r	-	40	ns
Turn-Off Time	$(V_{CC} = -6.0 \text{ Vdc}, I_C = -150 \text{ mAdc},$	t _{off}	_	100	ns
Storage Time	$I_{B1} = I_{B2} = 15 \text{ mAdc}$) (Figure 2)	ts	-	80	ns
Fall Time		t _f	-	30	ns

^{1.} Pulse Test: Pulse Width $\leq 300~\mu s$, Duty Cycle $\leq 2\%$. 2. f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.

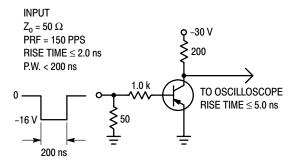


Figure 1. Delay and Rise Time Test Circuit

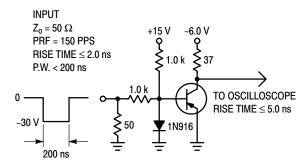


Figure 2. Storage and Fall Time Test Circuit

TYPICAL CHARACTERISTICS

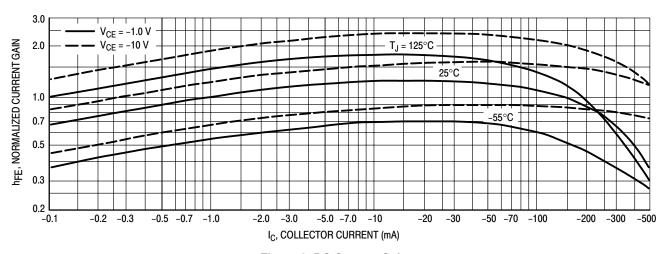


Figure 3. DC Current Gain

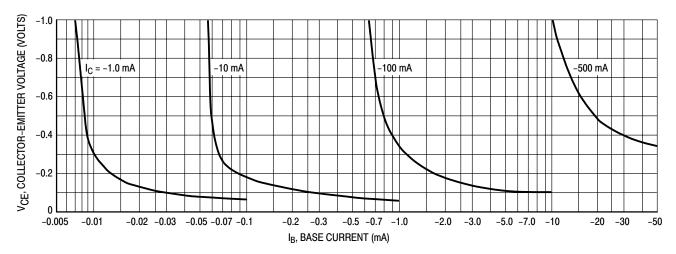


Figure 4. Collector Saturation Region

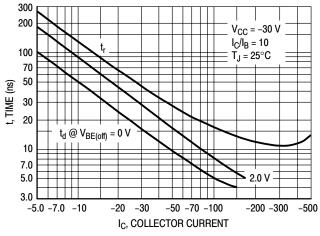


Figure 5. Turn-On Time

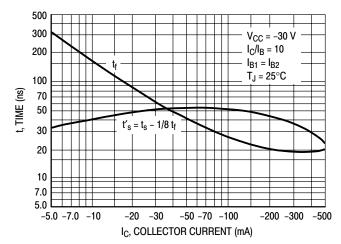
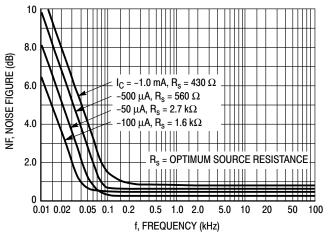


Figure 6. Turn-Off Time

TYPICAL SMALL-SIGNAL CHARACTERISTICS

NOISE FIGURE

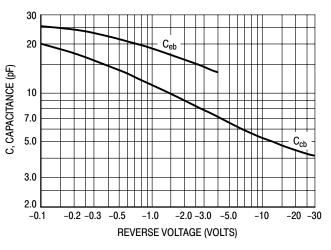
 $V_{CE} = 10 \text{ Vdc}, T_A = 25^{\circ}\text{C}$



8.0 NF, NOISE FIGURE (dB) 6.0 $I_C = -50 \mu A$ 100 μA -500 μA 4.0 -1.0 mA 2.0 50 100 200 1.0 k 2.0 k 5.0 k 10 k 20 k 50 k R_s, SOURCE RESISTANCE (OHMS)

Figure 7. Frequency Effects

Figure 8. Source Resistance Effects



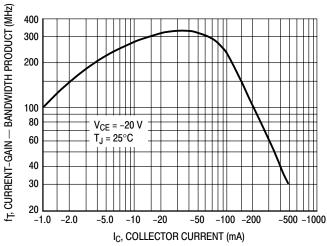
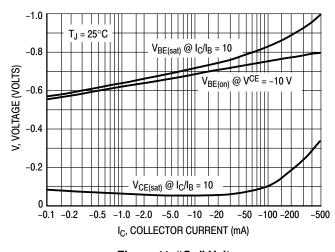


Figure 9. Capacitances

Figure 10. Current-Gain — Bandwidth Product



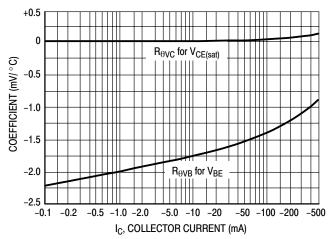


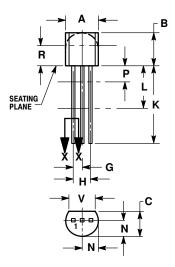
Figure 11. "On" Voltage

Figure 12. Temperature Coefficients

PACKAGE DIMENSIONS

TO-92 TO-226AA CASE 29-11

ISSUE AL





- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
V	0 135		3 43	

Notes

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