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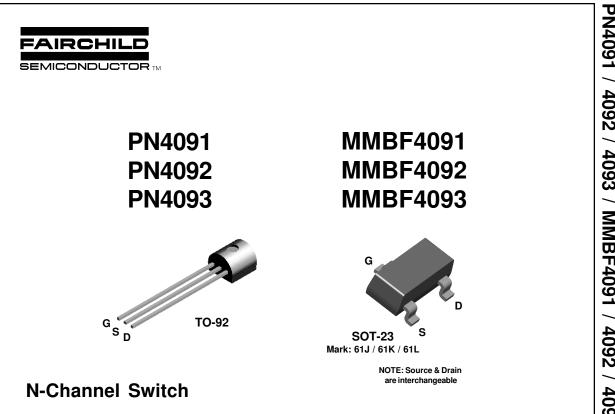
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This device is designed for low level analog switching, sample and hold circuits and chopper stabalized amplifiers. Sourced from Process 51. See J111 for characteristics.

Absolute Maximum Ratings*	TA = 25°C unless otherwise noted
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Symbol	Parameter	Value	Units
V _{DG}	Drain-Gate Voltage	40	V
V_{GS}	Gate-Source Voltage	- 40	V
I _{GF}	Forward Gate Current	50	mA
T _J ,T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES: 1) These ratings are based on a maximum junction temperature of 150 degrees C. 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	nbol Characteristic Max		lax	Units
		PN4091-4093	*MMBF4091-4093	
PD	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

*Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

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N-Channel Switch

(continued)

Electrical Characteristics	
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Electrical Characteristics TA = 25°C unless otherwise noted						
Symbol	Parameter		Test Conditions	Min	Max	Units

OFF CHARACTERISTICS

V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = 1.0 \ \mu A, \ V_{DS} = 0$		- 40		V
V _{GS(off)}	Gate-Source Cutoff Voltage	V _{DS} = 20 V, I _D = 1.0 nA	4091	- 5.0	- 10	V
. ,			4092	- 2.0	- 7.0	V
			4093	- 1.0	- 5.0	V
I _{DGO}	Drain-Gate Leakage Current	$V_{DG} = 20 \text{ V}, \text{ I}_{S} = 0$			- 200	pА
		$V_{DG} = 20 V, I_S = 0, T_A = 13$	50°C		- 400	nA
I _{D(off)}	Drain Cutoff Leakage Current	V _{DS} = 20 V, V _{GS} = - 12 V	4091		200	pА
	-	$V_{DS} = 20 V, V_{GS} = -8.0 V$	4092		200	pА
		$V_{DS} = 20 V, V_{GS} = -6.0 V$	4093		200	pА
		$V_{DS} = 20 V$, $V_{GS} = -12 V$,				
		$T_A = 150^{\circ}C$	4091		400	nA
		$V_{DS} = 20 V, V_{GS} = -8.0 V,$				
		T _A = 150°C	4092		400	nA
		$V_{DS} = 20 V, V_{GS} = -6.0 V,$			400	
		T _A = 150°C	4093		400	nA

ON CHARACTERISTICS

I _{DSS}	Zero-Gate Voltage Drain Current*	$V_{DS} = 20 V, V_{GS} = 0$	4091	30		mA
	-		4092	15		mA
			4093	8.0		mA
V _{DS(on)}	Drain-Source On Voltage	$I_D = 6.6 \text{ mA}, V_{GS} = 0$	4091		0.2	V
==(-)	-	$I_{D} = 4.0 \text{ mA}, V_{GS} = 0$	4092		0.2	V
		$I_D = 2.5 \text{ mA}, V_{GS} = 0$	4093		0.2	V
r _{DS(on)}	Drain-Source On Resistance	$I_D = 1.0 \text{ mA}, V_{GS} = 0$	4091		30	Ω
-(-)			4092		50	Ω
			4093		80	Ω

SMALL-SIGNAL CHARACTERISTICS

r _{ds(on)}	Drain-Source On Resistance	$V_{DS} = V_{GS} = 0$, f= 1.0 kHz 4091 4092 4093	30 50 80	Ω Ω Ω
Ciss	Input Capacitance	$V_{DS} = 20, V_{GS} = 0, f = 1.0 \text{ MHz}$	16	pF
Crss	Reverse Transfer Capacitance	V _{GS} = - 20 V, f = 1.0 MHz	5.0	pF

SWITCHING CHARACTERISTICS

t _{on}	Turn-On Time	$I_{D(on)} = 12 \text{ mA}$	4091	25	ns
		$I_{D(on)} = 6.0 \text{ mA}$	4092	35	ns
		$I_{D(on)} = 3.0 \text{ mA}$	4093	60	ns
toff	Turn-Off Time	V _{GS(off)} = 12 V	4091	40	ns
		$V_{GS(off)} = 6.0 V$	4092	60	ns
		$V_{GS(off)} = 3.0 V$	4093	80	ns

*Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 1.0%

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