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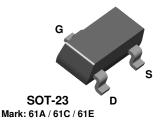




PN4117 **PN4118 PN4119**

MMBF4117 MMBF4118 MMBF4119





NOTE: Source & Drain are interchangeable

N-Channel Switch

This device is designed for low current DC and audio applications. These devices provide excellent performance as input stages for sub-picoamp instrumentation or any high impedance signal sources. Sourced from Process 53.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V_{DG}	Drain-Gate Voltage	40	V	
V _{GS}	Gate-Source Voltage	- 40	V	
I _{GF}	Forward Gate Current	50	mA	
T _J ,T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C	

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units
		PN4117-4119	*MMBF4117-4119	
P_D	Total Device Dissipation Derate above 25°C	350 2.8	225 1.8	mW mW/°C
R _{θJC}	Thermal Resistance, Junction to Case	125		°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient	357	556	°C/W

^{*}Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

N-Channel Switch

- 1.8 - 3.0

- 6.0

(continued)

V V

Electrical Characteristics TA = 25°C unless otherwise noted					
Symbol	I Parameter Test Conditions			Max	Units
OFF CHAI	RACTERISTICS				
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = -1.0 \mu A, V_{DS} = 0$	- 40		V
I _{GSS}	Gate Reverse Current	V _{GS} = - 20 V, V _{DS} = 0		- 10	pА
		$V_{GS} = -20 \text{ V}, V_{DS} = 0, T_A = 150^{\circ}\text{C}$		- 25	nA

 $V_{DS} = -10 \text{ V}, I_{D} = 1.0 \text{ nA}$

4117

4118

4119

- 0.6 - 1.0

- 2.0

ON CHARACTERISTICS

V_{GS(off)}

I _{DSS}	Zero-Gate Voltage Drain Current*	$V_{DS} = 10 \text{ V}, V_{GS} = 0$	4117	30	90	μΑ
			4118	80	240	μA
			4119	200	600	μA

SMALL-SIGNAL CHARACTERISTICS

Gate-Source Cutoff Voltage

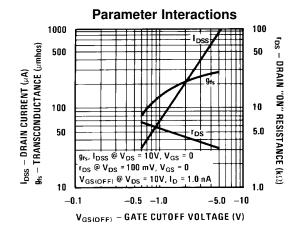
g fs	Common-Source Forward Transconductance	V _{DS} = 10 V V _{GS} = 0, f= 1.0 kHz 4117 4118 4119	70 80 100	210 250 330	μmhos μmhos μmhos
goss	Common-Source Output Conductance	V _{DS} = 10 V V _{GS} = 0, f= 1.0 kHz 4117 4118 4119		3.0 5.0 10	μmhos μmhos μmhos
R _{e(yfs)}	Common-Source Forwad Transconductance	V _{DS} = 10 V, V _{GS} = 0, f= 30 MHz 4117 4118 4119	60 70 90		μmhos μmhos μmhos
Ciss	Input Capacitance	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1.0 \text{ kHz}$		3.0	pF
C _{rss}	Reverse Transfer Capacitance	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1.0 \text{ MHz},$		1.5	pF

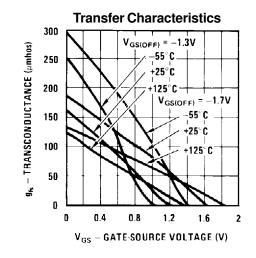
^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 1.0%

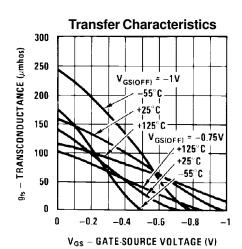
N-Channel Switch

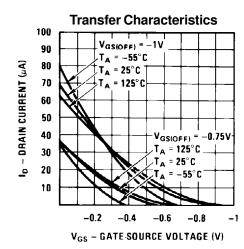
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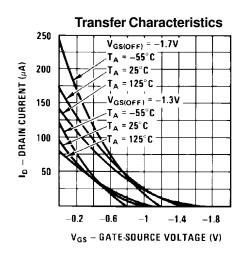
Typical Characteristics

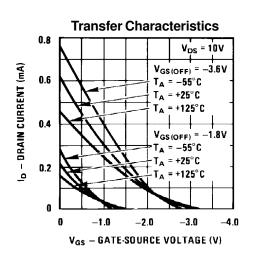








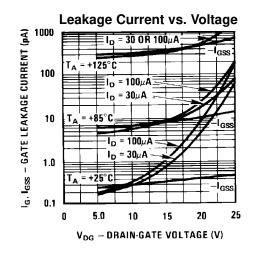


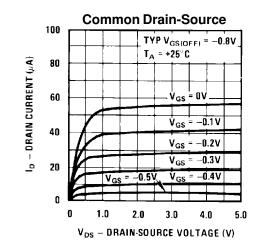


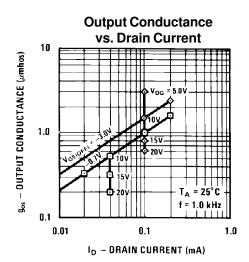
N-Channel Switch

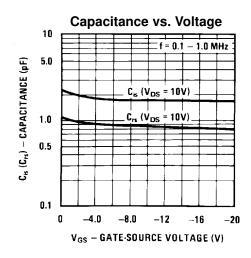
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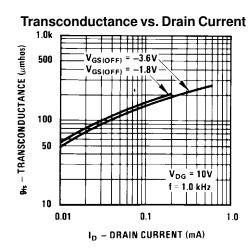
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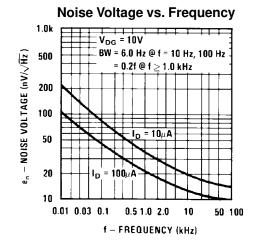












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Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.		
No Identification Needed Full Production		This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.		
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