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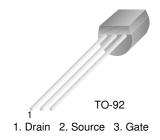






# PN4861 N-Channel Switch

- · This device is designed for electronic switching applications such as low ON resistance analog switching.
- · Sourced from process 51.



## **Absolute Maximum Ratings\*** T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	30	V
V <sub>GS</sub> Gate-Source Voltage		-30	V
I <sub>GF</sub>	Forward Gate Current	50	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

<sup>\*</sup> This ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES

These rating are based on a maximum junction temperature of 150 degrees C.

## Thermal Characteristics $T_a=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Max.	Units
P <sub>D</sub>	D Total Device Dissipation		mW
	Derate above 25°C	5.0	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

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<sup>2)</sup> These are steady limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

<sup>\*</sup> Device mounted on FR-4 PCB 1.5" X 1.6" X 0.06"

# $\textbf{Electrical Characteristics*} \ \, \textbf{T}_{a} = 25^{\circ} \textbf{C} \ \, \textbf{unless otherwise noted}$

	Symbol	Parameter	Test Condition	Min.	Max.	Units
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## **Off Characteristics**

١	V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_{G} = 1.0 \mu A, V_{DS} = 0 V$	-30		V
I	l <sub>GSS</sub>	Gate Reverse Current	V <sub>GS</sub> = 15 V, V <sub>DS</sub> = 0, T = 25°C T = 100°C		-0.25 -500	nA
١	V <sub>GS(OFF)</sub>	Gate-Source Cut-off Voltage	$V_{DS} = 15 \text{ V}, I_D = 0.5 \text{ nA}$	-0.8	-4.0	V

## On Characteristics

I <sub>DSS</sub>	Zero-Gate Voltage Drain Current *	$V_{DS} = 15V, V_{GS} = 0$	8	80	mA
VDS(ON)	Drain-Source On Voltage	$I_D = 5 \text{ mA}$		0.5	V
RDS(ON)	Drain-Source On Voltage	$V_{DS} = 0 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{kHz}$		60	Ω

## **Small Signal Characteristics**

Ciss	Input Capacitance	$V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz$	18	pF
Crss	Reverse Transfer Capacitance	$V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz$	8	pF

<sup>\*</sup> Pulse Test: Pulse Width  $\leq 300 \mu s$ , Duty Cycle = 2%





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Datasheet Identification	Product Status	Definition
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