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POW-R-PAK<sup>TM</sup> 150A / 600V H-Bridge IGBT Assembly

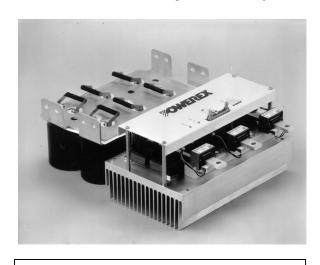
### **Description:**

The Powerex POW-R-PAK<sup>TM</sup> is a configurable IGBT based power assembly that may be used as a converter, chopper, half or full bridge, or three phase inverter for motor control, power supply, UPS or other power conversion applications.

The power assembly is mounted on a forced air-cooled heatsink and features state-of-the-art Powerex F-series trench gate IGBTs with low conduction and switching losses for high efficiency operation. The POW-R-PAK<sup>TM</sup> includes a low inductance laminated bus structure, optically isolated gate drive interfaces, isolated gate drive power supplies, and a DC-link capacitor bank. The control board provides a simple user interface along with built-in protection features including overvoltage, undervoltage lockout, overcurrent, overtemperature, and short circuit detection.

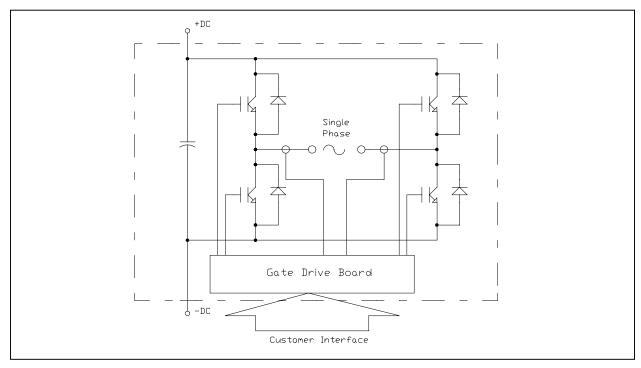
Depending on application characteristics the POW-R-PAK<sup>™</sup> is suitable for operation with DC bus voltages up to 400VDC and switching frequencies above 20kHz.

#### **Schematic**



### Features:

- High performance IGBT inverter bridge
- Integrated gate drive with fault monitoring & protection
- System status / troubleshooting LEDs to verify or monitor proper operation
- Isolated gate drive power supplies
- Low inductance laminated bus
- Output current measurement & feedback
- Superior short circuit detection & shoot through prevention



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POW-R-PAK<sup>TM</sup> 150A / 600V H-Bridge IGBT Assembly

# Absolute Maximum Ratings, T<sub>j</sub> = 25°C unless otherwise specified

General	Symbol		Units
IGBT Junction Temperature	T <sub>j</sub>	-40 to +150	°C
Storage Temperature	T <sub>stg</sub>	-40 to +125	°C
Operating Temperature	T <sub>op</sub>	-25 to +85	°C
Voltage Applied to DC terminals	V <sub>CC</sub>	400	Volts
Isolation Voltage, AC 1 minute, 60Hz sinusoidal	V <sub>iso</sub>	2500	Volts
IGBT Inverter			
Collector Current (T <sub>C</sub> = 25°C)	Ic	150	Amperes
Peak Collector Current (T <sub>j</sub> < 150°C)	I <sub>CM</sub>	300	Amperes
Emitter Current	I <sub>E</sub>	150	Amperes
Peak Emitter Current	I <sub>EM</sub>	300	Amperes
Maximum Collector Dissipation (T <sub>j</sub> < 150°C)	Pc	520	Watts
Gate Drive Board			
Unregulated +24V Power Supply		30	Volts
Regulated +15V Power Supply		18	Volts
PWM Signal Input Voltage		20	Volts
Fault Output Supply Voltage		30	Volts
Fault Output Current		50	mA

# IGBT Inverter Electrical Characteristics, $T_j = 25^{\circ}C$ unless otherwise specified

Characteristics	Symbol	<b>Test Conditions</b>	Min	Тур	Max	Units
Collector Cutoff Current	I <sub>CES</sub>	$V_{CE} = V_{CES}$ , $V_{GE} = 0V$	-	-	1	mA
Collector – Emitter Saturation Voltage	V	$I_C = 150A, T_j = 25^{\circ}C$	-	1.6	2.2	Volts
Collector – Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 150A, T_j = 125^{\circ}C$	-	1.6	-	Volts
Emitter – Collector Voltage	$V_{EC}$	I <sub>E</sub> = 150A	-	-	2.6	Volts
ladadina Laad Quitabiaa Tiraa	$t_{d(on)}$	V = 200V	-	-	120	ns
	t <sub>r</sub>		-	-	100	ns
Inductive Load Switching Times	$t_{d(off)}$	$V_{CC} = 300V$ $I_C = 150A$	-	-	350	ns
	t <sub>f</sub>	$V_{GE} = 15V$ $R_G = 4.2\Omega$	-	-	250	ns
Diode Reverse Recovery Time	t <sub>rr</sub>	1\G - 4.2\square	-	-	150	ns
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	2.8	-	μC
DC Link Capacitance				18000		μF

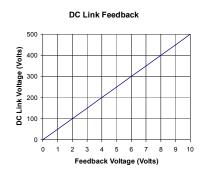
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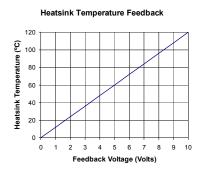


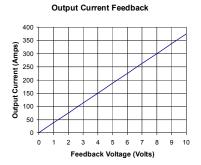
POW-R-PAK<sup>TM</sup> 150A / 600V H-Bridge IGBT Assembly

#### **Gate Drive Board Electrical Characteristics**

Characteristics	Min	Тур	Max	Units
Unregulated +24V Power Supply	20	24	30	Volts
Regulated +15V Power Supply	14.4	15	18	Volts
PWM Input On Threshold	12	15		Volts
PWM Input Off Threshold		0	2	Volts
Output Overcurrent Trip		225		Amperes
Overtemperature Trip	96	98	100	°C
Overvoltage Trip		460		Volts
DC Link Voltage Feedback	Se	e Figure B	elow	Volts
Heatsink Temperature Feedback	Se	e Figure B	elow	Volts
Output Current Feedback	Sec	e Figure B	elow	Volts







## **Thermal and Mechanical Characteristics**

Characteristics	Symbol	Test Conditions	Min	Тур	Max	Units
IGBT Thermal Resistance, Junction to Case	R <sub>th(j-c)</sub> Q	Per IGBT ½ module	-	0.16	0.24	°C/W
FWD Thermal Resistance, Junction to Case	$R_{th(j-c)}D$	Per FWD ½ module			0.47	°C/W
Contact Thermal Resistance	R <sub>th(c-f)</sub>		-	0.035	-	°C/W
Heatsink Thermal Resistance	$R_{\text{th(f-a)}}$	1500 LFM airflow		0.040		°C/W
Mounting Torque, AC terminals				75	90	in-lb
Mounting Torque, DC terminals				130	150	in-lb
Mounting Torque, Mounting plate				130	150	in-lb
Weight				21		lb

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POW-R-PAK<sup>™</sup> 150A / 600V H-Bridge IGBT Assembly

## **Gate Drive Board Interface Signal Definitions**

1 Shield Connected to circuit ground 2 PWM A- 3 Phase A Error¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase A Deverument or short circuit 4 PWM A+ 5 O-15 V signal controlling the duty cycle of A- IGBT 5 PWM B- 5 O-15 V signal controlling the duty cycle of A- IGBT 6 Phase B Error¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase B Overcurrent or short circuit 7 PWM B+ 6 O-15 V signal controlling the duty cycle of B- IGBT 6 Phase B Error¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase B Overcurrent or short circuit 7 PWM B+ 7 O-15 V signal controlling the duty cycle of B+ IGBT 8 Not Used 9 Not Used 10 Not Used 11 Overtemp¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = heatsink overtemp 12 Not Connected 13 DC Link Voltage Analog voltage representation of DC link voltage 14 24 VDC input power² 20 – 30 VDC input voltage range 15 24 VDC input power² 20 – 30 VDC input voltage range 16 15 VDC input power² 14.4 – 18 VDC input voltage range 17 15 VDC input power² 14.4 – 18 VDC input voltage range 18 GND Ground reference for 15 and 24 VDC inputs 19 GND Ground reference for 15 and 24 VDC inputs 20 Heatsink Temperature Analog voltage representation of phase A output current 21 GND³ Tied to pins 18 and 19 22 I <sub>loat</sub> Phase A Analog voltage representation of phase A output current 23 GND³ Tied to pins 18 and 19 24 I <sub>loat</sub> Phase B Analog voltage representation of phase B output current 25 Not Used	Pin	Signal Name	Description
Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase A overcurrent or short circuit  PWM A+ 0-15 V signal controlling the duty cycle of A+ IGBT  Phase B Error¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase B overcurrent or short circuit  PWM B+ 0-15 V signal controlling the duty cycle of B+ IGBT  Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase B overcurrent or short circuit  Not Used  Not Used  Open collector output, external pull-up resistor required LOW = Not Used  Not Used  Open collector output, external pull-up resistor required LOW = No Error; Floating = heatsink overtemp  Not Connected  Not Connected  Analog voltage representation of DC link voltage  Analog voltage representation of DC link voltage  Analog voltage range  14 24 VDC input power² 20 – 30 VDC input voltage range  15 24 VDC input power² 14.4 – 18 VDC input voltage range  16 15 VDC input power² 14.4 – 18 VDC input voltage range  17 15 VDC input power² 14.4 – 18 VDC input voltage range  18 GND Ground reference for 15 and 24 VDC inputs  Oround reference for 15 and 24 VDC inputs	1	Shield	Connected to circuit ground
LÓW = No Error; Floating = Phase A overcurrent or short circuit  4 PWM A+ 0.15 V signal controlling the duty cycle of A+ IGBT  5 PWM B- 0.15 V signal controlling the duty cycle of B- IGBT  6 Phase B Error¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase B overcurrent or short circuit  7 PWM B+ 0.15 V signal controlling the duty cycle of B+ IGBT  8 Not Used  9 Not Used  10 Not Used  11 Overtemp¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = heatsink overtemp  12 Not Connected  13 DC Link Voltage Analog voltage representation of DC link voltage  14 24 VDC input power² 20 – 30 VDC input voltage range  15 24 VDC input power² 20 – 30 VDC input voltage range  16 15 VDC input power² 14.4 – 18 VDC input voltage range  17 15 VDC input power² 14.4 – 18 VDC input voltage range  18 GND Ground reference for 15 and 24 VDC inputs  19 GND Ground reference for 15 and 24 VDC inputs  20 Heatsink Temperature Analog voltage representation of phase A output current  21 GND³ Tied to pins 18 and 19  22 I <sub>out</sub> Phase A Analog voltage representation of phase B output current  25 Not Used	2	PWM A-	0-15 V signal controlling the duty cycle of A- IGBT
5 PWM B- 0-15 V signal controlling the duty cycle of B- IGBT Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase B overcurrent or short circuit 7 PWM B+ 0-15 V signal controlling the duty cycle of B+ IGBT  8 Not Used 9 Not Used 10 Not Used 11 Overtemp¹	3	Phase A Error <sup>1</sup>	
Open collector output, external pull-up resistor required LOW = No Error; Floating = Phase B overcurrent or short circuit  7 PWM B+ 0-15 V signal controlling the duty cycle of B+ IGBT  8 Not Used  9 Not Used  10 Not Used  11 Overtemp¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = heatsink overtemp  12 Not Connected  13 DC Link Voltage Analog voltage representation of DC link voltage  14 24 VDC input power² 20 – 30 VDC input voltage range  15 24 VDC input power² 20 – 30 VDC input voltage range  16 15 VDC input power² 14.4 – 18 VDC input voltage range  17 15 VDC input power² 14.4 – 18 VDC input voltage range  18 GND Ground reference for 15 and 24 VDC inputs  19 GND Ground reference for 15 and 24 VDC inputs  20 Heatsink Temperature Analog voltage representation of phase A output current  21 GND³ Tied to pins 18 and 19  22 Iout Phase A Analog voltage representation of phase B output current  25 Not Used	4	PWM A+	0-15 V signal controlling the duty cycle of A+ IGBT
LÓW = No Error; Floating = Phase B overcurrent or short circuit  7 PWM B+ 0-15 V signal controlling the duty cycle of B+ IGBT  8 Not Used  9 Not Used  10 Not Used  11 Overtemp¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = heatsink overtemp  12 Not Connected  13 DC Link Voltage Analog voltage representation of DC link voltage  14 24 VDC input power² 20 – 30 VDC input voltage range  15 24 VDC input power² 20 – 30 VDC input voltage range  16 15 VDC input power² 14.4 – 18 VDC input voltage range  17 15 VDC input power² 14.4 – 18 VDC input voltage range  18 GND Ground reference for 15 and 24 VDC inputs  19 GND Ground reference for 15 and 24 VDC inputs  20 Heatsink Temperature Analog voltage representation of heatsink temperature  21 GND³ Tied to pins 18 and 19  22 Iour Phase A Analog voltage representation of phase A output current  23 GND³ Tied to pins 18 and 19  24 Iour Phase B Analog voltage representation of phase B output current  25 Not Used	5	PWM B-	
8 Not Used 9 Not Used 10 Not Used 11 Overtemp¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = heatsink overtemp 12 Not Connected 13 DC Link Voltage Analog voltage representation of DC link voltage 14 24 VDC input power² 20 – 30 VDC input voltage range 15 24 VDC input power² 20 – 30 VDC input voltage range 16 15 VDC input power² 14.4 – 18 VDC input voltage range 17 15 VDC input power² 14.4 – 18 VDC input voltage range 18 GND Ground reference for 15 and 24 VDC inputs 19 GND Ground reference for 15 and 24 VDC inputs 20 Heatsink Temperature Analog voltage representation of heatsink temperature 21 GND³ Tied to pins 18 and 19 22 I <sub>out</sub> Phase A Analog voltage representation of phase A output current 23 GND³ Tied to pins 18 and 19 24 I <sub>out</sub> Phase B Analog voltage representation of phase B output current 25 Not Used	6	Phase B Error <sup>1</sup>	
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10 Not Used  11 Overtemp¹ Open collector output, external pull-up resistor required LOW = No Error; Floating = heatsink overtemp  12 Not Connected  13 DC Link Voltage Analog voltage representation of DC link voltage  14 24 VDC input power² 20 – 30 VDC input voltage range  15 24 VDC input power² 20 – 30 VDC input voltage range  16 15 VDC input power² 14.4 – 18 VDC input voltage range  17 15 VDC input power² 14.4 – 18 VDC input voltage range  18 GND Ground reference for 15 and 24 VDC inputs  19 GND Ground reference for 15 and 24 VDC inputs  20 Heatsink Temperature Analog voltage representation of heatsink temperature  21 GND³ Tied to pins 18 and 19  22 I <sub>out</sub> Phase A Analog voltage representation of phase A output current  23 GND³ Tied to pins 18 and 19  24 I <sub>out</sub> Phase B Analog voltage representation of phase B output current  25 Not Used	8	Not Used	
Open collector output, external pull-up resistor required LOW = No Error; Floating = heatsink overtemp  12 Not Connected  13 DC Link Voltage Analog voltage representation of DC link voltage  14 24 VDC input power <sup>2</sup> 20 – 30 VDC input voltage range  15 24 VDC input power <sup>2</sup> 20 – 30 VDC input voltage range  16 15 VDC input power <sup>2</sup> 14.4 – 18 VDC input voltage range  17 15 VDC input power <sup>2</sup> 14.4 – 18 VDC input voltage range  18 GND Ground reference for 15 and 24 VDC inputs  19 GND Ground reference for 15 and 24 VDC inputs  20 Heatsink Temperature Analog voltage representation of heatsink temperature  21 GND <sup>3</sup> Tied to pins 18 and 19  22 I <sub>out</sub> Phase A Analog voltage representation of phase A output current  23 GND <sup>3</sup> Tied to pins 18 and 19  24 I <sub>out</sub> Phase B Analog voltage representation of phase B output current  25 Not Used	9	Not Used	
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17 15 VDC input power <sup>2</sup> 14.4 – 18 VDC input voltage range  18 GND Ground reference for 15 and 24 VDC inputs  19 GND Ground reference for 15 and 24 VDC inputs  20 Heatsink Temperature Analog voltage representation of heatsink temperature  21 GND <sup>3</sup> Tied to pins 18 and 19  22 I <sub>out</sub> Phase A Analog voltage representation of phase A output current  23 GND <sup>3</sup> Tied to pins 18 and 19  24 I <sub>out</sub> Phase B Analog voltage representation of phase B output current  25 Not Used	15	24 VDC input power <sup>2</sup>	20 – 30 VDC input voltage range
Ground reference for 15 and 24 VDC inputs  Ground reference for 15 and 24 VDC inputs  Heatsink Temperature Analog voltage representation of heatsink temperature  GND³ Tied to pins 18 and 19  I <sub>out</sub> Phase A Analog voltage representation of phase A output current  GND³ Tied to pins 18 and 19  I <sub>out</sub> Phase B Analog voltage representation of phase B output current  Not Used	16	15 VDC input power <sup>2</sup>	14.4 – 18 VDC input voltage range
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20 Heatsink Temperature Analog voltage representation of heatsink temperature 21 GND³ Tied to pins 18 and 19 22 I <sub>out</sub> Phase A Analog voltage representation of phase A output current 23 GND³ Tied to pins 18 and 19 24 I <sub>out</sub> Phase B Analog voltage representation of phase B output current 25 Not Used	18	GND	Ground reference for 15 and 24 VDC inputs
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22 I <sub>out</sub> Phase A Analog voltage representation of phase A output current 23 GND <sup>3</sup> Tied to pins 18 and 19 24 I <sub>out</sub> Phase B Analog voltage representation of phase B output current 25 Not Used	20	Heatsink Temperature	Analog voltage representation of heatsink temperature
23 GND <sup>3</sup> Tied to pins 18 and 19  24 I <sub>out</sub> Phase B Analog voltage representation of phase B output current  25 Not Used	21	GND <sup>3</sup>	Tied to pins 18 and 19
24 I <sub>out</sub> Phase B Analog voltage representation of phase B output current 25 Not Used	22	I <sub>out</sub> Phase A	Analog voltage representation of phase A output current
25 Not Used	23	GND <sup>3</sup>	Tied to pins 18 and 19
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	24	I <sub>out</sub> Phase B	Analog voltage representation of phase B output current
26 Not Used	25	Not Used	
	26	Not Used	

#### Notes:

- Open collectors can be pulled up to 30 V max and sink 50mA continuous. Do not connect a 15 VDC and 24 VDC source to the unit at the same time, use one or the other.
- GND signals to be used for analog feedback signals, i.e. twisted pair with Iout Phase A.

#### **Gate Drive Board Interface Connector**

Description	Symbol	Туре	Manufacturer
Gate Drive Board Interface Header	J1	0.100" x 0.100" latching header, 26 pin	3M# 3429-6002 or equivalent
Recommended Mating Socket	-	0.100" x 0.100" IDC socket, 26 pin	3M# 3399-7600 or equivalent
Recommended Strain Relief	-	Plastic strain relief	3M# 3448-3026 or equivalent

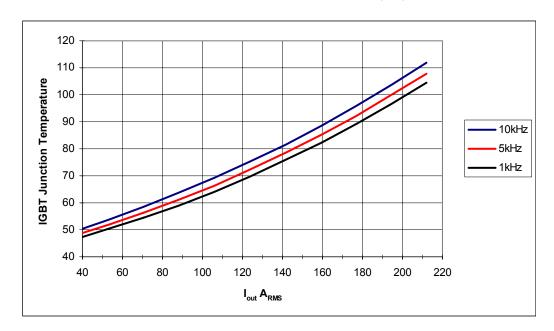
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#### **Performance Curves**

## **Effective Output Current vs. Carrier Frequency (Typical)**



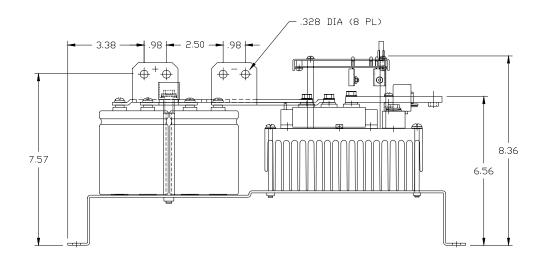
Condition	Symbol	Value	Units		
Ambient Temperature	T <sub>A</sub>	40	°C		
DC Bus Voltage	V <sub>CC</sub>	300	Volts		
Load Power Factor	cos φ	0.8			
IGBT Saturation Voltage	$V_{CE(sat)}$	Typical @ T <sub>J</sub> = 125°C	Volts		
IGBT Switching Loss	Esw	Typical @ T <sub>J</sub> = 125°C	mJ		
Airflow	-	1500	LFM		
Switching Conditions	3 pha	3 phase PWM, 60Hz sinusoidal output			

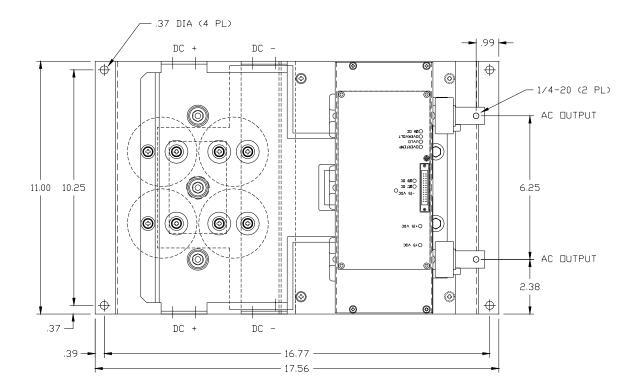
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POW-R-PAK<sup>TM</sup> 150A / 600V H-Bridge IGBT Assembly

## **Mechanical Drawing**





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