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SHARP PQ1CZ21H2Z

## PQ1CZ21H2Z

### **■** Features

- 1. Maximum switching current:1.5A
- 2. Low dissipation current at OFF-state (I<sub>qs</sub>=Max. 1µA)
- 3. Built-in oscillation circuit

(Oscillation frequency:TYP.100kHz)

- 4. Built-in overheat/overcurrent protection function
- 5. Variable output voltage

(Output variable range: V<sub>ref</sub> to 35V/–V<sub>ref</sub> to -30V)

[Possible to select step-down output/inversing output according to external connection circuit]

PQ1CZ21H2ZZ:sleeve-packaged product

PQ1CZ21H2ZP:tape-packaged product

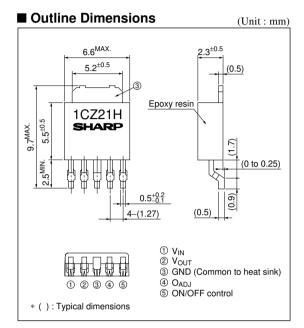
### ■ Applications

- 1. Facsimiles
- 2. Printers
- 3. Switching power supplies

■ Absolute Maximu	(Ta=25°C)		
Parameter	Symbol	Rating	Unit
*1 Input voltage	Vin	40	V
Output adjustment terminal voltage	V <sub>ADJ</sub>	7	V
Dropout voltage	V <sub>I-O</sub>	41	V
*2Output-COM voltage	Vout	-1	V
*3ON/OFF control voltage	Vc	-0.3 to +40	V
Switching current	Isw	1.5	A
*4Power dissipation	PD	8	W
*5 Junction temperature	Tj	150	°C
Operating temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-40 to +150	°C
*6 Soldering temperature	Tsol	260	°C

- \*1 Voltage between V<sub>IN</sub> terminal and COM terminal
- \*2 Voltage between V<sub>OUT</sub> terminal and COM terminal
- \*3 Voltage between ON/OFF control and COM terminal
- \*4 PD:With infinite heat sink
- \*5 Overheat protection may operate at the condition T<sub>j</sub>:125°C to 150°C
- \*6 For 10s

# Low Dissipation Current at OFF-state Chopper Regulator



■ Electrical Characteristics	$(Unless \ otherwise \ specified, \ condition \ shall \ be \ V_{IN}=12V, \ Io=0.2A, \ Vo=5V, \ ON-OFF \ terminal=2.7V, \ Ta=25^{\circ}C)$						
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
Output saturation voltage	Vsat	Isw=1A	_	0.9	1.5	V	
Reference voltage	Vref	_	1.235	1.26	1.285	V	
Reference voltage temperature fluctuation	$\Delta V_{ref}$	Tj=0 to 125°C	_	±0.5	_	%	
Load regulation	RegL	Io=0.2 to 1A	_	0.1	1.5	%	
Line regulation	RegI	V <sub>IN</sub> =8 to 35V	_	0.5	2.5	%	
Efficiency	η	Io=1A	_	82	_	%	
Oscillation frequency	fo	-	80	100	120	kHz	
Oscillation frequency temperature fluctuation	Δfo	Tj=0 to 125°C	-	±3	_	%	
Overcurrent detecting level	IL	No L, C, D	1.55	2	2.6	A	
ON threshold voltage	V <sub>TH(ON)</sub>	4 terminal=0V, 5 terminal	0.8	1.5	2	V	
Output ON control current	Ic (on)	⑤ terminal=2.7V	-	_	200	μΑ	
Output OFF control current	Ic (off)	⑤ terminal=0.4V	_	_	2	μΑ	
Stand-by current	Isd	V <sub>IN</sub> =40V, (5) terminal=0V	-	_	1	μΑ	
Output OFF-state consumption current	Iqs	V <sub>IN</sub> =40V, 4 terminal=3V	_	8	12	mA	

Fig.1 Standard Test Circuit

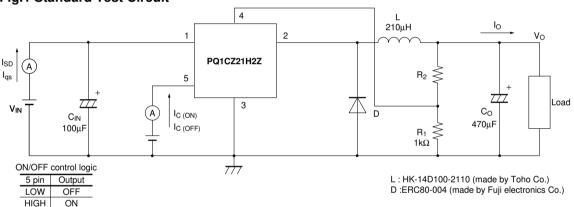
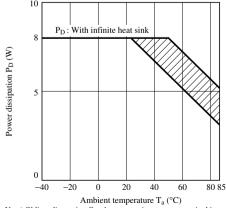


Fig.2 Power Dissipation vs. Ambient **Temperature** 

OPEN

OFF



Note) Oblique line prtion:Overheat protection may operate in this area

Fig.3 Overcurrent Protection Characteristics (Typical value)

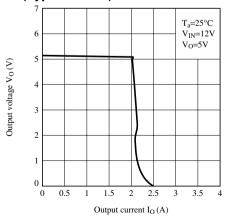


Fig.4 Efficiency vs. Input Current

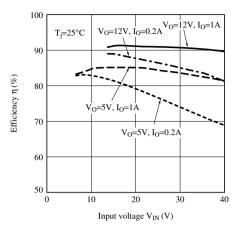


Fig.6 Reference Voltage Fluctuation vs. Junction Temperature

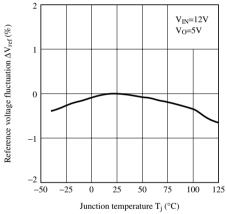


Fig.8 Line Regulation vs. Input Voltage

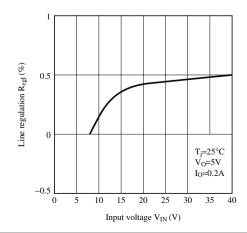


Fig.5 Switching Current vs. Output Saturation Voltage

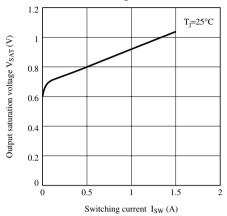


Fig.7 Load Regulation vs. Output Current

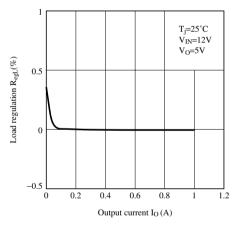
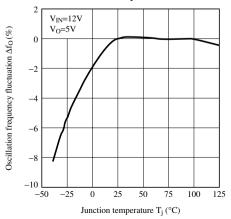


Fig.9 Oscillation Frequency Fluctuation vs. Junction Temperature



SHARP PQ1CZ21H2Z

Fig.10 Overcurrent Detection Level Fluctuation vs. Junction Temperature

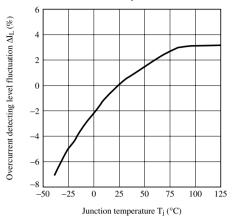


Fig.12 Operating Consumption Current vs. Input Voltage

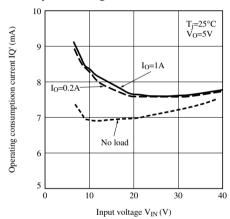


Fig.13 Power Dissipation vs. Ambient Temperature (Typical Value)

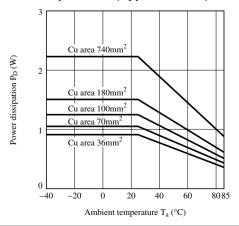
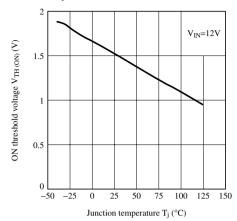


Fig.11 ON Threshold Voltage vs. Junction Temperature





Material: Glass-cloth epoxy resin

Size :  $50 \times 50 \times 1.6$ mm Cu thickness :  $35 \mu m$ 

Fig.14 Block Diagram

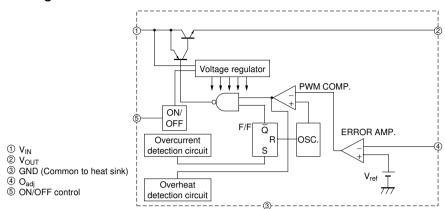


Fig.15 Step Down Type Circuit Diagram (5V output)

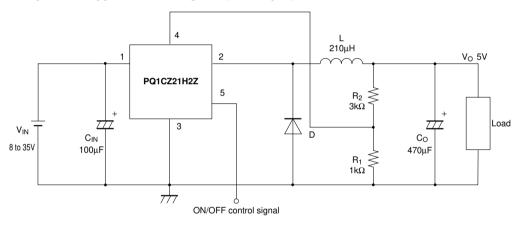
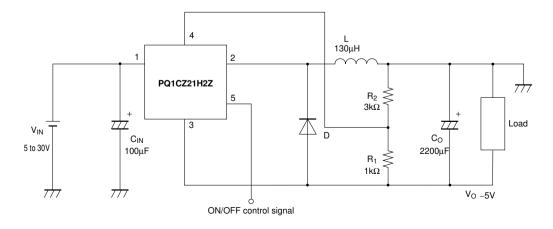
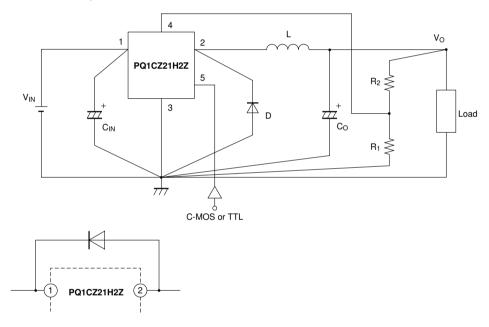


Fig.16 Polarity Inversion Type Circuit Diagram (-5V output)



### ■ Precautions for Use

- 1. External connection
  - (1) Wiring condition is very important. Noise associated with wiring inductance may cause problems. For minimizing inductance, it is recommended to design the thick and short pattern (between large current diodos, input/output capacitors, and terminal 1,2.) Single-point grounding (as indicated) should be used for best results.
  - (2) High switching speed and low forward voltage type schottky barrier diode should be recommended for the catch-diode D because it affects the efficiency. Please select the diode which the current rating is at least 1.2 times greater than maximum switching current.
  - (3) The output ripple voltage is highly influenced by ESR(Equivalent Series Resistor) of output capacitor, and can be minimized by selecting Low ESR capacitor.
  - (4) An inductor should not be operated beyond its maximum rated current so that it may not saturate.
  - (5) When voltage that is higher than  $V_{IN}$  ①, is applied to  $V_{OUT}$  ②, there is the case that the device is broken. Especially, in case  $V_{IN}$  ① is shorted to GND in normal condition, there is the case that the device is broken since the charged electric charge in output capacitor ( $C_0$ ) flows into input side. In such case a schottly barrier diode or a silicon diode shall be recommended to connect as the following circuit.



### ■ Thermal Protection Design

Internal power dissipation(P) of device is generally obtained by the following equation.

P=Isw(Average.) × Vsat×D' + Vin(voltage between Vin to COM terminal)× Iq'(consumption current)

Step down type

$$\overline{D'(Duty)} = \frac{T_{on}}{T(period)} = \frac{V_{O} + V_{F}}{V_{IN} - V_{SAT} + V_{F}}$$

Isw(Average) = Io(Output current.)

Polarity inversion type

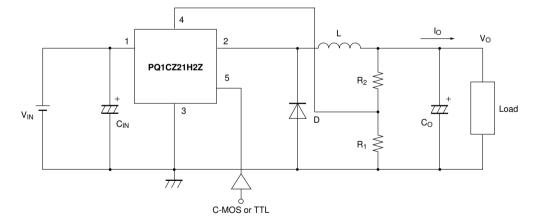
$$\begin{split} D'(Duty) &= \frac{T_{\rm on}}{T(period)} = \frac{|V_{\rm O}| + V_{\rm F}}{V_{\rm IN+} |V_{\rm O}| + V_{\rm SAT} + V_{\rm F}} \\ &= \frac{1}{1 - D'} \times Io(Output\ current.) \end{split}$$

V<sub>F</sub>: Forward voltage of the diode

When ambient temperature Ta and power dissipation Pd (MAX) during operation are determined, use Cu plate which allows the element to operate within the safety operation area specified by the derating curve. Insufficient radiation gives an unfavorable influence to the normal operation and reliability of the device.

### ■ ON/OFF Control Terminal

- 1. In the following circuit, when ON/OFF control terminal (§) becomes low by switching transistor Tr on, output voltage may be turned OFF and the device becomes stand-by mode. Dissipation current at stand-by mode becomes Max.1µA.
- 2. ON/OFF control terminal (§) is compatible with LS-TTL. It enables to be directly drive by TTL or C-MOS standard logic (RCA4000 series). If ON/OFF control terminal is not used, it is recommended to directly connect applicable terminals with input terminal.



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