# imall

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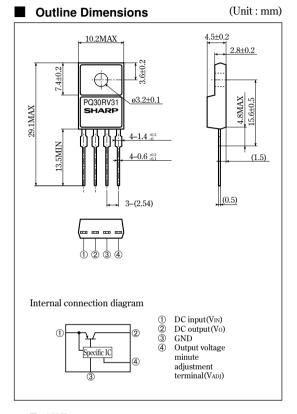


## **PQ30RV31**

Variable Output Low Power-Loss Voltage Regulator

#### Features

- Maximum output current: 3A
- Compact resin full-mold package
- Low power-loss(Dropout voltage: MAX.0.5V)
- Variable output voltage (setting range: 1.5 to 30V)
- Built-in ON/OFF control function.



#### Applications

- Power supply for print concentration control of word processors
- · Series power supply for motors and solenoid
- Series power supply for VCRs and TVs

Absolute Maximum Ratings		(T	a=25°C)
Parameter	Symbol	Rating	Unit
*1 Input voltage	VIN	35	V
*1 Output adjustment terminal voltage	VADJ	7	V
Output current	Io	3	A
Power dissipation (No heat sink)	PD1	2.0	W
Power dissipation (With infinite heat sink)	PD2	20	W
*2 Junction temperature	Tj	150	°C
Operating temperature	Topr	-20 to +80	°C
Storage temperature	Tstg	-40 to +150	°C
Soldering temperature	Tsol	260 (For 10s)	°C

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\*1 All are open except GND and applicable terminals.

\*2 Overheat protection function may operate at 125<=Tj<=150°C.

· Please refer to the chapter " Handling Precautions ".

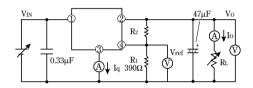
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<b>Electrical Characteristics</b> (Unless otherwise specified, condition shall be V <sub>IN</sub> =12V, Vo=10V, Io=1.5A, R <sub>1</sub> =390Ω, T <sub>a</sub> =25°C)							
Symbol	Conditions	MIN.	TYP.	MAX.	Unit		
VIN	_	4.5	-	35	V		
Vo	-	1.5	-	30	V		
RegL	Io=5mA to 3A	-	0.5	2.0	%		
RegI	VIN=11 to 21V, Io=0.5mA	-	0.5	2.5	%		
RR	Refer to Fig. 2	45	70	Ι	dB		
Vref	-	1.225	1.25	1.275	V		
TcVref	Tj=0 to 125°C,Io=5mA	-	±1.0	-	%/°C		
Vi-o	**3, Io=3A	-	0.3	1.0	v		
	**3, Io=2A	_	0.2	0.5			
Iq	Io=0	_	_	7	mA		
	Symbol VIN Vo RegL RegI RR Vref TcVref Vi-O	$\begin{array}{ c c c c c c c } \hline Symbol & Conditions \\ \hline V_{IN} & - \\ \hline V_0 & - \\ \hline V_0 & - \\ \hline R_{egL} & Io=5mA to 3A \\ \hline R_{egI} & V_{IN}=11 to 21V, Io=0.5mA \\ \hline RR & Refer to Fig. 2 \\ \hline V_{ref} & - \\ \hline T_cV_{ref} & T_j=0 to 125^\circ\text{C}, Io=5mA \\ \hline V_{i^+O} & \stackrel{@3}{=}3A \\ \hline & & & & & & & & \\ \hline V_{i^+O} & \stackrel{@3}{=}3A \\ \hline & & & & & & & & & & \\ \hline \end{array}$	$\begin{array}{ c c c c c c c c } \hline Symbol & Conditions & MIN. \\ \hline V_{IN} & - & 4.5 \\ \hline V_O & - & 1.5 \\ \hline RegL & Io=5mA to 3A & - \\ \hline RegI & V_{IN}=11 to 21V, Io=0.5mA & - \\ \hline RR & Refer to Fig. 2 & 45 \\ \hline V_{ref} & - & 1.225 \\ \hline T_cV_{ref} & T_i=0 to 125^\circ\text{C}, Io=5mA & - \\ \hline V_{i^*O} & \stackrel{@3}{=} 3A & - \\ \hline & \stackrel{@3}{=} 3, Io=2A & - \\ \hline \end{array}$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $		

\*3 Input voltage shall be the value when output voltage is 95% in comparison with the initial value.

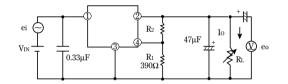
#### Fig. 1 Test Circuit



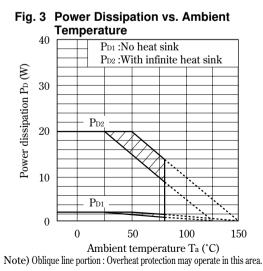
 $V_0=V_{ref} \times \left(1+\frac{R_2}{R_1}\right)$ 

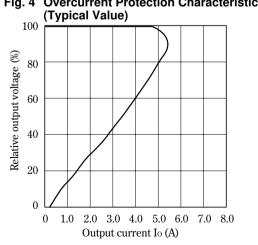
[R1=390Ω, Vref Nearly=1.25V]

Fig. 2 Test Circuit of Ripple Rejection

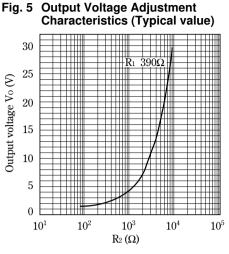


Io=0.5A, VIN=12V, Vo=10V f=120Hz(sine wave) ei(rms)=0.5Vrms RR=20 log(ei(rms)/eo(rms))





### Fig. 4 Overcurrent Protection Characteristics





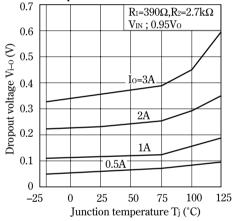
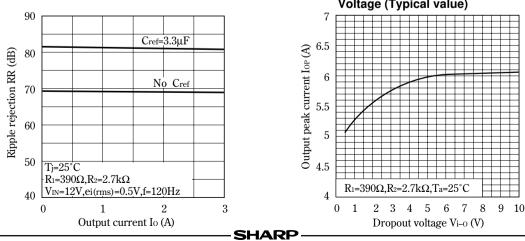


Fig. 9 Ripple Rejection vs. Output Current



#### Fig. 6 Output Voltage vs. Input Voltage

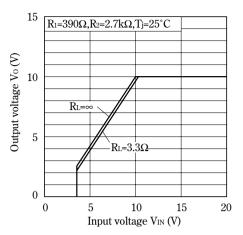


Fig. 8 Ripple Rejection vs. Input Ripple Frequency

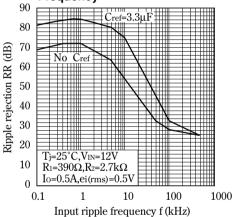
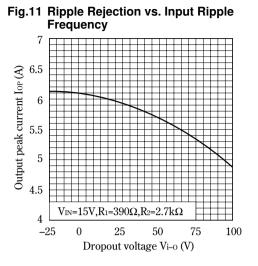
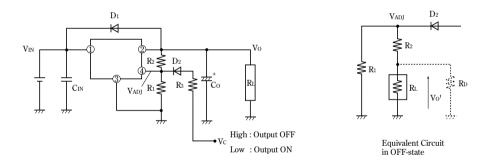


Fig.10 Output Peak Current vs. Dropout Voltage (Typical value)



#### **ON/OFF** Operation



- ON/OFF operation is available by mounting externally D2 and R3.
- When V<sub>ADJ</sub> is forcibly raised above V<sub>REF</sub> (1.25V TYP) by applying the external signal, the output is turned off(pass transistor of regulator is turned off. When the output is OFF, V<sub>ADJ</sub> must be higher then V<sub>REF</sub> MAX., and at the same time must be lower than maximum rating 7V.

In OFF-state, the load current flows to RL from VADJ through R2. Therefore the value of R2 must be as high as possible.

• Vo'=VADJ  $\times RL/(RL+R_2)$ 

occurs at the load. OFF-state equivalent circuit  $R_1$  up to  $10k\Omega$  is allowed. Select as high value of  $R_L$  and  $R_2$  as possible in this range. In some case, as output voltage is getting lower(Vo<1V), impedance of load resistance rises. In such condition, it is sometime impossible to obtain the minimum value of Vo'. So add the dummy resistance indicated by  $R_D$  in the figure to the circuit parallel to the load.

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    - --- Telecommunication equipment [terminal]
    - --- Test and measurement equipment
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    - --- Consumer electronics
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    - --- Alarm equipment
    - --- Various safety devices, etc.

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