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PQ5EV3/PQ5EV5/ PQ5EV7

Features

- 1. Low power-loss
- (Dropout voltage: MAX.0.5V)
- 2. Package with exposed radiation fin (Equivalent to TO-220)
- 3. Large output current 3.5A:PQ5EV3, 5A:PQ5EV5, 7.5A:PQ5EV7
- 4. Variable output voltage (1.5V to 5V)
- 5. High-precision output type (Reference voltage precision:±1.0%)
- 6. Overcurrent, overheat protection functions

Absolute Maximum Ratings

Applications

- 1. Personal computers
- 2. Power supplies for various electronic equipment such as AV or OA

 $(T_{a}-25^{\circ}C)$

Absolute Maximum Ratings (Ta=25°C						
Parameter		Symbol	Rating	Unit		
*1Input voltage		VIN	7	V		
Dropout voltage		VI-0	4	V		
*1 Output control voltage		Vc	7	V		
*1 Output adjustment terminal voltage		VADJ	5	V		
Output current	PQ5EV3		3.5	А		
	PQ5EV5	Io	5.0			
	PQ5EV7		7.5			
*2Power dissipation		PD1	1.6	W		
		PD2	45	W		
*3 Junction temperature		Tj	150	°C		
Operating temperature		Topr	-20 to +80	°C		
Storage temperature		Tstg	-40 to +150	°C		
*4Soldering temperature		Tsol	260	°C		

*1 All are open except GND and applicable terminals

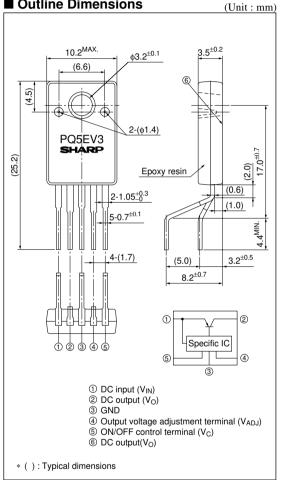
*2 PD1:No heat sink, PD2:With infinite heat sink

*3 Overheat protection may operate at the condition Ti:125°C to 150°C

*4 For 10s

Large Output Current Type Low Power-Loss Voltage Regulator

Outline Dimensions



triad Characteristics

Electrical Characteristics		(Unless otherwise specified, V _{IN} =5V, *5 ,Vo=3V (R1=2k Ω), Ta=25°C)					
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
Input voltage	VIN	_	2.35	-	7	V	
Output voltage	Vo	_	1.5	-	5	V	
Reference voltage	Vref	_	1.2276	1.24	1.2524	V	
Load line regulation	RegL	Io=5mA to rating	-	0.1	0.5	%	
Input line regulation	RegI	$V_{IN}=4$ to 7V, Io=5mA	-	0.05	0.1	%	
Reference voltage temperature coefficient	$T_{\rm C}V_{\rm ref}$	Tj=0 to 125°C	-	±1	-	%	
Ripple Rejection	RR	Refer to Fig.2	60	70	-	dB	
Dropout voltage	VI-0	*6	-	-	0.5	V	
*7 Output on control voltage	VC (ON)	_	2	-	-	v	
Output on control current	IC (ON)	Vc=2.7V	-	_	20	μA	
Output off control voltage	VC (OFF)	_	-	_	0.8	V	
Output off control current	IC (OFF)	Vc=0.4V	-	-	-0.4	mA	
Non-operating dissipatiion current	Iq	Io=0A	-	10	15	mA	

*5 PQ5EV3:IO=1.75A, PQ5EV5:IO=2.5A, PQ5EV7:IO=3.75A

*6 PQ5EV3:10=3.5A, PQ5EV5:10=5A, PQ5EV7:10=7.5A. Input voltage shall be the value when output voltage is 95% in comparison with the initial value

*7 In case of opening control terminal 5, output voltage turns on.

Fig.1 Standard Test Circuit

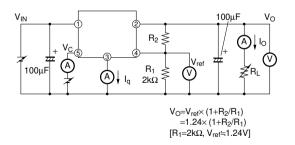
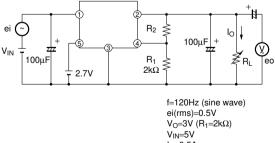
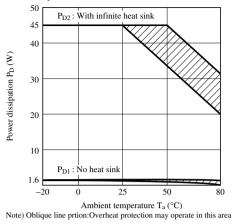


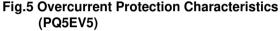
Fig.2 Test Circuit for Ripple Rejection

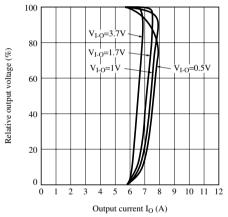


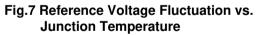
I_O=0.5A RR=20log (ei(rms)/eo(rms))

Fig.3 Power Dissipation vs. Ambient Temperature









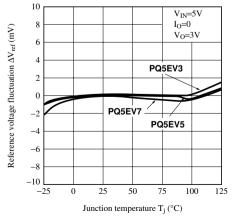


Fig.4 Overcurrent Protection Characteristics (PQ5EV3)

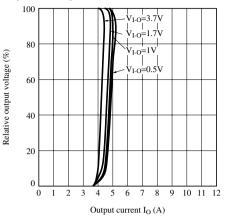


Fig.6 Overcurrent Protection Characteristics (PQ5EV7)

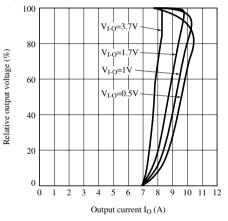


Fig.8 Output Voltage vs. Input Voltage (PQ5EV3)

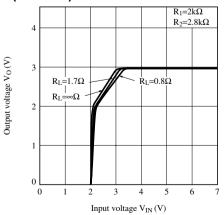
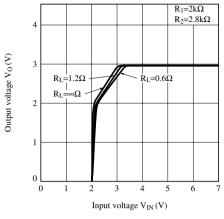
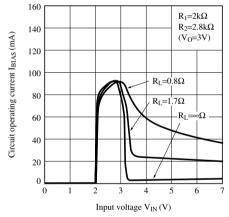


Fig.9 Output Voltage vs. Input Voltage (PQ5EV5)









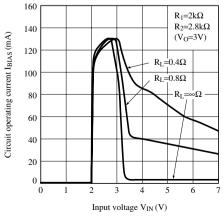


Fig.10 Output Voltage vs. Input Voltage (PQ5EV7)

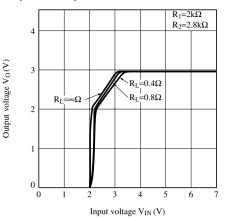


Fig.12 Circuit Operating Current vs. Input Voltage (PQ5EV5)

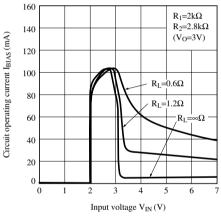
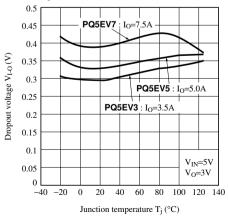


Fig.14 Dropout Voltage vs. Junction Temperature





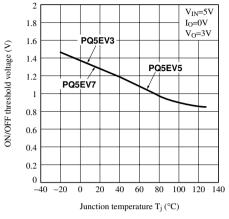


Fig.17 Ripple Rejection vs. Input Ripple Frequency

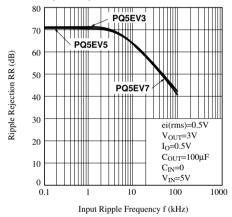


Fig.19 External Connection

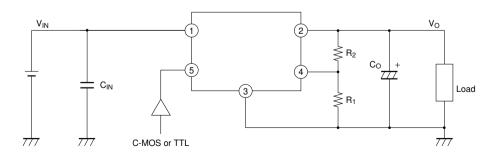


Fig.16 Non-operating Dissipatiion Current vs. Junctiion Temperature

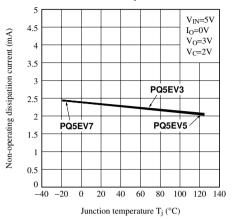
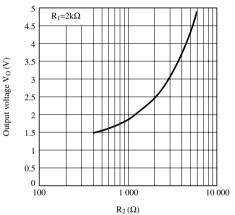


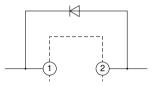
Fig.18 Output Voltage Adjustment Characteristics



SHARP

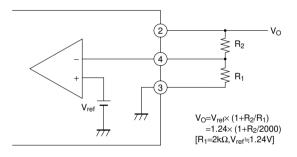
Precautions for Use

- 1. The connecting wiring of C₀ and each terminal must be as short as possible. Owing to type, value and wiring condition of capacitor, it may oscillate. Confirm the output waveform under the actual condition before using.
- 2. ON/OFF control terminal (5) is compatible with LS-TTL. It enables to be directly drive by TTL or C-MOS standard logic (RCA4000 series). If ON/OFF control terminal is not used, it is recommended to directly connect applicable terminals with input terminal.
- 3. If voltage is applied under the conditions that the device pin is connected divergently or reversely, the deterioration of characteristics or damage may occur. Never allow improper mounting.
- 4. If voltage exceeding the voltage of DC input terminal ① is applied to the output terminal ②, the element may be damaged. Especially when the DC input terminal ① is short-circuited to the GND in ordinary operating state, charges accumulated in the output capacitor C₀ flow to the input side, causing damage to the element. In this case, connect the ordinary silicon diode as shown in the figure.



Adjustment of Output Voltage

1. Output voltage is able to set (1.5V to 5V) when resistors R_1 , R_2 are attached to (2), (3), (4) terminals. As for the external resistors to set output voltage, refer to the following figure and Fig.18.



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