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date 06/05/2013

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## **SERIES:** PQZ6-D | **DESCRIPTION:** DC-DC CONVERTER

#### **FEATURES**

- 6 W isolated output
- smaller package
- single/dual regulated output
- 1,500 Vdc isolation
- continuous short circuit, over current protection
- six-sided shielded case
- high vibration tolerance
- rapid dynamic response
- temperature range (-40~85°C)
- high efficiency at light load
- efficiency up to 86%

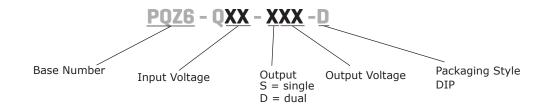


MODEL		nput oltage	output voltage		tput rrent	output power	ripple and noise¹	efficiency
	<b>typ</b> (Vdc)	range (Vdc)	(Vdc)	min (mA)	max (mA)	max (W)	<b>max</b> (mVp-p)	<b>typ</b> (%)
PQZ6-Q24-S3-D	24	9~36	3.3	75	1500	5	80	79
PQZ6-Q24-S5-D	24	9~36	5	60	1200	6	80	83
PQZ6-Q24-S12-D	24	9~36	12	25	500	6	100	87
PQZ6-Q24-S15-D	24	9~36	15	20	400	6	100	88
PQZ6-Q24-D5-D	24	9~36	±5	±30	±600	6	80	83
PQZ6-Q24-D12-D	24	9~36	±12	±12	±250	6	100	87
PQZ6-Q24-D15-D	24	9~36	±15	±10	±200	6	100	87
PQZ6-Q48-S3-D	48	18~75	3.3	75	1500	5	80	80
PQZ6-Q48-S5-D	48	18~75	5	60	1200	6	80	84
PQZ6-Q48-S12-D	48	18~75	12	25	500	6	100	87
PQZ6-Q48-S15-D	48	18~75	15	20	400	6	100	88
PQZ6-Q48-D5-D	48	18~75	±5	±30	±600	6	80	83
PQZ6-Q48-D12-D	48	18~75	±12	±12	±250	6	100	87
PQZ6-Q48-D15-D	48	18~75	±15	±10	±200	6	100	88

Notes:

1. ripple and noise are measured at 20 MHz BW by "parallel cable" method

## **PART NUMBER KEY**



## **INPUT**

parameter	conditions/description	min	typ	max	units
operating input voltage	24 V input models	9	24	36	Vdc
	48 V input models	18	48	75	Vdc
ataut	24 V input models			9	Vdc
start-up voltage	48 V input models			18	Vdc
	for maximum of 1 second				
surge voltage	24 V input models	-0.7		50	Vdc
	48 V input models	-0.7		100	Vdc
filter	pi filter				

## **OUTPUT**

parameter	conditions/description	min	typ	max	units
line regulation	full load, input voltage from low to high		±0.2	±0.5	%
load regulation	5% to 100% load		±0.5	±1	%
cross regulation	dual output main output 50% load, supplement output from 10%-100% load			±5	%
voltage accuracy	5% to 100% load		±1	±2	%
voltage balance	dual output, balanced loads dual output, unbalanced loads		±0.5	±1.5 ±5	% %
switching frequency	5% to 100% load		300		KHz
transient recovery time	25% load step change		300	500	μs
transient response deviation	25% load step change		±3	±5	%
temperature coeffecient	100% load			±0.03	%/°C

## **PROTECTIONS**

parameter	conditions/description	min	typ	max	units
short circuit protection	continuous, automatic recovery				
over voltage protection		110		140	%Vo

## **SAFETY AND COMPLIANCE**

parameter	conditions/description	min	typ	max	units
isolation voltage	for 1 minute at 1 mA max.	1,500			Vdc
isolation resistance	at 500 Vdc	1,000			МΩ
isolation capacitance	input to output, 100 KHz/0.1 V		1,000		pF
conducted emissions	CISPR22/EN55022, class A, class B (extern	al circuit required, see	Figure 1-b)		
radiated emissions	CISPR22/EN55022, class A, class B (extern	CISPR22/EN55022, class A, class B (external circuit required, see Figure 1-b)			
ESD	IEC/EN61000-4-2, class B, contact ± 4kV				
radiated immunity	IEC/EN61000-4-3, class A, 10V/m				
EFT/burst	IEC/EN61000-4-4, class B, ± 2kV (external	circuit required, see F	igure 1-a)		
surge	IEC/EN61000-4-5, class B, ± 2kV (external	circuit required, see F	igure 1-a)		
conducted immunity	IEC/EN61000-4-6, class A, 3 Vr.m.s				
voltage dips & interruptions	IEC/EN61000-4-29, class B, 0%-70%				
MTBF	as per MIL-HDBK-217F @ 25°C	1,000,000			hours
RoHS compliant	yes				

## **ENVIRONMENTAL**

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		105	°C
storage temperature		-55		125	°C
storage humidity	non-condensing	5		95	%
temperature rise	at full load, Ta=25°C		25		°C

## **SOLDERABILITY**

parameter	conditions/description	min	typ	max	units
hand soldering	1.5 mm from case for 10 seconds			300	°C
wave soldering	see wave soldering profile			260	°C

## **MECHANICAL**

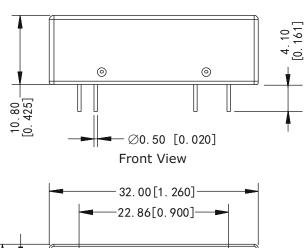
parameter	conditions/description	min	typ	max	units
dimensions	32.00 x 20.00 x 10.80 (1.260 x 0.787 x 0.425 inch)				mm
case material	aluminum alloy				
weight			14		g

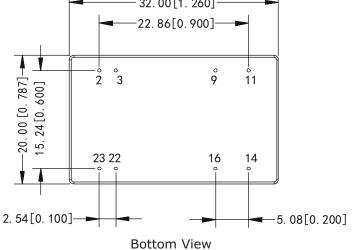
## **MECHANICAL DRAWING**

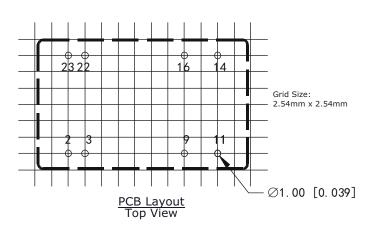
units: mm[inch]

tolerance:  $\pm 0.25[\pm 0.010]$ 

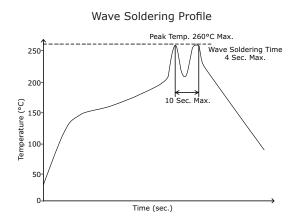
pin diameter tolerance:  $\pm 0.10[\pm 0.004]$ 

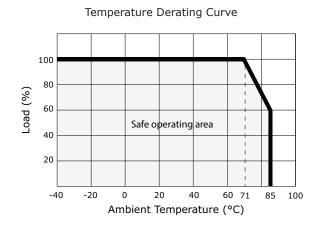






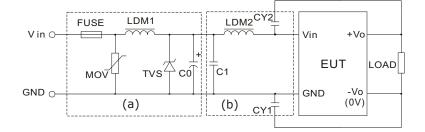
PIN CONNECTIONS				
PIN	Single Output	Dual Output		
2	GND	GND		
3	GND	GND		
9	NO PIN	0V		
11	NC	-Vo		
14	+Vo	+Vo		
16	0V	0V		
22	Vin	Vin		
23	Vin	Vin		





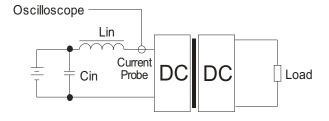
## **EMC RECOMMENDED CIRCUIT**

Figure 1



Recommended external circuit components				
Vin (Vdc)	24	48		
FUSE	choose according to p	practical input current		
MOV	10D560K	10D101K		
LDM1	56µH	56µH		
TVS	SMCJ48A	SMCJ90A		
C0	120μF/50V	120μF/100V		
C1	1μF/50V	1μF/100V		
LDM2	4.7µH	4.7µH		
CY1	1nF/2000V	1nF/2000V		
CY2	1nF/2000V	1nF/2000V		

## **TEST CONFIGURATION**



External components		
Lin 4.7µH		
Cin	220μF, ESR < $1.0\Omega$ at 100 KHz	

Note: Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.

#### **APPLICATION NOTES**

#### 1. Recommended circuit

This series has been tested according to the following recommended testing circuit before leaving the factory. This series should be tested under load (see Figure 2). If you want to further decrease the input/output ripple, you can increase the capacitance accordingly or choose capacitors with low ESR. However, the capacitance of the output filter capacitor must be appropriate. If the capacitance is too high, a startup problem might arise. For every channel of the output, to ensure safe and reliable operation, the maximum capacitance must be less than the maximum capacitive load (see table 1).

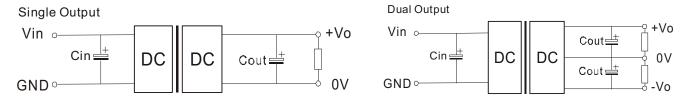


Figure 2

Table 1

Vin (V)	Cin (µF)	Cout (µF)
12	100	10
24	10~47	10
48	10~47	10

Note: 1. Minimum load shouldn't be less than 5%, otherwise ripple may increase dramatically. Operation under minimum load will not damage the converter, however, they may not meet all specifications listed.

<sup>2.</sup> Maximum capacitive load is tested at input voltage range and full load.

<sup>3.</sup> All specifications are measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.

#### **REVISION HISTORY**

rev.	description	date
1.0	initial release	06/05/2013

The revision history provided is for informational purposes only and is believed to be accurate.



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