# imall

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# Contact us

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**Vishay Siliconix** 

Dual P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)			
- 20	0.024 at V <sub>GS</sub> = - 4.5 V	- 5.4			
	0.030 at V <sub>GS</sub> = - 2.5 V	- 4.8			
	0.042 at V <sub>GS</sub> = - 1.8 V	- 4.0			

#### FEATURES

- Halogen-free
- TrenchFET<sup>®</sup> Power MOSFET

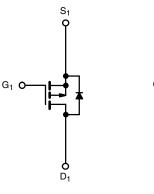
#### APPLICATIONS

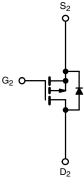
- Load Switch
- Battery Switch



COMPLIANT

TSSOP-8
D1 1 • 8 D2
S1 2 7 S2
S1 3 6 S2
G1 4 5 G2
Top View





Ordering Information: Si6983DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T<sub>A</sub> = 25 °C, unless otherwise noted Parameter Symbol 10 s **Steady State** Unit **Drain-Source Voltage** V<sub>DS</sub> - 20 v  $V_{GS}$ Gate-Source Voltage ± 8 T<sub>A</sub> = 25 °C - 5.4 - 4.6 Continuous Drain Current (T<sub>J</sub> = 150 °C)<sup>a</sup>  $I_D$ T<sub>A</sub> = 70 °C - 4.3 - 3.7 А Pulsed Drain Current (10 µs Pulse Width)  $I_{DM}$ - 30 - 1.0 - 0.7 IS Continuous Source Current (Diode Conduction)<sup>a</sup> T<sub>A</sub> = 25 °C 1.14 0.83  $\mathsf{P}_\mathsf{D}$ w Maximum Power Dissipation<sup>a</sup> T<sub>A</sub> = 70 °C 0.73 0.53 T<sub>J</sub>, T<sub>stg</sub> Operating Junction and Storage Temperature Range - 55 to 150 °C

THERMAL RESISTANCE RATINGS								
Parameter		Symbol	Typical	Maximum	Unit			
Maximum haration to Analyticata	t ≤ 10 s	R <sub>thJA</sub>	86	110				
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		124	150	°C/W			
Maximum Junction-to-Foot (Drain)	Steady State		52	65				

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

### Vishay Siliconix



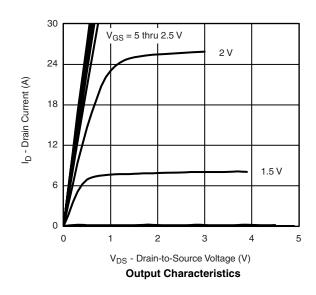
<b>SPECIFICATIONS</b> $T_J = 25 \text{ °C}$ , unless otherwise noted									
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit			
Static									
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -400 \ \mu A$	- 0.40		- 1.0	V			
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -20 V, V_{GS} = 0 V$ -			- 1				
		$V_{DS}$ = - 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 °C			- 25	μΑ			
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 20			А			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5.4 A		0.019	0.024				
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 4.8 A		0.024	0.030	Ω			
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 4.0 A		0.033	0.042	l			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 5.4 A		25		S			
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.0 A, V <sub>GS</sub> = 0 V		- 0.63	- 1.1	V			
Dynamic <sup>b</sup>	-								
Total Gate Charge	Qg			20	30	nC			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 10 V, $V_{GS}$ = - 4.5 V, $I_D$ = - 5.4 A		3.0					
Gate-Drain Charge	Q <sub>gd</sub>			4.5					
Gate Resistance	Rg	f = 1.0 MHz		4.5		Ω			
Turn-On Delay Time	t <sub>d(on)</sub>			40	60				
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 6 $\Omega$		55	85	ns			
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ - 1 A, $\text{V}_\text{GEN}$ = - 4.5 V, $\text{R}_\text{G}$ = 6 $\Omega$		135	200				
Fall Time	t <sub>f</sub>			52	80				
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.0 A, dl/dt = 100 A/μs		40	70				

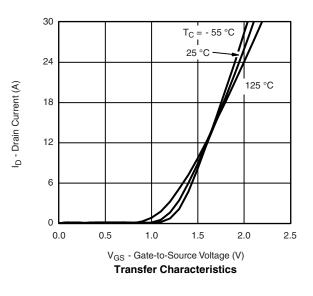
Notes:

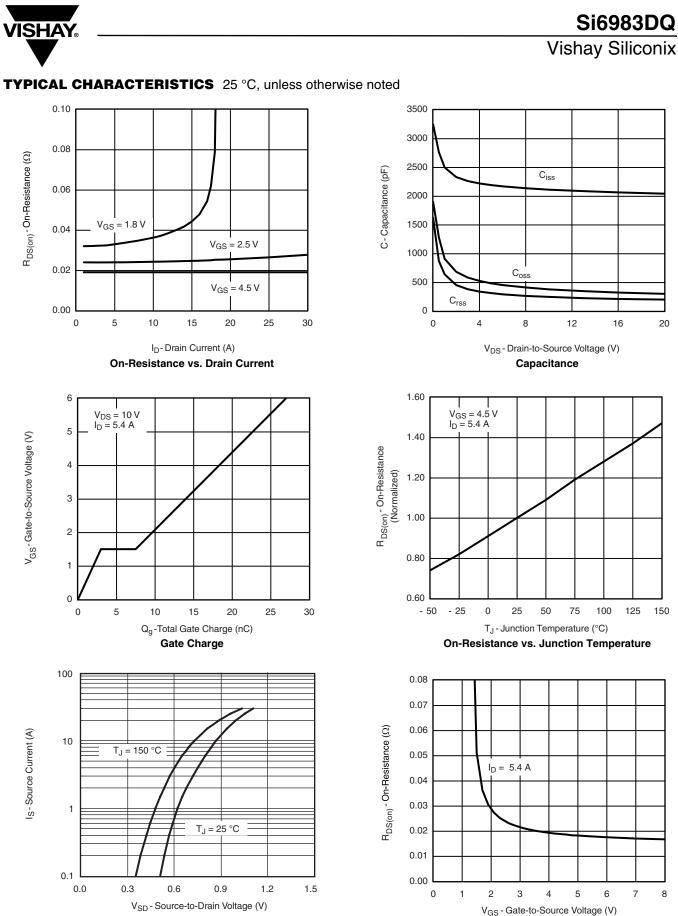
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







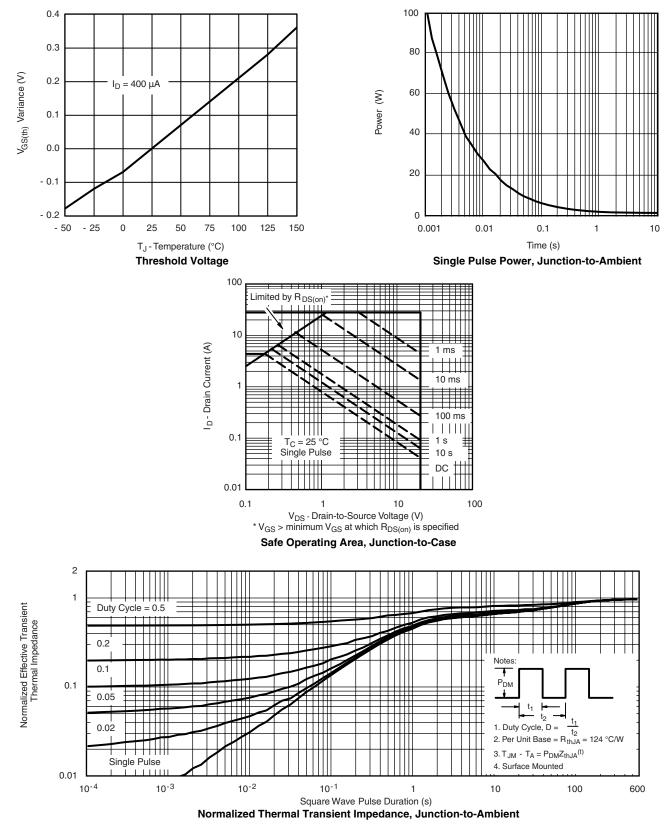
Source-Drain Diode Forward Voltage

Document Number: 72367 S-81221-Rev. D, 02-Jun-08 **On-Resistance vs. Gate-to-Source Voltage** 

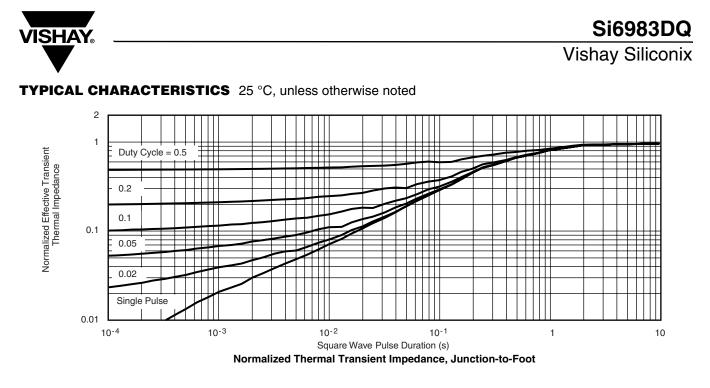
# Si6983DQ

#### Vishay Siliconix

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72367.



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