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NEC'S HIGH ISOLATION VOLTAGE SOP MULTI PHOTOCOUPLER

PS2703-1

FEATURES

- HIGH ISOLATION VOLTAGE BV: 3.75 k Vr.m.s. MIN
- SOP (SMALL OUT-LINE PACKAGE)
- ISOLATED CHANNELS PER EACH PACKAGE
- HIGH COLLECTOR TO EMITTER VOLTAGE VCEO: 120 V MIN
- HIGH SPEED SWITCHING tr, tf = 10 μs TYP
- LOW COLLECTOR DARK CURRENT ICEO: 5 nA TYP @ TA = 25 °C, VCE = 40 V
- TAPE AND REEL AVAILABLE

DESCRIPTION

NEC's PS2703-1 is an optically coupled isolator containing a GaAs light emitting diode and a NPN silicon phototransistor. This device is mounted in a plastic SOP (Small Out-line Package) for high density applications and has a shield effect to cut off ambient light.

APPLICATIONS

Interface circuit for various instrumentations and control equipment.

- AC LINE/DIGITAL LOGIC
- DIGITAL LOGIC INTERFACE
- TWISTED PAIR LINE RECEIVER
- TELEPHONE/TELEGRAPH LINE RECEIVER
- HIGH FREQUENCY POWER SUPPLY FEEDBACK CONTROL
- RELAY CONTACT MONITOR
- POWER SUPPLY MONITOR

PART NUMBER PS2703-1 SYMBOLS PARAMETERS UNITS ТҮР MIN MAX VF Forward Voltage, IF = 5 mA V 1.1 1.4 Diode Reverse Current, VR = 5 V μA 5 ΙR Ct Terminal Capacitance, V= 0, f = 1.0 MHz рF 30 Transistor ICEO Collector to Emitter Dark Current, VCE = 120 V, IF = 0 nA 100 CTR Current Transfer Ratio¹, IF= 5 mA, VCE = 5 V % 50 150 400 10 IF= 1 mA. VCE = 5 V % 80 VCE (sat) Collector Saturation Voltage, IF = 10 mA, Ic = 2 mA V 0.3 Coupled RI-0 Isolation Resistance, Vin-out = 1.0 k VDC Ω 1011 Isolation Capacitance, V = 0, f = 1.0 MHz CI-0 рF 0.4 Rise Time², Vcc = 5 V, Ic = 2 mA, RL = 1 k Ω tr 10 μS Fall Time². Vcc = 5 V. lc = 2 mA. $RL = 1 k \Omega$ 10 tf μS

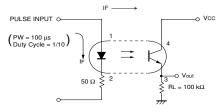
Notes:

1. CTR rank:

CTR Rank	CTR (%)	Conditions	
К	200 to 400	IF = 5 mA, VCE = 5 V	
	80 to	IF = 1 mA, $VCE = 5 V$	
L	100 to 300	IF = 5 mA, VCE = 5 V	
	25 to	IF = 1 mA, $VCE = 5 V$	
М	50 to 150	IF = 5 mA, VCE = 5 V	
	10 to	IF = 1 mA, $VCE = 5 V$	



2. Test Circuit for Switching



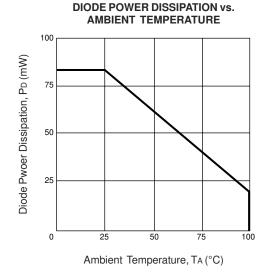
California Eastern Laboratories

ELECTRICAL CHARACTERISTICS (TA = 25°C)

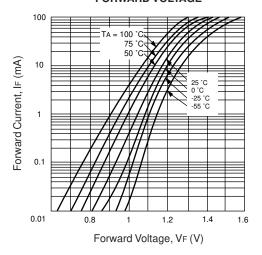
ABSOLUTE MAXIMUM RATINGS¹ (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS PS2703-1
Diode	,		
lF	Forward Current (DC)	mA	50
Vr	Reverse Voltage	V	6
PD	Power Dissipation	mW/Ch	80
lf (peak)	Peak Forward Current (PW = 100 μs, Duty Cycle 1%)	A	1
Transistor			
Vceo	Collector to Emitter Voltage $(Ic = ImA, IB = 0)$	V	120
Veco	Emitter to Collector Voltage $(I_E = 100 \mu A, I_B = 0)$	V	6
lc	Collector Current	mA/Ch	30
PD	Power Dissipation	mW/Ch	150
Coupled			
BV	Isolation Voltage ²	Vr.m.s.	3750
Tstg	Storage Temperature	°C	-55 to +150
TA	Operating Ambient Temperature	°C	-55 to +100

TYPICAL PERFORMANCE CURVES (TA = 25 °C)



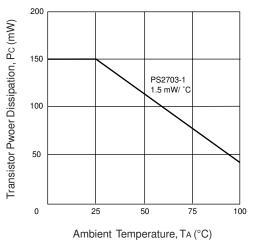
FORWARD CURRENT vs. FORWARD VOLTAGE



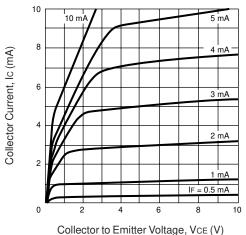
Notes:

- 1. Operation in excess of any one of these parameters may result in permanent damage.
- 2. AC voltage for 1 minute at TA = 25 °C, RH = 60 % between input and ouput.

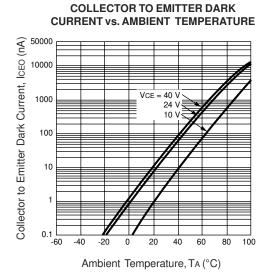
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

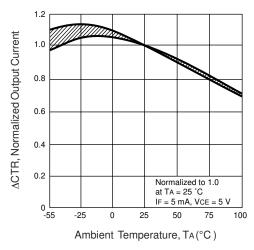


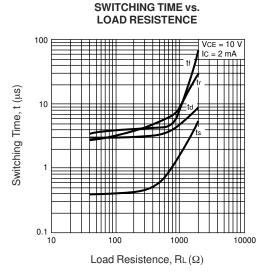
TYPICAL PERFORMANCE CURVES (TA = 25 °)



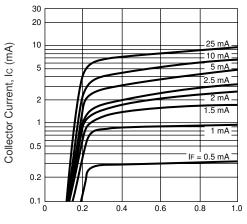
NORMALIZED OUTPUT CURRENT

vs. AMBIENT TEMPERATURE



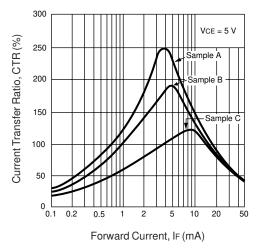


COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE

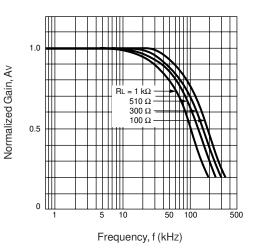


Collector Saturation Voltage, VCE(sat) (V)

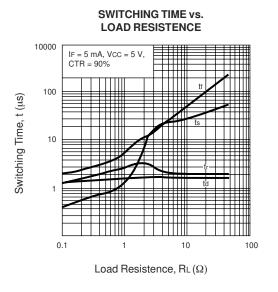
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



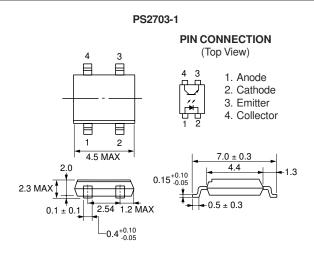
FREQUENCY RESPONSE

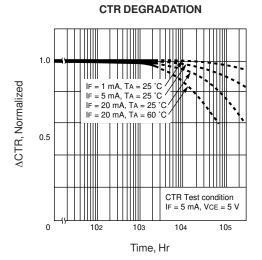


TYPICAL PERFORMANCE CURVES (TA = 25 °)



OUTLINE DIMENSIONS (Units in mm)





Life Support Applications

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