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PS9317L, PS9317L2

HIGH CMR, 10 Mbps, OPEN COLLECTOR OUTPUT TYPE, 8 mm CREEPAGE 6-PIN SDIP PHOTOCOUPLER

DESCRIPTION

The PS9317L and PS9317L2 are optical coupled high-speed, active low type isolators containing a GaAlAs LED on the input side and a photodiode and a signal processing circuit on the output side on one chip.

The PS9317L and PS9317L2 are designed specifically for high common mode transient immunity (CMR) and low pulse width distortion.

The PS9317L is lead bending type (Gull-wing) for surface mounting.

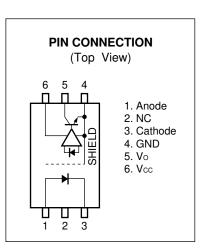
The PS9317L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

FEATURES

- Pulse width distortion ($|t_{PHL} t_{PLH}| = 35 \text{ ns MAX.}$)
- High common mode transient immunity (CM_H, CM_L = $\pm 15 \text{ kV}/\mu \text{s}$ MIN.)
- Half size of 8-pin DIP
- Long creepage distance (8 mm MIN. : PS9317L2)
- High-speed (10 Mbps)
- High isolation voltage (BV = 5 000 Vr.m.s.)
- Open collector output
- Pb-Free product
- Safety standards
 - UL approved: No. E72422
 - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)
 - DIN EN 60747-5-5 (VDE 0884-5) approved (Option)

APPLICATIONS

- Measurement equipment
- PDP
- FA Network



TRUTH TABLE

LED	Output
ON	L
OFF	Н

The mark <R> shows major revised points.

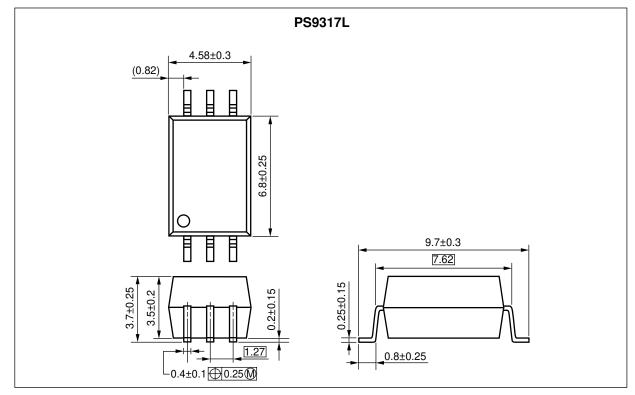
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.



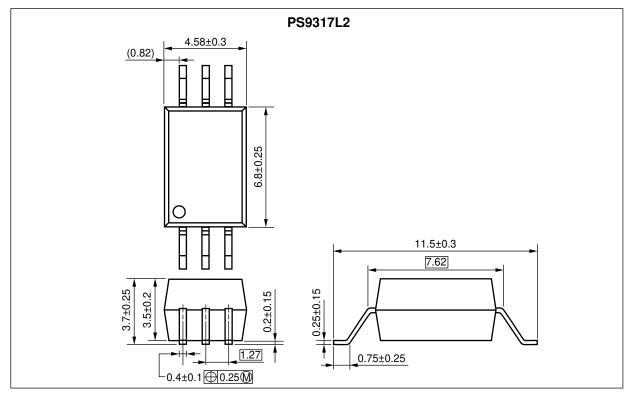
R08DS0135EJ0500 Rev.5.00 Oct 30, 2015

PACKAGE DIMENSIONS (UNIT: mm)

Lead Bending Type (Gull-wing) For Surface Mount



Lead Bending Type For Long Creepage Distance (Gull-wing) For Surface Mount

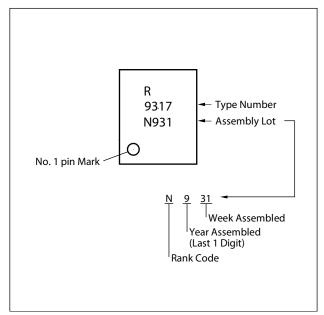




PHOTOCOUPLER CONSTRUCTION

Parameter	PS9317L	PS9317L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

MARKING EXAMPLE



ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number ^{*1}
PS9317L	PS9317L-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9317L
PS9317L-E3	PS9317L-E3-AX	(Ni/Pd/Au)	Embossed Tape 2 000 pcs/reel	(UL, CSA approved)	
PS9317L2	PS9317L2-AX		20 pcs (Tape 20 pcs cut)		PS9317L2
PS9317L2-E3	PS9317L2-E3-AX		Embossed Tape 2 000 pcs/reel		
PS9317L-V	PS9317L-V-AX		20 pcs (Tape 20 pcs cut)	UL, CSA approved	PS9317L
PS9317L-V-E3	PS9317L-V-E3-AX		Embossed Tape 2 000 pcs/reel	DIN EN 60747-5-5	
PS9317L2-V	PS9317L2-V-AX]	20 pcs (Tape 20 pcs cut)	(VDE 0884-5):	PS9317L2
PS9317L2-V-E3	PS9317L2-V-E3-AX	1	Embossed Tape 2 000 pcs/reel	2011-11 approved (Option)	

Note: *1. For the application of the Safety Standard, following part number should be used.



ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current ^{*1}	IF	20	mA
	Reverse Voltage	VR	5	V
Detector	Supply Voltage	Vcc	7	V
	Output Voltage	Vo	7	V
	Output Current	lo	25	mA
	Power Dissipation*2	Pc	40	mW
Isolation Voltage ^{*3}		BV	5 000	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +85	°C
Storage Temperature		Tstg	–55 to +125	°C

Notes: *1. Reduced to 0.3 mA/°C at $T_A = 65^{\circ}C$ or more.

*2. Applies to output pin Vo (collector pin). Reduced to 1.5 mW/°C at $T_A = 65^{\circ}C$ or more.

*3. AC voltage for 1 minute at $T_A = 25^{\circ}$ C, RH = 60% between input and output. Pins 1-3 shorted together, 4-6 shorted together.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	VFL	0		0.8	V
High Level Input Current	Ігн	6		12	mA
Supply Voltage	Vcc	4.5	5.0	5.5	V
TTL ($R_{L} = 1 \ k\Omega$, loads)	Ν			5	
Pull-up Resistor	R∟	330		4 k	Ω



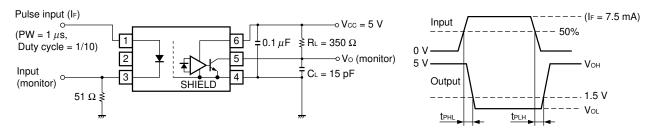
Parameter		Symbol	Conditions	MIN.	TYP. *1	MAX.	Unit
Diode	Diode Forward Voltage		I _F = 10 mA, T _A = 25°C	1.2	1.56	1.9	V
	Reverse Current	I _R	$V_{R} = 3 V, T_{A} = 25^{\circ}C$			10	μA
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T _A = 25°C		30		pF
Detector	High Level Output Current	I _{ОН}	$V_{CC} = V_{O} = 5.5 \text{ V}, \text{ VF} = 0.8 \text{ V}$		1	100	μA
	Low Level Output Voltage ^{*2}	V _{OL}	$V_{CC} = 5.5 \text{ V}, \text{ IF} = 5 \text{ mA},$ $I_{OL} = 13 \text{ mA}$		0.16	0.6	V
	High Level Supply Current	I _{CCH}	V_{CC} = 5.5 V, I_F = 0 mA, V_O = open		3.5	7	mA
	Low Level Supply Current	I _{CCL}	V_{CC} = 5.5 V, I_{F} = 10 mA, V_{O} = open		6.5	10	mA
Coupled	Threshold Input Current $(H \rightarrow L)$	I _{FHL}	$V_{CC} = 5 \text{ V}, \text{V}_{\text{O}} = 0.8 \text{V}, \text{R}_{\text{L}} = 350 \Omega$		2	5	mA
	Isolation Resistance	R _{I-O}	$V_{I\text{-O}} = 1 \text{ kV}_{DC}, \text{RH} = 40 \text{ to } 60\%,$ $T_A = 25^{\circ}\text{C}$	1011			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1 MHz, T _A = 25°C		0.7		pF
	Propagation Delay Time $(H \rightarrow L)^{3}$	t _{PHL}	$\begin{split} V_{CC} &= 5 \ V, \ R_L = 350 \ \Omega, \ C_L = 15 \ pF, \\ I_F &= 7.5 \ mA, \ V_{THHL} = V_{THLH} = 1.5 \ V \end{split}$		40	75	ns
	Propagation Delay Time $(L \rightarrow H)^{*3}$	t _{PLH}			35	75	
	Rise Time	tr			20		
	Fall Time	t _f			5		
	Pulse Width Distortion (PWD)	t _{PHL} -t _{PLH}			5	35	
	Propagation Delay Skew	t _{PSK}				40	
	Common Mode Transient Immunity at High Level Output ^{*4}	CM _H	$\begin{split} V_{CC} &= 5 \ V, \ R_L = 350 \ \Omega, \ T_A = 25^\circ C, \\ I_F &= 0 \ mA, \ V_O > 2 \ V, \ V_{CM} = 1.5 \ kV \end{split}$	15			kV/ <i>µ</i> s
	Common Mode Transient Immunity at Low Level Output ^{*4}	CM∟	$\begin{split} V_{CC} &= 5 \ V, \ R_L = 350 \ \Omega, \ T_A = 25^\circ C, \\ I_F &= 7.5 \ mA, \ V_O < 0.8 \ V, \ V_{CM} = 1.5 \ kV \end{split}$	15			kV/ <i>µ</i> s

ELECTRICAL CHARACTERISTICS ($T_A = -40$ to +85°C, unless otherwise specified)

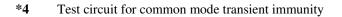


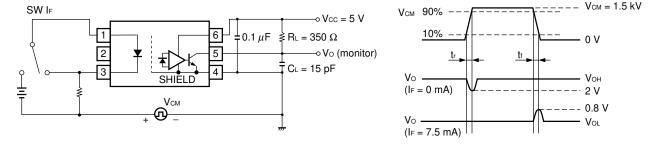
Notes: *1. Typical values at $T_A = 25^{\circ}C$.

- *2. Because VoL of 2 V or more may be output when the LED current is input and when output supply of Vcc = 2.6 V or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.
- *3. Test circuit for propagation delay time
- *4. V_{OH} is measured with the DC load current in this testing (Maximum pulse width = 2 ms, Maximum duty cycle = 20%).



Remark CL includes probe and stray wiring capacitance.





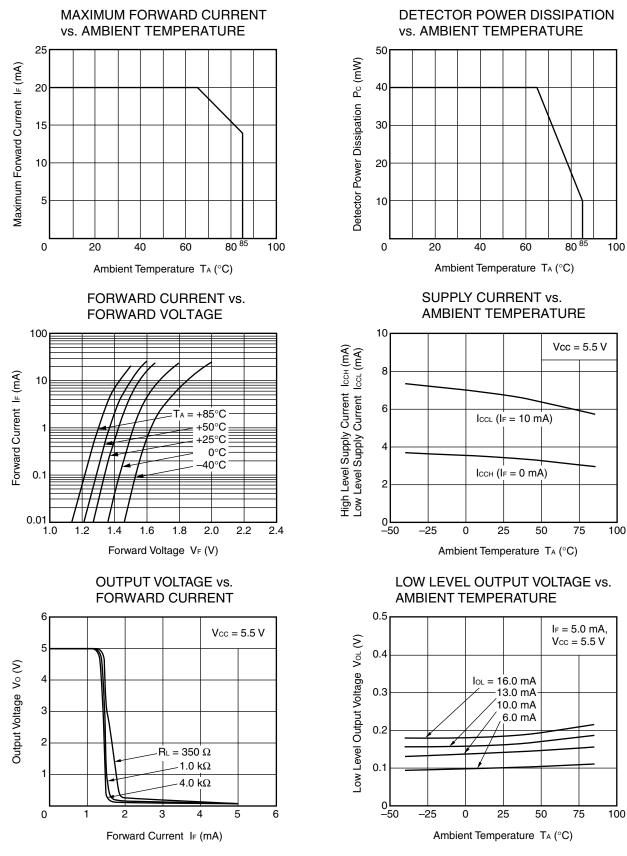
Remark C_L includes probe and stray wiring capacitance.

USAGE CAUTIONS

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of more than $0.1 \,\mu$ F is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Pin 2 (which is an NC^{*1} pin) can either be connected directly to the GND pin on the LED side or left open. Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.
 - *1 NC: Not connected (No connection)
- 4. Avoid storage at a high temperature and high humidity.

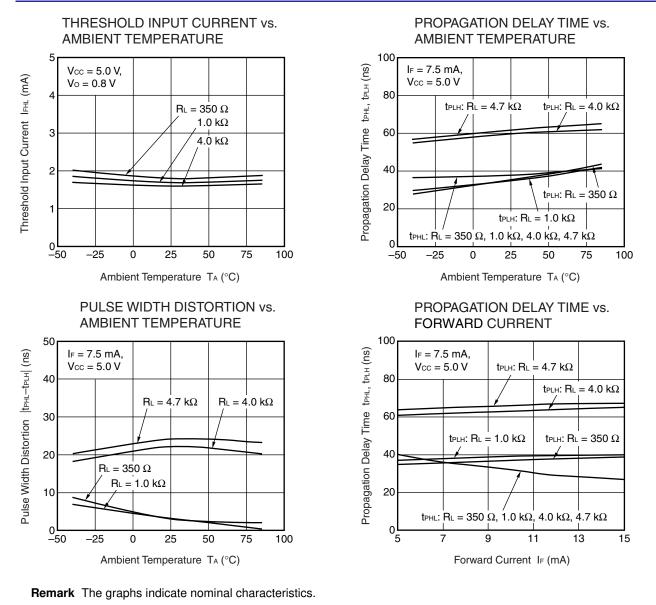






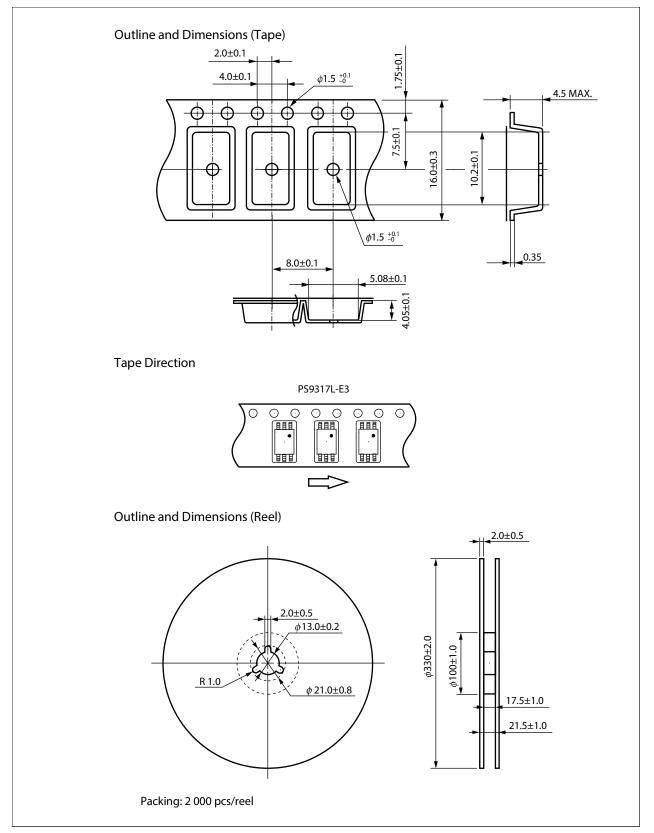
Remark The graphs indicate nominal characteristics.

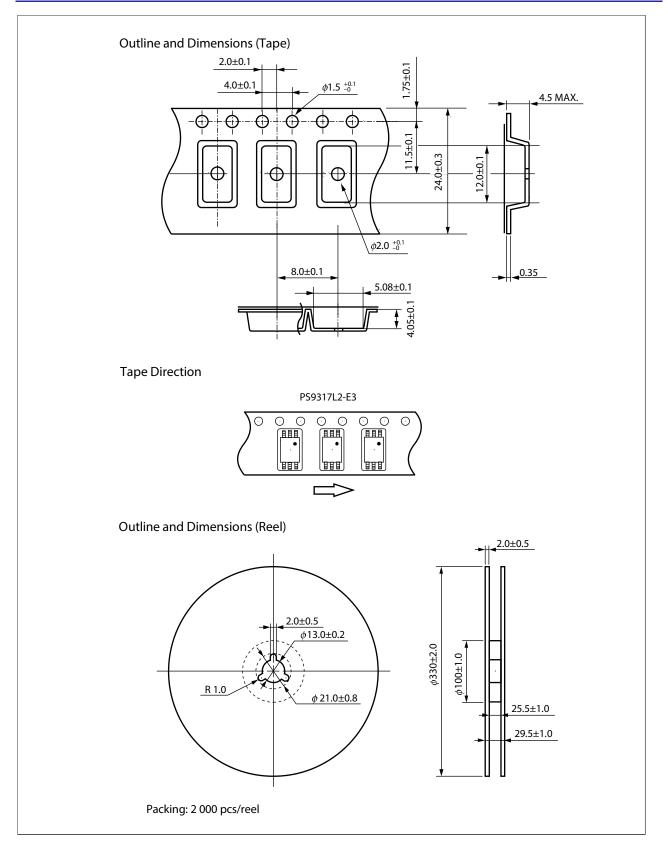




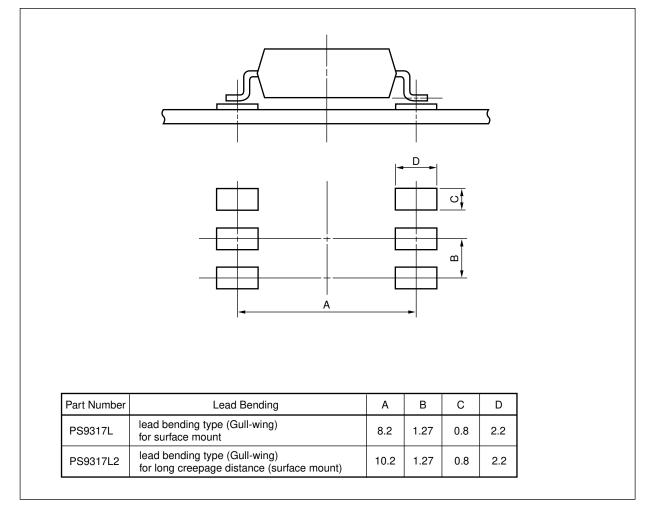


TAPING SPECIFICATIONS (UNIT: mm)





RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)





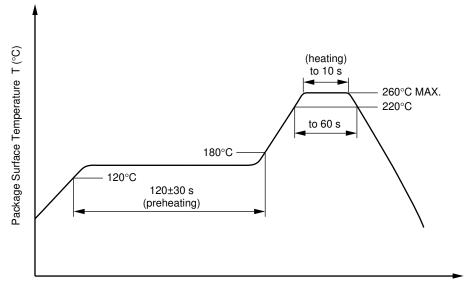
NOTES ON HANDLING

- 1. Recommended soldering conditions
 - (1) Infrared reflow soldering
 - Peak reflow temperature
 - Time of peak reflow temperature
 - Time of temperature higher than 220°C
 - Time to preheat temperature from 120 to 180°C
 - Number of reflows
 - Flux

260 °C or below (package surface temperature) 10 seconds or less 60 seconds or less 120 ± 30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is

Recommended Temperature Profile of Infrared Reflow

recommended.)



Time (s)

(2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

- Peak Temperature (lead part temperature) 350°C or below
- Time (each pins) 3 seconds or less
- Flux

Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100° C.



(4) Cautions

• Fluxes Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collectoremitters at startup, the output side may enter the on state, even if the voltage is within the absolute maximum ratings.

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/85/21	
Dielectric strength			
maximum operating isolation voltage	UIORM	1 130	Vpeak
Test voltage (partial discharge test, procedure a for type test and random test)	Upr	1 808	Vpeak
$U_{pr} = 1.6 \times U_{IORM}, P_d < 5 \ pC$			
Test voltage (partial discharge test, procedure b for all devices)	Upr	2 119	Vpeak
$U_{\text{pr}} = 1.875 \times U_{\text{IORM}}, \ P_{\text{d}} < 5 \ pC$			
Highest permissible overvoltage	Utr	8 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	СТІ	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	-40 to +85	°C
Isolation resistance, minimum value			
$V_{IO} = 500 \text{ V} \text{ dc} \text{ at } T_A = 25^{\circ}\text{C}$	Ris MIN.	10 ¹²	Ω
Vio = 500 V dc at TA MAX. at least 100°C	Ris MIN.	10 ¹¹	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal			
derating curve)			
Package temperature	Tsi	175	°C
Current (input current IF, Psi = 0)	lsi	400	mA
Power (output or total power dissipation)	Psi	700	mW
Isolation resistance			
$V_{IO} = 500 \text{ V dc at } T_A = Tsi$	Ris MIN.	10 ⁹	Ω

SPECIFICATION OF VDE MARKS LICENSE DOCUMENT



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	 Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	 Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
	• Do not burn, destroy, cut, crush, or chemically dissolve the product.
	Do not lick the product or in any way allow it to enter the mouth.



Revision	History
1101101011	11101019

PS9317L, PS9317L2 Data Sheet

		Description			
Rev.	Date	Page Summary			
5.00	Oct 30, 2015	-	First edition issued		

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