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PS9332L, PS9332L2

R08DS0105EJ0100 Rev.1.00 Sep 06, 2013

2.0 A OUTPUT CURRENT, HIGH CMR, IGBT GATE DRIVE, ACTIVE MILLER CLAMP, 8-PIN SDIP PHOTOCOUPLER

DESCRIPTION

The PS9332L and PS9332L2 are optical coupled isolators containing a GaAlAs LED on the input side and a photo diode, a signal processing circuit and a power output transistor on the output side on one chip.

The PS9332L and PS9332L2 are designed specifically for high common mode transient immunity (CMR), high output current, active miller clamp and high switching speed.

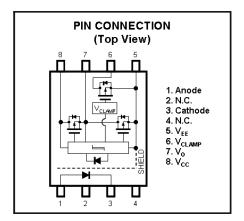
The PS9332L and PS9332L2 are suitable for driving IGBTs and MOS FETs.

FEATURES

- Long creepage distance (8 mm MIN.: PS9332L2)
- Peak output current (2.0 A MAX., 1.5 A MIN.)
- High speed switching (t_{PLH} , $t_{PHL} = 200$ ns MAX.)
- UVLO (Under Voltage Lock Out) protection with hysteresis
- Built-in Active Miller Clamp
- High common mode transient immunity (CM_H, CM_L = ± 50 kV/ μ s MIN.)
- Operating Ambient Temperature (125 °C)
- Embossed tape product: PS9332L-E3, PS9332L2-E3: 2 000 pcs/reel
- Pb-Free product
- <R> Safety standards
 - UL approved: No. E72422
 - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)
 - SEMKO approved (EN 60065, EN 60950)
 - DIN EN 60747-5-5 (VDE 0884-5) approved (Option)

APPLICATIONS

- IGBT, Power MOS FET Gate Driver
- Industrial inverter
- IH (Induction Heating)

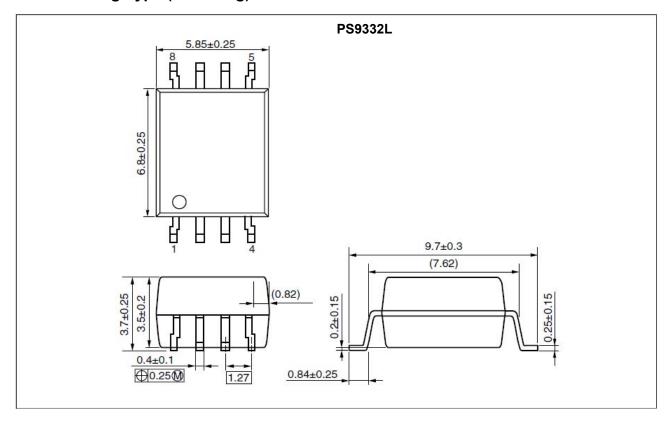


The mark <R> shows major revised points.

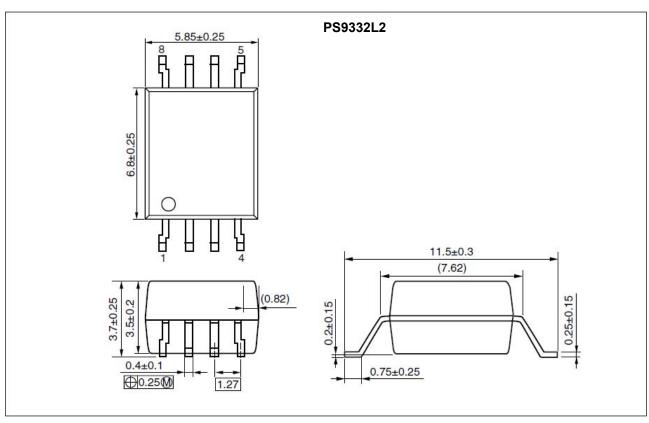
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

PACKAGE DIMENSIONS (UNIT: mm)

Lead Bending Type (Gull-wing) For Surface Mount



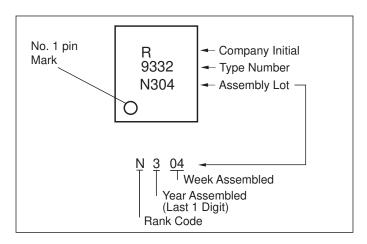
Lead Bending Type (Gull-wing) For Long Creepage Distance (Surface Mount)



PHOTOCOUPLER CONSTRUCTION

Parameter	PS9332L	PS9332L2	
Air Distance (MIN.)	7 mm	8 mm	
Outer Creepage Distance (MIN.)	8 mm	8 mm	
Isolation Distance (MIN.)	0.4 mm	0.4 mm	

MARKING EXAMPLE



<R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1
PS9332L	PS9332L-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard	PS9332L
PS9332L-E3	PS9332L-E3-AX	(Ni/Pd/Au)	Embossed Tape 2 000	products	
			pcs/reel	(UL, CSA,	
PS9332L2	PS9332L2-AX		20 pcs (Tape 20 pcs cut)	SEMKO	PS9332L2
PS9332L2-E3	PS9332L2-E3-AX		Embossed Tape 2 000	approved)	
			pcs/reel		
PS9332L-V	PS9332L-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN 60747-5-5	PS9332L
PS9332L-V-E3	PS9332L-V-E3-AX		Embossed Tape 2 000	(VDE 0884-5)	
			pcs/reel	approved	
PS9332L2-V	PS9332L2-V-AX		20 pcs (Tape 20 pcs cut)	(Option)	PS9332L2
PS9332L2-V-E3	PS9332L2-V-E3-AX		Embossed Tape 2 000		
			pcs/reel		

Note: *1. For the application of the Safety Standard, following part number should be used.

<R> ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current	I _F	25	mA
	Peak Transient Forward Current	I _{F (TRAN)}	1.0	Α
	(Pulse Width < 1 μs)			
	Reverse Voltage	V _R	5	V
	Power Dissipation *1	P _D	45	mW
Detector	High Level Peak Output Current *2	I _{OH (PEAK)}	2.0	Α
	Low Level Peak Output Current *2	I _{OL (PEAK)}	2.0	Α
	Supply Voltage		0 to 35	V
	Output Voltage	Vo	-0.5 to V _{CC}	V
	Peak Clamp Sink Current	I _{CLAMP}	2.0	Α
	Miller Clamping Pin Voltage	V_{CLAMP}	-0.5 to V _{CC}	V
Power Dissipation *3		Pc	250	mW
Isolation \	/oltage *4	BV	5 000	Vr.m.s.
Operating Frequency*5		f	50	kHz
Operating Ambient Temperature		T _A	-40 to +125	°C
Storage T	emperature	T _{stg}	-55 to +150	°C

Notes: *1. Reduced to 1.1 mW/ $^{\circ}$ C at T_A = 105 $^{\circ}$ C or more.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	$(V_{CC} - V_{EE})$	15		30	٧
Forward Current (ON)	I _{F (ON)}	7		16	mA
Forward Voltage (OFF)	V _{F (OFF)}	-2		0.8	V
Operating Ambient Temperature	T _A	-40		125	°C

^{*2.} Maximum pulse width = 10 μ s, Maximum duty cycle = 0.2%

^{*3.} Reduced to 5.5 mW/ $^{\circ}$ C at T_A = 105 $^{\circ}$ C or more.

^{*4.} AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output. Pins 1-4 shorted together, 5-8 shorted together.

^{*5.} $I_{OH (PEAK)} \le 2.0 \text{ A} (\le 0.3 \mu\text{s}), I_{OL (PEAK)} \le 2.0 \text{ A} (\le 0.3 \mu\text{s})$

<R> ELECTRICAL CHARACTERISTICS (at RECOMMENDED OPERATING CONDITIONS, V_{EE} = GND, unless otherwise specified)

Parameter		Parameter Symbol Conditions		MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	V_{F}	I _F = 10 mA, T _A = 25°C	1.35	1.56	1.75	V
	Reverse Current	I _R	V _R = 3 V, T _A = 25°C			10	μΑ
	Input Capacitance	C _{IN}	$f = 1 \text{ MHz}, V_F = 0 \text{ V}, T_A = 25^{\circ}\text{C}$		30		pF
Detector	High Level Output Current	I _{OH}	$V_{\rm O} = (V_{\rm CC} - 4 \text{ V})^{*2}$	0.5	1.5		Α
			$V_{\rm O} = (V_{\rm CC} - 15 \text{ V})^{*3}$	1.5			
	Low Level Output Current	I _{OL}	$V_{\rm O} = (V_{\rm EE} + 2.5 \text{ V})^{*2}$	0.5	1.5		Α
			$V_{\rm O} = (V_{\rm EE} + 15 \text{ V})^{*3}$	1.5			
	Clamp Output Peak Current	I _{CLAMP}	$V_{CLAMP} = V_{EE} + 2.5V$	0.5	1.6		Α
	Clamp Pin Threshold Voltage High Level Output Voltage				3.0		V
			I _O = - 100 mA ^{*4}	$V_{CC} - 3.0$	V _{CC} - 1.3		V
	Low Level Output Voltage	V _{OL}	I _O = 100 mA		0.1	0.5	V
	High Level Supply Current	I _{CCH}	I_F = 10 mA, V_O = open		1.5	2.5	mA
	Low Level Supply Current	I _{CCL}	$V_F = 0 \text{ to } 0.8 \text{ V}, V_O = \text{open}$		1.5	2.5	mA
	UVLO Threshold Voltage	Vuvlo+	Vo > 5V , I _F = 10mA	10.8	12.4	13.4	V
		Vuvlo-	Vo < 5V , I _F = 10mA	9.5	11.2	12.5	
	UVLO Hysteresis	UVLO _{HYS}	(V _{UVLO+}) - (V _{UVLO-})		1.2		
Coupled	Threshold Input Current $(L \rightarrow H)$	I _{FLH}	I _O = 0 mA, V _O > 5 V		1.5	4.0	mA
	Threshold Input Voltage $(H \rightarrow L)$	V_{FHL}	I _O = 0 mA, V _O < 5 V	0.8			٧

Notes: *1. Typical values at $T_A = 25^{\circ}C$.

$^{\mbox{\footnotesize <R>}}$ SWITCHING CHARACTERISTICS (at RECOMMENDED OPERATING CONDITIONS, $V_{\mbox{\footnotesize EE}}$ = GND, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Propagation Delay Time $(L \rightarrow H)$	t _{PLH}	$R_g = 10 \Omega, C_g = 10 nF,$		75	200	ns
Propagation Delay Time $(H \rightarrow L)$	t _{PHL}	f = 10 kHz,		110	200	ns
Pulse Width Distortion (PWD)	t _{PHL} -t _{PLH}	Duty Cycle = 50% ^{*2} ,		35	75	ns
Propagation Delay Time	t _{PHL} -t _{PLH}	I _F = 10 mA	-90		90	ns
(Difference Between Any Two						
Products)						
Rise Time	t _r			17		ns
Fall Time	t _f			17		ns
Common Mode Transient	CM _H	$T_A = 25^{\circ}C, I_F = 10 \text{ mA},$	50			kV/ <i>μ</i> s
Immunity at High Level Output		$V_{CC} = 30 \text{ V}, V_{CM} = 1.5 \text{ kV}$				
		V _{O (MIN.)} = 26 V				
Common Mode Transient	CM _L	$T_A = 25^{\circ}C, I_F = 0 \text{ mA},$	50			kV/ <i>μ</i> s
Immunity at Low Level Output		$V_{CC} = 30 \text{ V}, V_{CM} = 1.5 \text{ kV}$				
		V _{O (MAX.)} = 1V				

Notes: *1. Typical values at $T_A = 25$ °C.

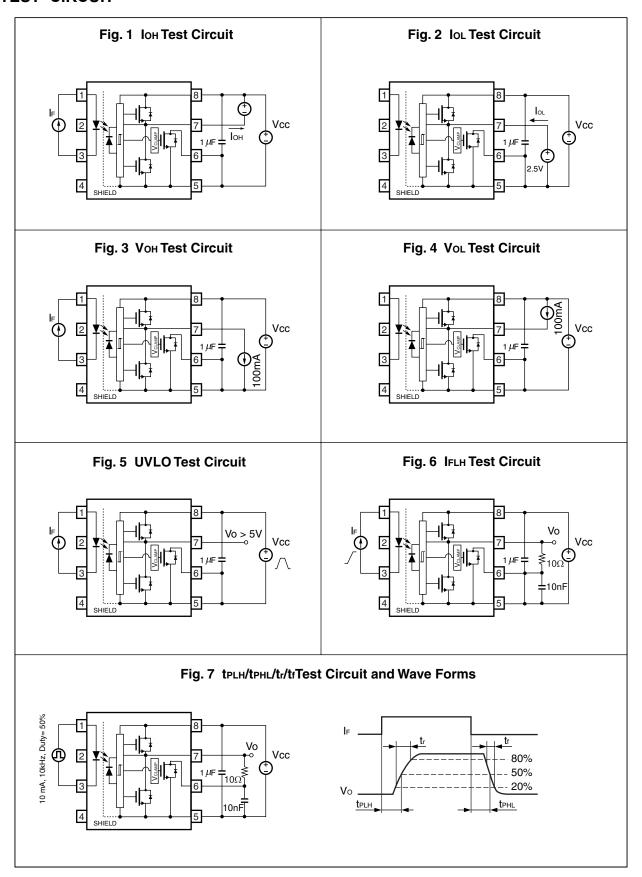
^{*2.} Maximum pulse width = 50 μ s, Maximum duty cycle = 0.5%.

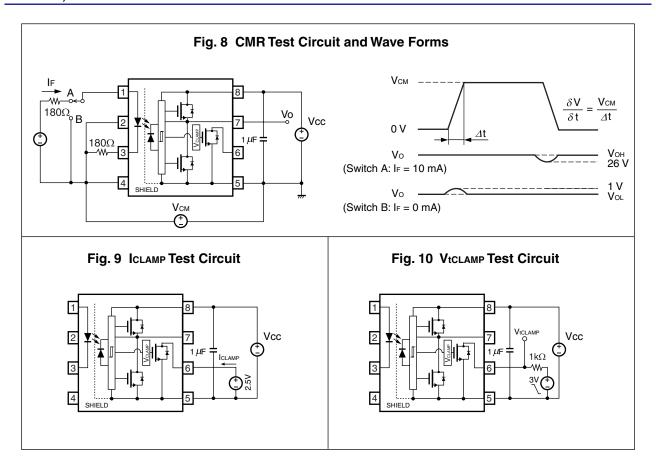
^{*3.} Maximum pulse width = 10 μ s, Maximum duty cycle = 0.2%.

^{*4.} V_{OH} is measured with the DC load current in this testing. (Maximum pulse width = 2 ms, Maximum duty cycle = 20%)

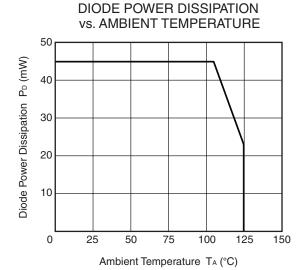
^{*2.} This load condition is equivalent to the IGBT load at 1 200 V/75 A.

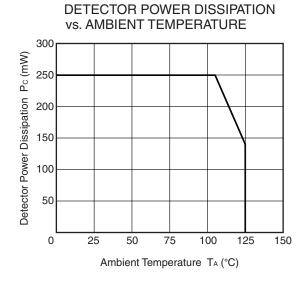
<R> TEST CIRCUIT

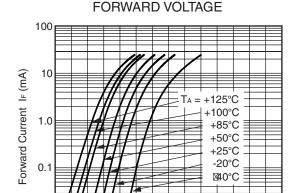




<R> TYPICAL CHARACTERISTICS (T_A = 25°C, unless otherwise specified)

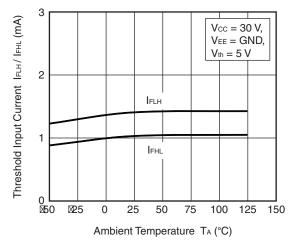






FORWARD CURRENT vs.





OUTPUT VOLTAGE vs. FORWARD CURRENT

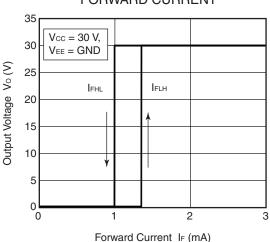
Forward Voltage V_F (V)

1.6

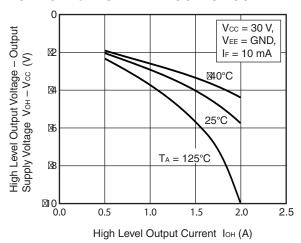
2.0

2.2

2.4



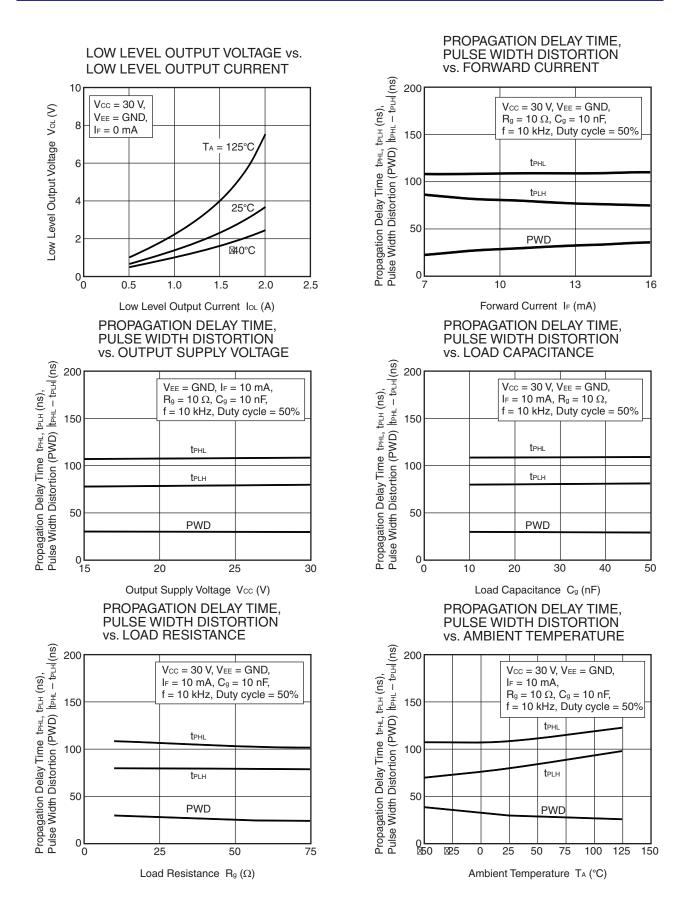
HIGH LEVEL OUTPUT VOLTAGE – OUTPUT SUPPLY VOLTAGE vs. HIGH LEVEL OUTPUT CURRENT



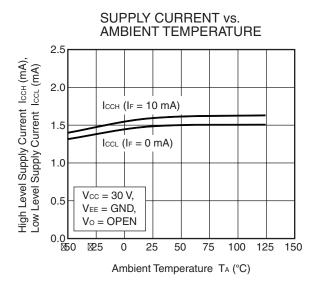
Remark The graphs indicate nominal characteristics.

0.01

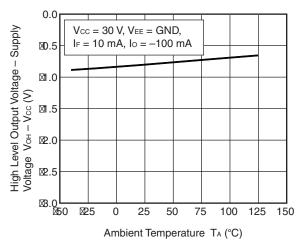
1.2



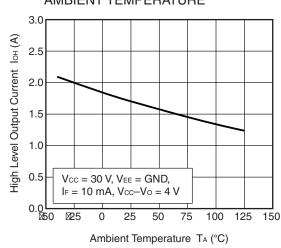
Remark The graphs indicate nominal characteristics.



HIGH LEVEL OUTPUT VOLTAGE – SUPPLY VOLTAGE vs. AMBIENT TEMPERATURE

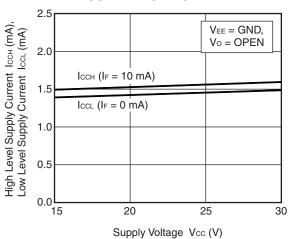


HIGH LEVEL OUTPUT CURRENT vs. AMBIENT TEMPERATURE

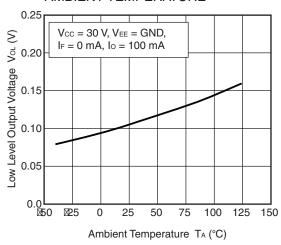


Remark The graphs indicate nominal characteristics.

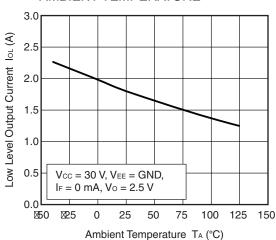
SUPPLY CURRENT vs. SUPPLY VOLTAGE



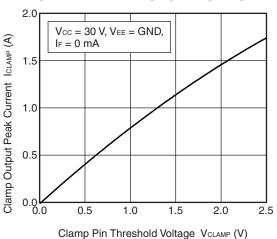
LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE



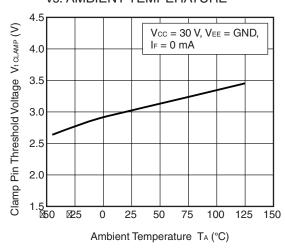
LOW LEVEL OUTPUT CURRENT vs. AMBIENT TEMPERATURE



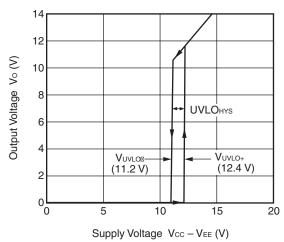
CLAMP OUTPUT PEAK CURRENT vs. CLAMP PIN THRESHOLD VOLTAGE



CLAMP PIN THRESHOLD VOLTAGE vs. AMBIENT TEMPERATURE

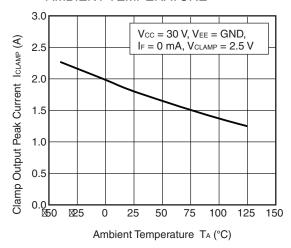


OUTPUT VOLTAGE vs. SUPPLY VOLTAGE

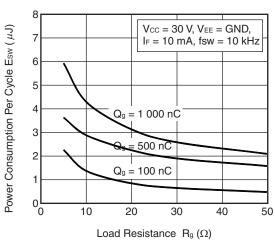


Remark The graphs indicate nominal characteristics.

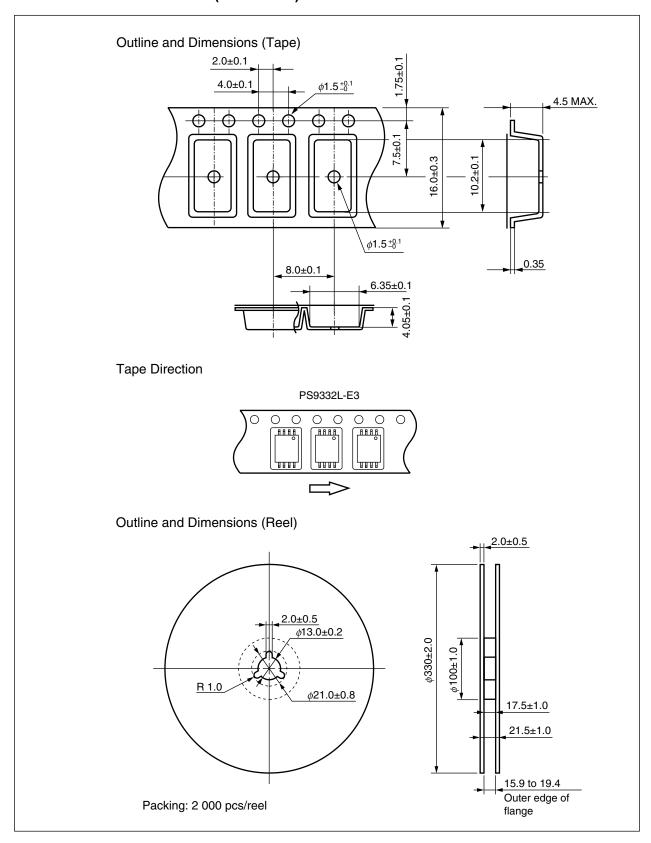
CLMAP OUTPUT PEAK CURRENT vs. AMBIENT TEMPERATURE

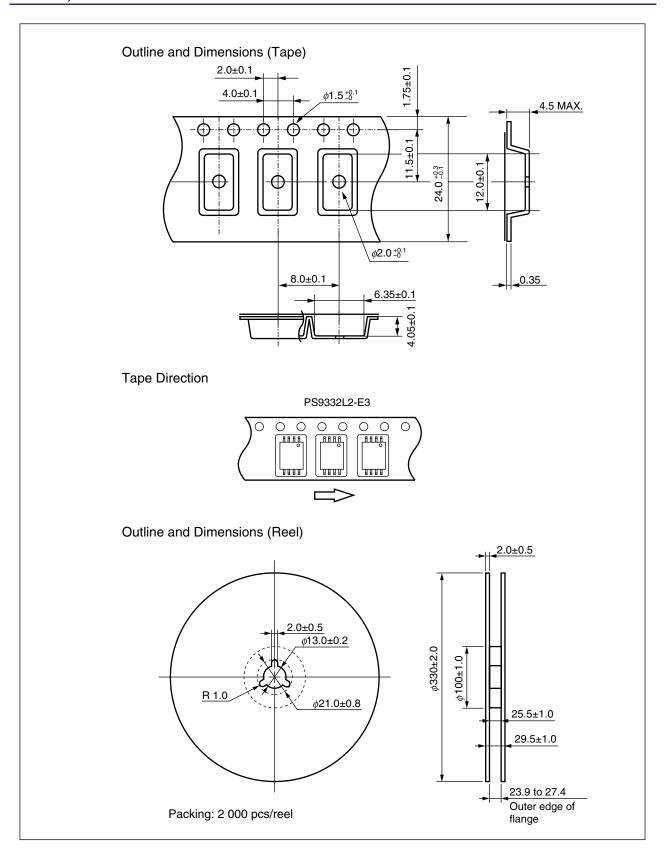


POWER CONSUMPTION PER CYCLE vs. LOAD RESISTANCE

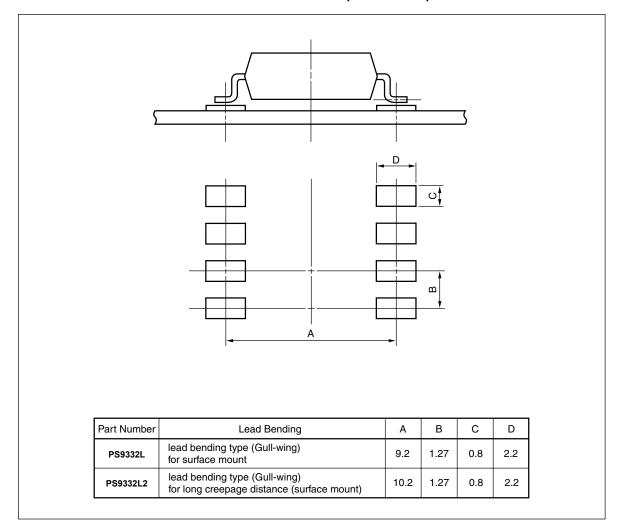


TAPING SPECIFICATIONS (UNIT: mm)





RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



NOTES ON HANDLING

- 1. Recommended soldering conditions
 - (1) Infrared reflow soldering

Peak reflow temperature 260°C or below (package surface temperature)

Time of peak reflow temperature 10 seconds or less Time of temperature higher than 220°C 60 seconds or less

Time to preheat temperature from 120 to 180°C $120 \pm 30 \text{ s}$

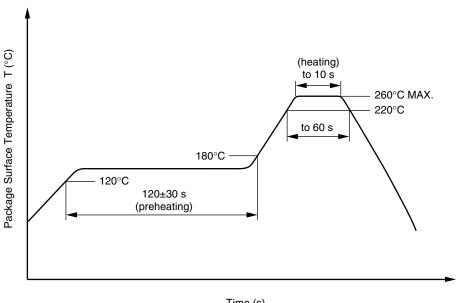
Number of reflows Three

Flux Rosin flux containing small amount of chlorine (The flux

with a maximum chlorine content of 0.2 Wt% is

recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

Temperature 260°C or below (molten solder temperature)

Time 10 seconds or less

Preheating conditions 120°C or below (package surface temperature)

Number of times One (Allowed to be dipped in solder including plastic mold portion.)

Rosin flux containing small amount of chlorine (The flux with a maximum chlorine Flux

content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below

Time (each pins)

Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(4) Cautions

 Fluxes Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. Board designing
 - (1) By-pass capacitor of more than 1.0 μ F is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
 - (2) When designing the printed wiring board, ensure that the pattern of the IGBT collectors/emitters is not too close to the input block pattern of the photocoupler. If the pattern is too close to the input block and coupling occurs, a sudden fluctuation in the voltage on the IGBT output side might affect the photocoupler's LED input, leading to malfunction or degradation of characteristics. (If the pattern needs to be close to the input block, to prevent the LED from lighting during the off state due to the abovementioned coupling, design the input-side circuit so that the bias of the LED is reversed, within the
 - range of the recommended operating conditions, and be sure to thoroughly evaluate operation.)
 (3) Pin 2,4 (which is an NC*1 pin) can either be connected directly to the GND pin on the LED side or left open.
 Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.
 - Note: *1. NC: Non-Connection (No Connection).
- 3. Make sure the rise/fall time of the forward current is 0.5 μ s or less.
- 4. In order to avoid malfunctions, make sure the rise/fall slope of the supply voltage is $3 \text{ V}/\mu\text{s}$ or less.
- 5. Avoid storage at a high temperature and high humidity.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Maximum repetitive peak operating isolation voltage	U_IORM	1 130	V_{peak}
Partial discharge test voltage at 100% production test	U _{pr}	1 808	V_{peak}
U_{pr} = 1.875 × $U_{IORM.}$, Method b, t_m =1sec, p_d < 5 pC			
Partial discharge test voltage at Type test and Sample test	U_pr	2 119	V_{peak}
U_{pr} = 1.875 × $U_{IORM.}$, Method a, t_m =10sec, p_d < 5 pC			
Maximum transient isolation voltage (Transient overvoltage t _{ini} =60sec)	U _{iOTM}	8 000	V_{peak}
Installation classification (IEC 60664/ DIN EN 60664-1/ VDE0110 Part 1)			
for rated mains voltage ≤ 300 Vr.m.s.		I - IV	
for rated mains voltage < 600 Vr.m.s.		I - IV	
for rated mains voltage ≤ 1 000 Vr.m.s.		I - III	
Comparative tracking index (IEC 60112/ DIN EN 60112/ VDE 0303 Part 11)	CTI	175	
Material group (DIN EN 60664-1/ VDE0110 Part 1)		III a	
Pollution degree (DIN EN 60664-1/ VDE0110 Part 1)		2	
Climatic category (IEC 60068-1/ DIN EN 60068-1)		40/125/21	
Operating temperature range	T _A	-40 to +125	°C
Storage temperature range	T _{stg}	-55 to +150	°C
Isolation resistance, minimum value			
V_{IO} = 500 Vdc at T_A = 25°C	Ris MIN.	10 ¹²	Ω
V _{IO} = 500 Vdc at T _A MAX. at least 100°C	Ris MIN.	10 ¹¹	Ω
Safety limiting values ratings (maximum allowable in the event of a fault			
or a failure, see thermal derating curve)			
Maximum ambient safety temperature	Ts	175	°C
Maximum input current	Isi	400	mA
Maximum output power	Pso	700	mW
Isolation resistance at V _{IO} = 500 Vdc, T _A =Ts	Ris MIN.	10 ⁹	Ω

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

Revision History

PS9332L, PS9332L2 Data Sheet

		Description		
Rev.	Date	Page	Summary	
0.01	Nov 30, 2012	-	First edition issued	
1.00	Sep 06, 2013	Throughout	"Preliminary Data Sheet" is changed to "Data Sheet."	
		p.1	Addition of Safety standards	
		p.3	Addition of ORDERING INFORMATION	
		p.4	Modification of ABSOLUTE MAXIMUM RATINGS	
		p.5	Modification of ELECTRICAL / SWITCHING CHARACTERISTICS	
		p.6 to 7	Modification of TEST CIRCUIT	
		p.8 to 11	Addition of TYPICAL CHARACTERISTICS	
		p.17	Addition of SPECIFICATION OF VDE MARKS LICENSE DOCUMENT	

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