

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









PSMNR90-30BL

N-channel 30 V 1.0 m Ω logic level MOSFET in D2PAK **Product data sheet**

1. **General description**

Logic level N-channel MOSFET in D2PAK package qualified to 175 °C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

2. **Features and benefits**

- High efficiency due to low switching and conduction losses
- Suitable for logic level gate drive sources

Applications 3.

- DC-to-DC converters
- Load switiching
- Motor control
- Server power supplies

Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	30	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; <u>Fig. 2</u>	[1]	-	-	120	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 1</u>		-	-	306	W
Tj	junction temperature			-55	-	175	°C
Static charact	eristics						
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; Fig. 12		-	0.89	1	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 100 °C; Fig. 13; Fig. 12		-	1.19	1.5	mΩ
Dynamic char	acteristics						
Q_{GD}	gate-drain charge	V_{GS} = 4.5 V; I_D = 75 A; V_{DS} = 15 V;		-	37	-	nC
Q _{G(tot)}	total gate charge	Fig. 14; Fig. 15		-	118	-	nC



N-channel 30 V 1.0 m Ω logic level MOSFET in D2PAK

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Avalanche rug	Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain- source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 120 A; $V_{sup} \le$ 30 V; R_{GS} = 50 Ω; unclamped		-	-	1.9	J

^[1] Continuous current is limited by package.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D I
2	D	drain[1]		
3	S	source		G TITA
mb	D	mounting base; connected to drain	D2PAK (SOT404)	mbb076 S

^[1] It is not possible to make connection to pin 2

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PSMNR90-30BL	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404			

7. Marking

Table 4. Marking codes

Type number	Marking code
PSMNR90-30BL	PSMNR90-30BL

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	30	V
V_{DGR}	drain-gate voltage	$T_j \ge 25 ^{\circ}\text{C}; T_j \le 175 ^{\circ}\text{C}; R_{GS} = 20 \text{k}\Omega$	-	30	V

N-channel 30 V 1.0 m Ω logic level MOSFET in D2PAK

Symbol	Parameter	Conditions		Min	Max	Unit
V _{GS}	gate-source voltage			-20	20	V
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 1</u>		-	306	W
I _D	drain current	V _{GS} = 10 V; T _{mb} = 100 °C; <u>Fig. 2</u>	[1]	-	120	Α
		V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 2</u>	[1]	-	120	Α
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 °C$; Fig. 3		-	1573	Α
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
T _{sld(M)}	peak soldering temperature			-	260	°C
Source-dra	in diode	'				
I _S	source current	T _{mb} = 25 °C	[1]	-	120	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	1573	Α
Avalanche	ruggedness		•			
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_{D} = 120 A; V_{sup} ≤ 30 V; R_{GS} = 50 Ω; unclamped		-	1.9	J

[1] Continuous current is limited by package.

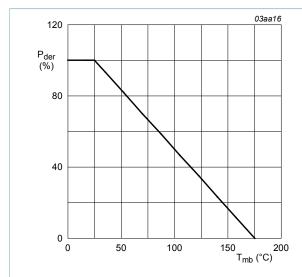


Fig. 1. Normalized total power dissipation as a function of mounting base temperature

$$P_{\textit{der}} = \frac{P_{\textit{tot}}}{P_{\textit{tot}(25^{\circ}\text{C})}} \times \textbf{100 \%}$$

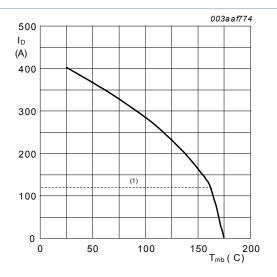


Fig. 2. Continuous drain current as a function of mounting base temperature.

 $V_{\it GS} \! \geq 10 \; {
m V}; \;\;$ (1) Capped at 120 A due to package

N-channel 30 V 1.0 m Ω logic level MOSFET in D2PAK

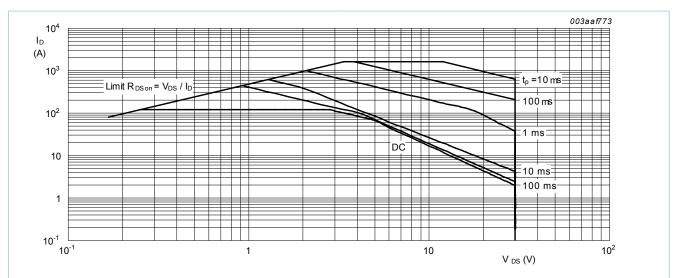


Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

 T_{mb} = 25 °C; I_{DM} is a single pulse; Capped at 120 A due to package

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	Fig. 4	-	0.22	0.49	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	minimum footprint; mounted on a printed-circuit board	-	50	-	K/W

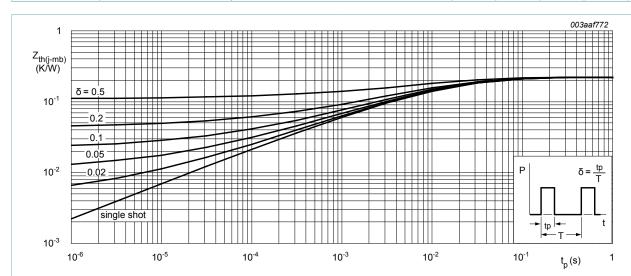


Fig. 4. Transient thermal impedance from junction to mounting base as a function of pulse duration; typical values

PSMNR90-30BL

All information provided in this document is subject to legal disclaimers.

© Nexperia B.V. 2017. All rights reserved

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics		'			_
V _{(BR)DSS}	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	30	-	-	V
	breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	27	-	-	V
V _{GS(th)}	gate-source threshold voltage	I _D = 1 mA; V _{DS} = V _{GS} ; T _j = 25 °C; Fig. 10; Fig. 11	1.3	1.7	2.2	V
		I_D = 1 mA; V_{DS} = V_{GS} ; T_j = 175 °C; Fig. 11	0.65	-	-	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ Fig. 11	-	-	2.5	V
I _{DSS}	drain leakage current	V _{DS} = 30 V; V _{GS} = 0 V; T _j = 25 °C	-	0.02	10	μΑ
		V _{DS} = 30 V; V _{GS} = 0 V; T _j = 175 °C	-	-	500	μΑ
I _{GSS}	gate leakage current	V _{GS} = 16 V; V _{DS} = 0 V; T _j = 25 °C	-	10	100	nA
		V _{GS} = -16 V; V _{DS} = 0 V; T _j = 25 °C	-	10	100	nA
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I_D = 25 A; T_j = 25 °C; Fig. 12	-	0.89	1	mΩ
		V_{GS} = 4.5 V; I_D = 25 A; T_j = 25 °C; Fig. 12	-	1.1	1.4	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 175 °C; Fig. 13; Fig. 12	-	1.65	2	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 100 °C; Fig. 13; Fig. 12	-	1.19	1.5	mΩ
R _G	gate resistance	f = 1 MHz	-	1.1	-	Ω
Dynamic o	characteristics		'			
Q _{G(tot)}	total gate charge	I _D = 75 A; V _{DS} = 15 V; V _{GS} = 10 V; Fig. 14; Fig. 15	-	243	-	nC
		I _D = 0 A; V _{DS} = 0 V; V _{GS} = 10 V	-	222	-	nC
		I _D = 75 A; V _{DS} = 15 V; V _{GS} = 4.5 V;	-	118	-	nC
Q_{GS}	gate-source charge	Fig. 14; Fig. 15	-	39	-	nC
Q _{GS(th)}	pre-threshold gate- source charge		-	22	-	nC
Q _{GS(th-pl)}	post-threshold gate- source charge		-	17	-	nC
Q_{GD}	gate-drain charge		-	37	-	nC
V _{GS(pl)}	gate-source plateau voltage	I _D = 75 A; V _{DS} = 15 V; <u>Fig. 14</u> ; <u>Fig. 15</u>	-	2.8	-	V

PSMNR90-30BL

All information provided in this document is subject to legal disclaimers.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
C _{iss}	input capacitance	V _{DS} = 15 V; V _{GS} = 0 V; f = 1 MHz;	-	14850	-	pF
C _{oss}	output capacitance	T _j = 25 °C; <u>Fig. 16</u>	-	2799	-	pF
C _{rss}	reverse transfer capacitance		-	1215	-	pF
$t_{d(on)}$	turn-on delay time	V_{DS} = 15 V; R_L = 0.2 Ω ; V_{GS} = 5 V; $R_{G(ext)}$ = 5 Ω ; I_D = 75 A; T_j = 25 °C	-	95	-	ns
t _r	rise time	$V_{DS} = 15 \text{ V}; R_L = 0.2 \Omega; V_{GS} = 5 \text{ V};$ $R_{G(ext)} = 5 \Omega; T_j = 25 \text{ °C}; I_D = 75 \text{ A}$	-	213	-	ns
t _{d(off)}	turn-off delay time	V_{DS} = 15 V; R_L = 0.2 Ω ; V_{GS} = 5 V;	-	199	-	ns
t _f	fall time	$R_{G(ext)} = 5 \Omega$; $I_D = 75 A$; $T_j = 25 °C$	-	115	-	ns
Source-dra	ain diode					
V _{SD}	source-drain voltage	$I_S = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}; Fig. 17$	-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_S = 25 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V};$	-	67	-	ns
Q _r	recovered charge	V _{DS} = 15 V	-	123	-	nC

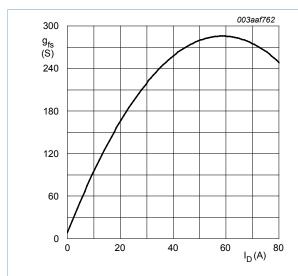


Fig. 5. Forward transconductance as a function of drain current; typical values

$$T_j = 25\,^{\circ}C; V_{DS} = 15\,V$$

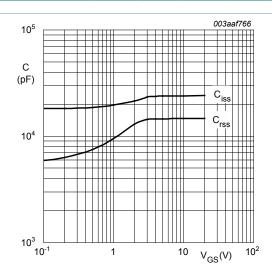


Fig. 6. Input and reverse transfer capacitances as a function of gate-source voltage; typical values

$$V_{DS} = 0V; f = 1MHz$$

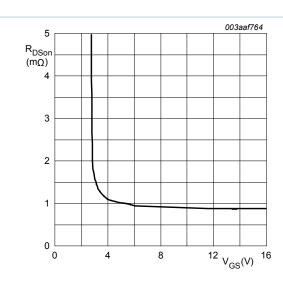


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

$$T_j = 25 \,^{\circ}C; I_D = 25 A$$

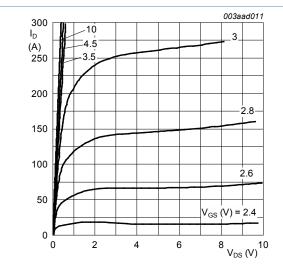


Fig. 9. Output characteristics: drain current as a function of drain-source voltage; typical values

$$T_j = 25 \,^{\circ}C$$

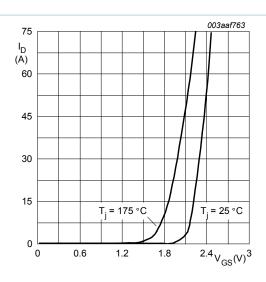


Fig. 8. Transfer characteristics: drain current as a function of gate-source voltage; typical values

$$V_{DS} > I_D \times R_{DSon}$$

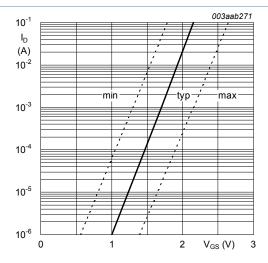


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$$T_j=25\,^{\circ}C; V_{DS}=5\,V$$

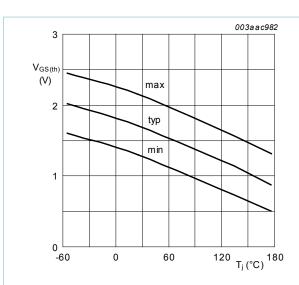
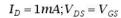


Fig. 11. Gate-source threshold voltage as a function of junction temperature



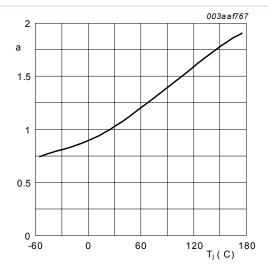


Fig. 13. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

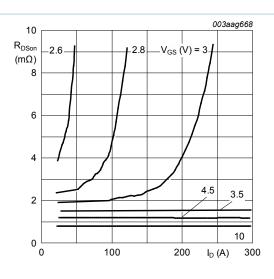


Fig. 12. Drain-source on-state resistance as a function of drain current; typical values

$$T_j = 25 \,^{\circ}C$$

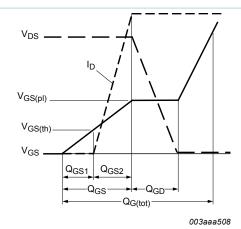


Fig. 14. Gate charge waveform definitions

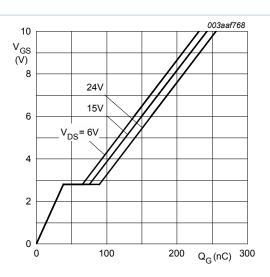


Fig. 15. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25$$
 °C; $I_D = 75$ A

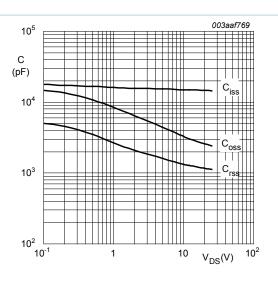


Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = 0V; f = 1MHz$$

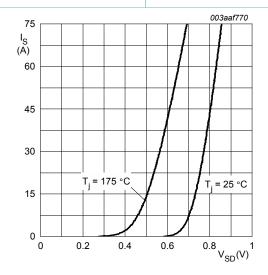
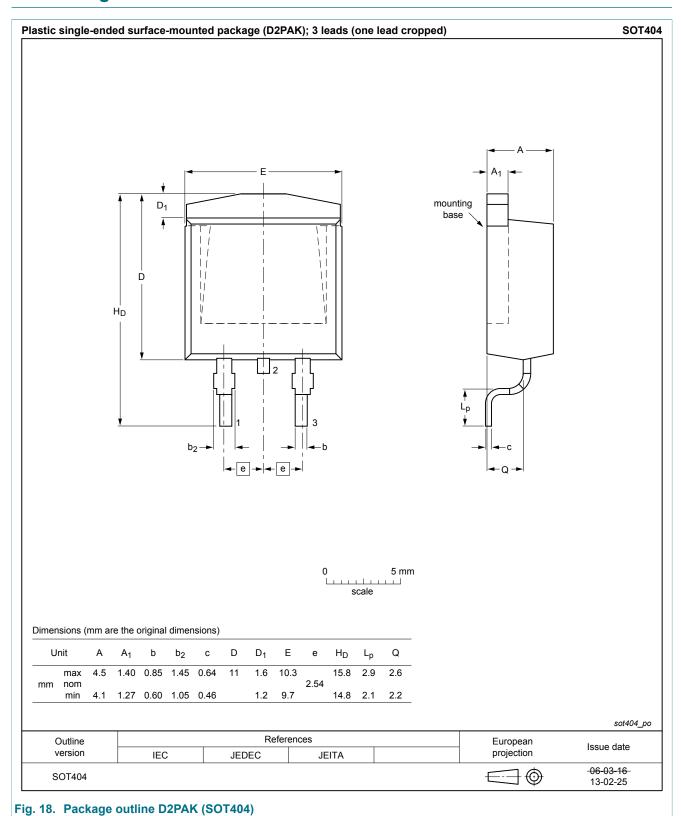


Fig. 17. Source current as a function of source-drain voltage; typical values

$$V_{GS} = 0 V$$

N-channel 30 V 1.0 $m\Omega$ logic level MOSFET in D2PAK

11. Package outline



PSMNR90-30BL

All information provided in this document is subject to legal disclaimers.

N-channel 30 V 1.0 mΩ logic level MOSFET in D2PAK

12. Legal information

12.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

12.2 Definitions

Preview — The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

12.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the

PSMNR90-30BL

All information provided in this document is subject to legal disclaimers.

© Nexperia B.V. 2017. All rights reserved

N-channel 30 V 1.0 m Ω logic level MOSFET in D2PAK

grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of nonautomotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

N-channel 30 V 1.0 m Ω logic level MOSFET in D2PAK

13. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	2
9	Thermal characteristics	4
10	Characteristics	5
11	Package outline	10
12	Legal information	11
12.1	Data sheet status	11
12.2	Definitions	11
12.3	Disclaimers	11
12.4	Trademarks	12

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 02 April 2014

[©] Nexperia B.V. 2017. All rights reserved