Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

Contact us

PT7M1818/1813

Supervisory Circuit

Features

- Highly accurate: ±1.5% (25°C)

- Accurate power monitoring: 2.5V, 2.9V, 3.0V (PT7M1818), and 4.1, 4.3, 4.6V (PT7M1813)

- Operating voltage range: 1.0V ~ 5.5V

- Operating temperature range: -40°C to + 85°C

- Detect voltage temperature characteristics: ±2.5% × TYP

- Efficient open-drain output with internal 5kΩ pull-up resistor

- Maintains reset for 200ms after V_{CC} returns to an in-tolerance condition
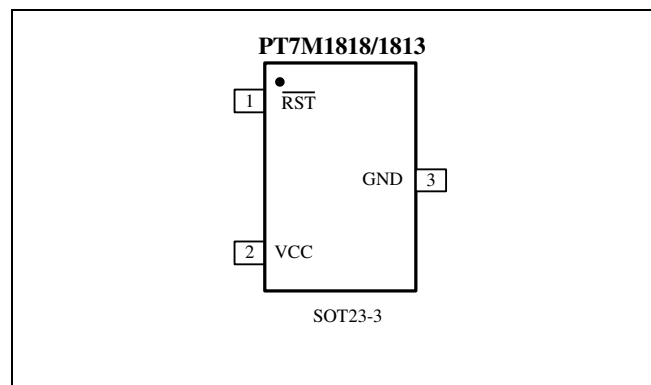
Description

The series are designed to monitor power supplies in µP and digital systems. It provides excellent circuit reliability and low cost by eliminating external components and adjustments.

This device performs a single function: it asserts a reset signal whenever the V_{CC} supply voltage falls below a preset threshold. Reset remains asserted for 200ms after V_{CC} has risen above the reset threshold.

PT7M1818/1813 are bidirectional output, allowing it to be directly connected to µP with bidirectional reset inputs.

Pin Configuration

PT7M1818/1813

| 1 | $\overline{RST}$ |

GND | 3 |

| 2 | VCC |

SOT23-3

Pin Description

Name	Type	Description
$\overline{RST}$	I/O	**Reset Output and Pushbutton Input:** $\overline{RST}$ is asserted when V_{CC} drops below voltage threshold $V_{TH\text{-}}$. Active low. When other devices pull $\overline{RST}$ low, the reset condition occurs and will remain a reset timeout period after the external signal is off.
GND	P	**Ground**
V_{CC}	P	**Supply Voltage.**

Maximum Ratings

Storage Temperature ....-65ºC to +150ºC
Ambient Temperature with Power Applied......................... -40ºC to +85ºC
Supply Voltage to Ground Potential (Vcc to GND) ....-0.3V to +7.0V
DC Input Voltage (All inputs except Vcc and GND)......-0.3V to V_{CC}+0.3V
DC Output Current (All outputs) 30mA
Power Dissipation .... 320mW (Depend on package)

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics

(T_A= -40~85ºC, unless otherwise noted. Typical values are at T_A = +25ºC)

Description		Sym.	Test Conditions	Min	Typ	Max	Unit
Supply Voltage		V_{CC}		1.0	-	5.5	V
Supply Current		I_{CC}	V_{CC} = 5.5V. No load.	-	-	12	µA
			V_{CC} = 3.6V. No load.	-	-	10	µA
Voltage Threshold		V_{TH-}	+25°C	(V_{TH-}) ×0.985	V_{TH-}	(V_{TH-}) ×1.015	V
			-40°C~85°C	(V_{TH-}) ×0.975	V_{TH-}	(V_{TH-}) ×1.025	V
Hysteresis		V_{HYS}	V_{TH+} - V_{TH-}*	-	50	-	mV
Output Driving	Output low	V_{OL}	I_{OH} = 8mA, V_{CC} = 5V	-	-	0.4	V
			I_{OH} = 4mA, V_{CC} = 3V	-	-	0.3	
			I_{OH} = -50µA, V_{CC} = 1V	-	-	0.09	
Internal pull-up resistor		Rp	$\overline{RST}$	3.75	5	6.25	kΩ

Note: V_{TH-} is voltage threshold when V_{CC} falls from high to low. V_{TH+} is voltage threshold when V_{CC} rises from low to high.

AC Electrical Characteristics
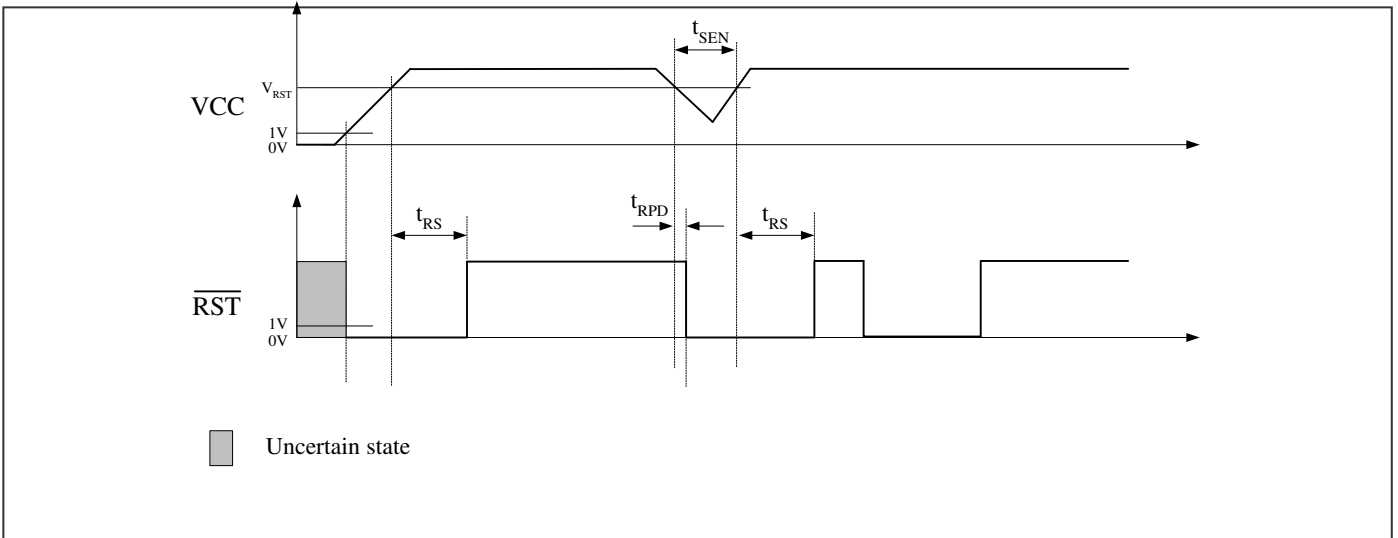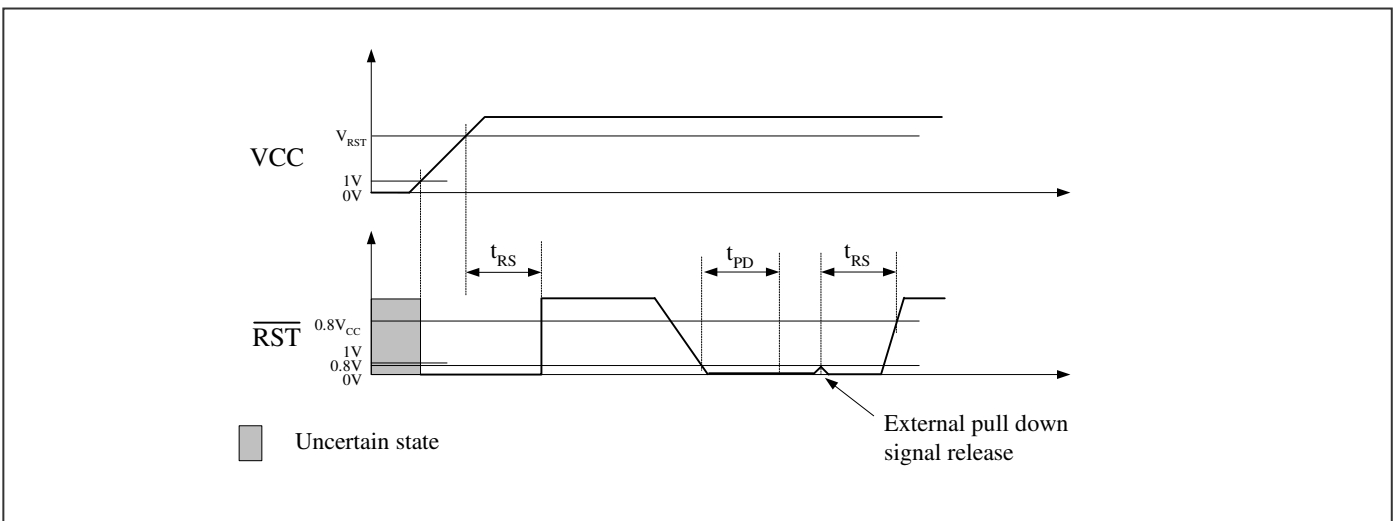
Fig 1. Timing diagram


Fig 2. Timing diagram

(V_{CC} = 1.0V to 5.5V, T_A = -40~85ºC, unless otherwise noted. Typical values are at T_A = +25ºC)

Sym.	Description	Test Conditions	Min	Typ	Max	Unit
t_{RS}	Reset Timeout Period	-	140	200	280	ms
t_{RPD}	Delay	-	-	17	-	μs
t_{SEN}	Sensitivity	-	20	-	-	μs
t_{PD}	External Pull Down Signal Pulse	$\overline{RST}$ pin	1	-	-	μs
	Pushbutton Detect[*]	$\overline{RST}$ pin	0.8	1.5	2.0	V

*Note: $\overline{RST}$ will be asserted when it is pull down to the typical value or less.

Block Diagram
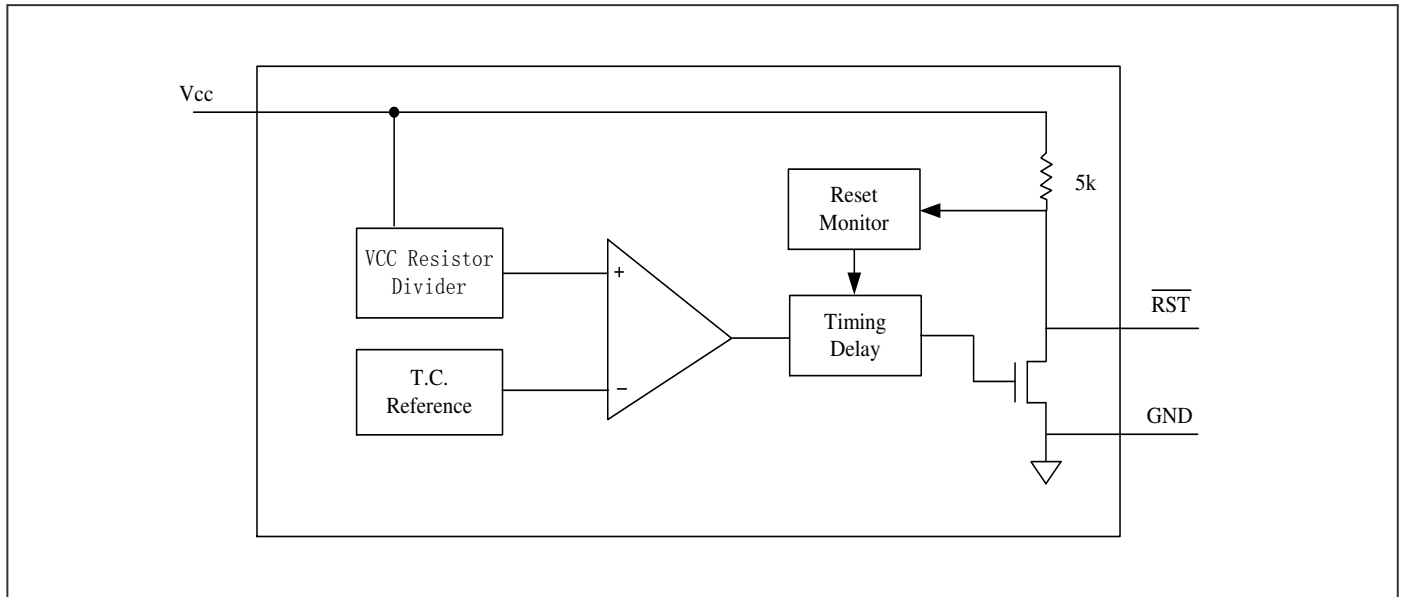
Function Description

Power Monitor

A microprocessor's (µP's) reset input starts the µP in a known state. Whenever the µP is in an unknown state, it should be held in reset. The supervisory circuits assert reset during power-up and prevent code execution errors during power-down or brownout conditions.

On power-up, once Vcc reaches about 1.0V, $\overline{RST}$ is a guaranteed logic low of 0.4V or less. As Vcc rises, $\overline{RST}$ stays low. When Vcc rises above the reset threshold V_{RST}, an internal timer releases $\overline{RST}$ after about 200ms. $\overline{RST}$ asserts whenever Vcc drops below the reset threshold, i.e. brownout condition. If brownout occurs in the middle of a previously initiated reset pulse, the pulse continues for at least another 200ms. On power-down, once Vcc falls below the reset threshold, $\overline{RST}$ stays low and is guaranteed to be 0.4V or less until Vcc drops below 1V.

Reset Output: Bi-direction

The devices provide $\overline{RST}$ output pin for a pushbutton switch. When the devices are not in a reset cycle, it continuously monitors the $\overline{RST}$ signal for a low going edge. If an edge is detected, the devices will debounce the switch by pulling the $\overline{RST}$ line low. After the internal timer has expired, the devices will continue to monitor the $\overline{RST}$ line. If the line is still low, they will continue to monitor the line looking for a rising edge. Upon detecting a release, they will force the $\overline{RST}$ line low and hold it low for 200ms.

Application Information

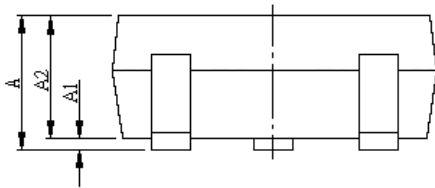**Typical Operation Circuit**
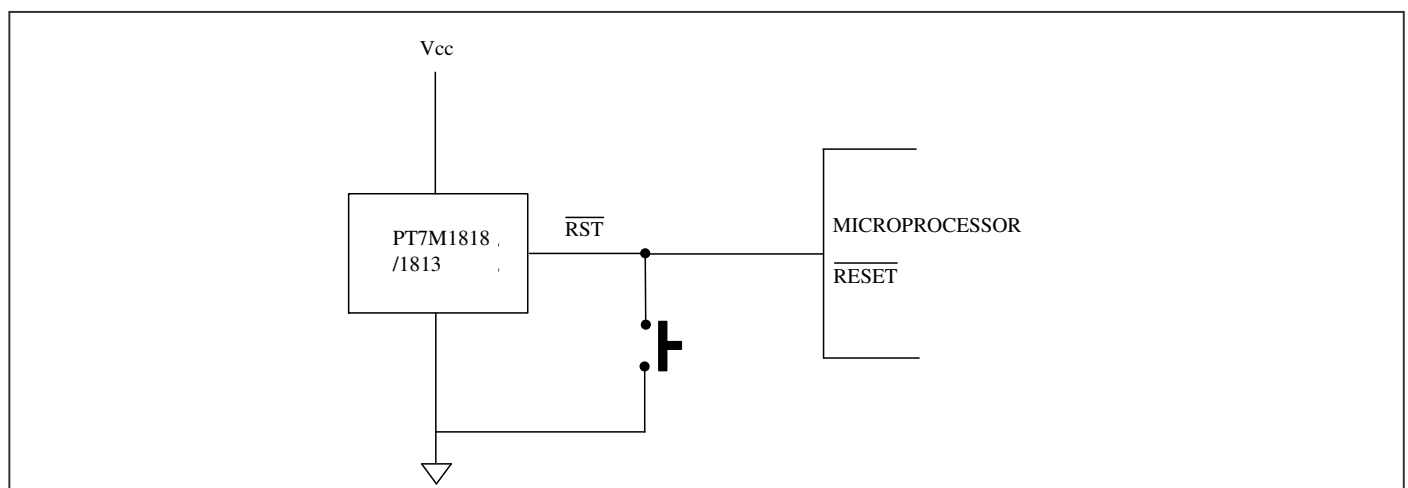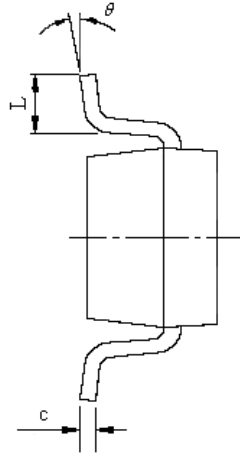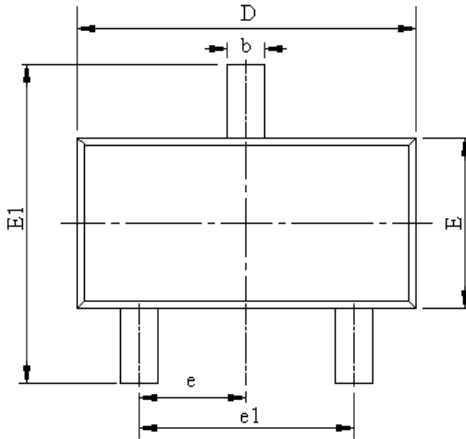
Mechanical Information

SOT23-3

	DOCUMENT CONTROL NO. PD-0018
	REVISION: A
	DATE: 5/17/2011

PKG.	DIMENSIONS(MM)	
SYMBOL	MIN	MAX
A	1.05	1.25
A1	0.00	0.10
A2	1.05	1.15
b	0.30	0.50
c	0.10	0.20
D	2.82	3.02
E	1.50	1.70
E1	2.65	2.95
e	0.95BSC	
e1	1.80	2.00
L	0.30	0.60
θ	0°	8°

Note:
1) Controlling dimensions in millimeters.

Pericom Technology (Shanghai) Co.Ltd.
3/F., 20 Building ,481 Gui Ping Road,
Shanghai,200233
Tel:(86-21)6485-0576. Fax: (86-21)6485-2181

DESCRIPTION: 3-Pin Small Outline Transistor, Plastic Package, SOT-23

PACKAGE CODE: TA3

Ordering Information

Part Number	Package Code	Package
PT7M1818-20TE	T	Lead free and Green SOT23-3
PT7M1818-10TE	T	Lead free and Green SOT23-3
PT7M1818-5TE	T	Lead free and Green SOT23-3
PT7M1813-15TE	T	Lead free and Green SOT23-3
PT7M1813-10TE	T	Lead free and Green SOT23-3
PT7M1813-5TE	T	Lead free and Green SOT23-3

Note:
- E = Pb-free or Pb-free and Green
- Adding X Suffix= Tape/Reel
- Contact Pericom for availability.
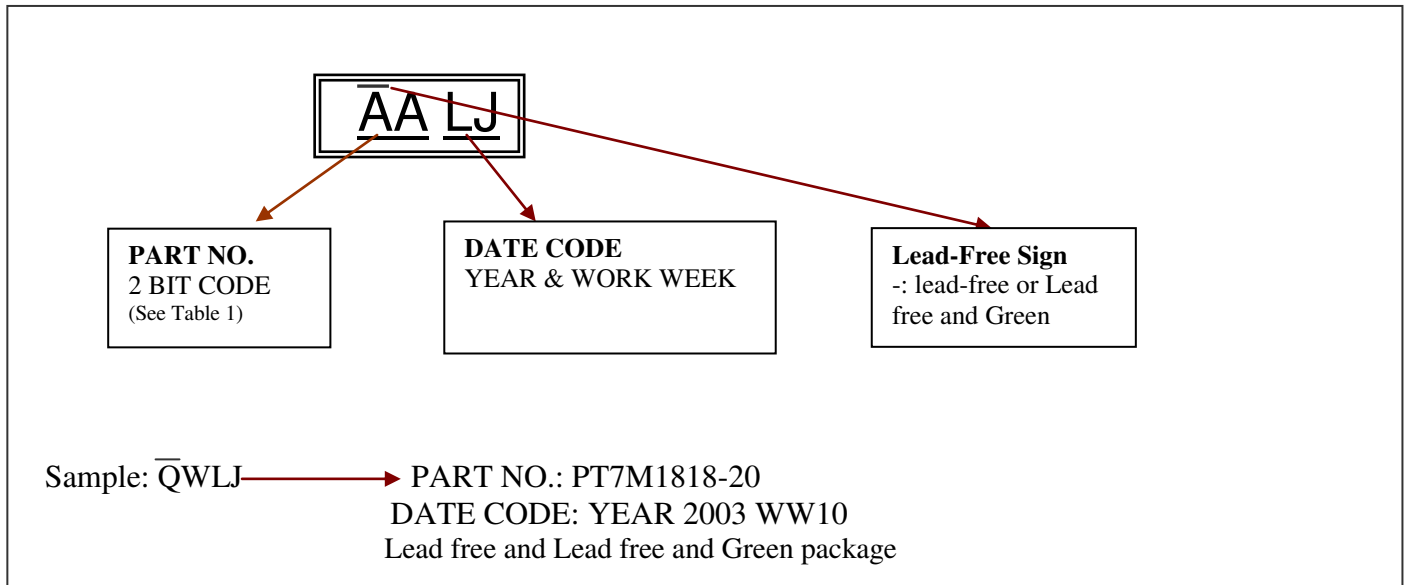
Small Package Top Marking Instruction


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 ___
|AA LJ|
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PART NO.
2 BIT CODE
(See Table 1)

DATE CODE
YEAR & WORK WEEK

Lead-Free Sign
-: lead-free or Lead
free and Green

Sample: $\overline{Q}$WLJ ⟶ PART NO.: PT7M1818-20
DATE CODE: YEAR 2003 WW10
Lead free and Lead free and Green package

Table 1. Function comparison

| Item | Part No. | Reset Output | | | | | V_{TH-} (V) |
| | | Open-Drain | | Push-Pull | | Bi-direct | |
		Active high	Active low	Active high	Active low	Active low	
1	PT7M1818-20	-	-	-	-	√	2.5
2	PT7M1818-10	-	-	-	-	√	2.9
3	PT7M1818-5	-	-	-	-	√	3.0
4	PT7M1813-15	-	-	-	-	√	4.1
5	PT7M1813-10	-	-	-	-	√	4.3
6	PT7M1813-5	-	-	-	-	√	4.6

Table 2 Marking code for products

No.	Part No.	Code
1	PT7M1818-20	QW
2	PT7M1818-10	RA
3	PT7M1818-5	RB
4	PT7M1813-15	RL
5	PT7M1813-10	RO
6	PT7M1813-5	RR

2014-08-0010   PT0190-7   09/11/14